

KENWOOD

SERVICE MANUAL

UHF DIGITAL TRANSCEIVER

NX-840H, NX-840H(U), NX-840

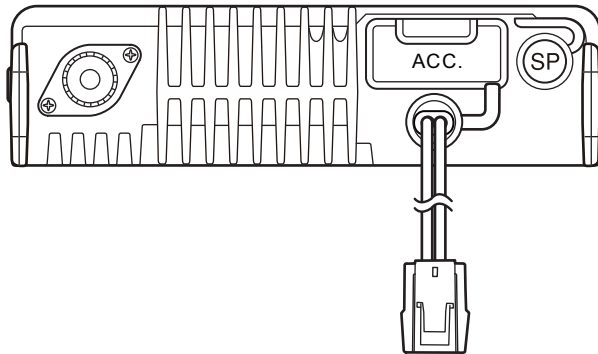
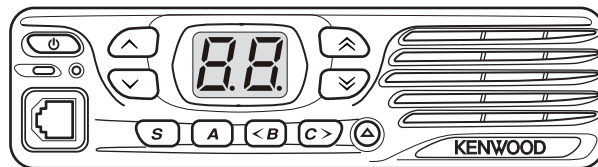


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This service manual is applied for Hardware Ver 2.0 transceivers (K, K2, M, M2 types) with B56xxxxx or subsequent serial numbers.

This product complies with the RoHS directive for the European market.



This product uses Lead Free solder.

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Transceivers containing AMBE+2™ Vocoder:

The AMBE+2™ voice coding technology is embedded in the firmware under the license of Digital Voice Systems, Inc.

SPECIFICATION

GENERAL		
Frequency Range		450 ~ 520MHz (K, M), 400~470MHz (K2, M2)
Number of Channels		Max. 32ch
Number of Zones		2 zones (Max. 16ch per Zone)
Channel Spacing	Analog	12.5/25kHz
	Digital	6.25/12.5kHz
Operating Voltage		13.6V DC \pm 15%
Operating Temperature Range		-22°F ~ +140°F (-30°C ~ +60°C)
Frequency Stability		\pm 1.0ppm
Antenna Impedance		50 Ω
Dimensions (W x H x D) (Projections not included)		160 x 43 x 122.6 mm (6.29 x 1.69 x 4.82 in)
Weight (net)	Radio only	1.10 kg (2.42 lb)
RECEIVER		
Sensitivity	Digital@6.25KHz (3% BER)	0.28 μ V
	Digital@12.5KHz (3% BER)	0.28 μ V
	Analog (12dB SINAD)	0.28 μ V
Selectivity	Analog@25kHz	75dB
	Analog@12.5kHz	65dB
Intermodulation Distortion	Analog	65dB
Spurious Response	Analog	75dB
Audio Output (4 Ω impedance)		4W with less than 5% distortion
TRANSMITTER		
RF Power Output		5W ~ 45W (NX-840H) 5W ~ 25W (NX-840)
Spurious Response		70dB
FM Hum & Noise	Analog@25kHz	45dB
	Analog@12.5kHz	40dB
Audio Distortion		Less than 5%
Modulation		16K0F3E, 11K0F3E, 4K00F1E, 4K00F1D, 4K00F7W, 4K00F2D, 8K30F1E, 8K30F1D, 8K30F7W

Analog measurements made per TIA/EIA-603 and specifications shown are typical.

Digital measurements made per NXDN CA1 and specifications shown are typical.

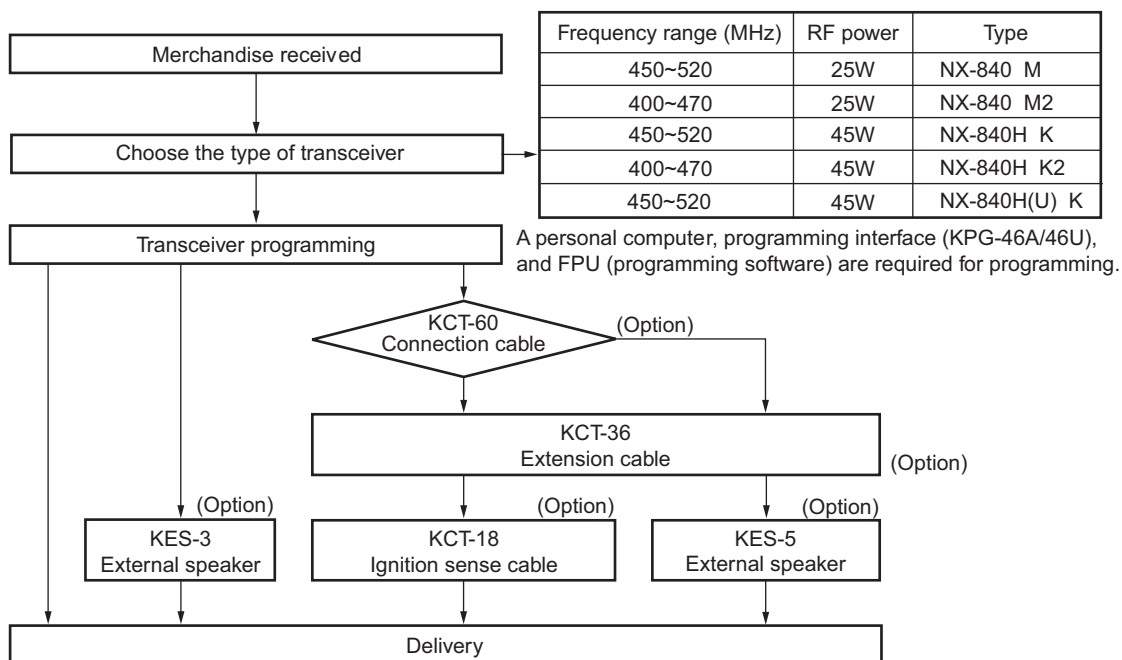
JVC KENWOOD Corporation reserves the right to change specifications without prior notice or obligation.

SECTION 1 PRECAUTION

This service manual does not describe PRECAUTION.

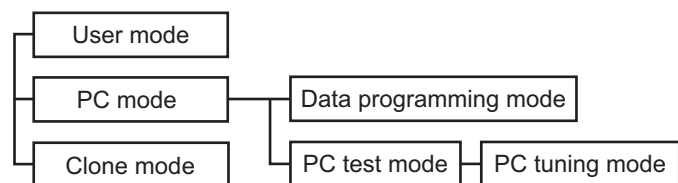
SECTION 2 SPECIFIC SERVICE INSTRUCTIONS

2.1 SYSTEM SET-UP



2.2 REALIGNMENT

2.2.1 Modes



2.2.2 How to Enter Each Mode

Mode	Function
User mode	Power ON
PC mode	Received commands from PC
Clone mode	[↵] + Power ON (Two seconds)

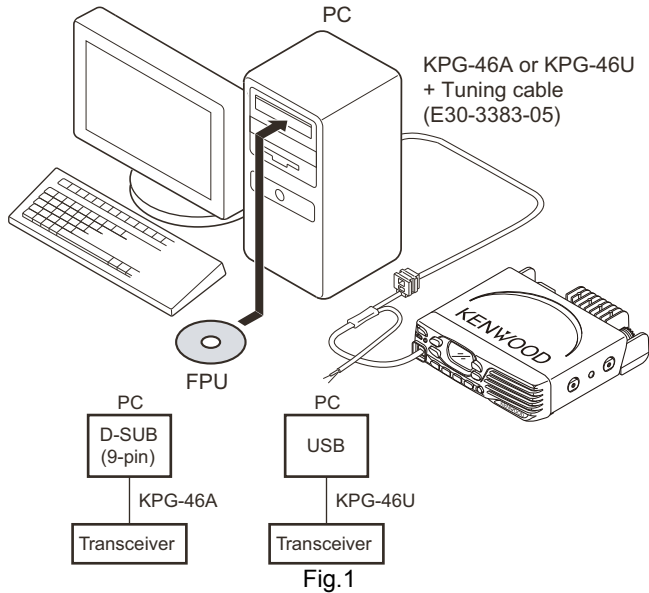
Mode	Function
User mode	For normal use.
PC mode	Used for communication between the transceiver and PC (IBM compatible).
PC programming mode	Used to read and write frequency data and other features to and from the transceiver.
PC test mode	Used to check the transceiver using the PC. This feature is included in the FPU.
PC tuning mode	Used to tune the transceiver using the PC.
Clone mode	Used to transfer programming data from one transceiver to another.

2.2.3 PC Mode

2.2.3.1 Preface

The transceiver is programmed using a personal computer, a programming interface (KPG-46A/46U) and FPU (programming software).

The programming software can be used with a PC. Figure 1 shows the setup of a PC for programming.



2.2.3.2 Connection procedure

- (1) Connect the transceiver to the computer using the interface cable.

Note:

You must install the KPG-46U driver in the computer to use the USB programming interface cable (KPG-46U).

- (2) When the Power is switched on, you can immediately enter user mode. When the PC sends a command, the transceiver enters PC mode, and "Pc" is displayed on the LED. When data is transmitting from the transceiver, the red LED blinks. When data is receiving by the transceiver, the green LED blinks.

Note:

The data stored in the computer must match the "Model Name" when it is written into the EEPROM.

2.2.3.3 KPG-46A description

(PC programming interface cable: Option)

The KPG-46A is required to interface the transceiver to the computer. It has a circuit in its D-sub connector (KPG-46A: 9-pin) case that converts the RS-232C logic level to the TTL level.

The KPG-46A connects the 8-pin microphone connector of the transceiver to the RS-232C serial port of the computer.

2.2.3.4 KPG-46U description

(USB programming interface cable: Option)

The KPG-46U is a cable which connects to a USB port on a computer.

When using the KPG-46U, install the supplied CD (with driver software) in the computer. The KPG-46U driver runs under Windows XP, Vista, 7, 8 or 8.1.

The latest version of the USB driver is available for download from the following URL:

<http://www.kenwood.com/usb-com/>

(This URL may change without notice.)

2.2.3.5 Programming Software: KPG-175D description

The FPU is the programming software for the transceiver supplied on a CD. This software runs under Windows XP, Vista, 7, 8 or 8.1 on a PC.

The data can be input to or read from the transceiver and edited on the screen. The programmed or edited data can be printed out. It is also possible to tune the transceiver.

2.2.4 Clone Mode

Programming data can be transferred from one transceiver to another by connecting them via their cloning cable. The operation is as follows (the transmit transceiver is the source and the receive transceiver is a target).

Note:

Clone mode should be enabled.

- (1) Turn the source transceiver power ON with the [M] key held down (2 seconds), "cL" is displayed on the LED.
- (2) Power on the target transceiver.
- (3) Connect the cloning cable (No. E30-3382-05) to the modular microphone jacks on the source and target.
- (4) Press the [S] key on the source transceiver.

The data of the source is sent to the target. While the source is sending data, red LED blinks.

While the target is receiving the data, "Pc" is displayed and green LED blinks. When cloning of data is completed, the source displays "En", and the source red LED turned off, and the target automatically operates in the User mode. The target can then be operated by the same program as the source.

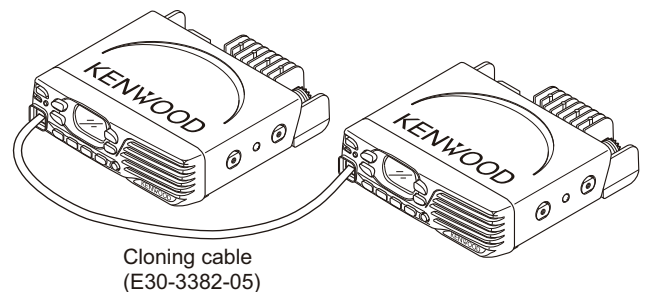
- (5) The other target can be continuously cloned. Carry out the operation in step 2 to 4.

2.2.4.1 Adding the data password.

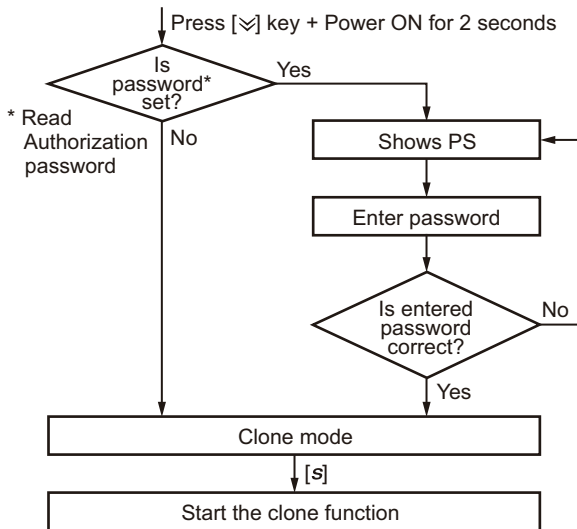
If the data password is set in the optional feature menu, you must enter the password (Source transceiver) to activate a clone mode.

You can use 0~9 to configure the password. The maximum length of the password is 6 digits.

- (1) [M] + Power ON.
- (2) "PS" is displayed on the LED.
- (3) If the [A] and [M] keys is pressed while "PS" is displayed, numbers (0 to 9) are displayed flashing. When you press the [C>] key, the currently selected number is determined. If you press the [S] key after entering the password in this procedure, "cL" is displayed if the entered password is correct. If the password is incorrect, "PS" is redisplayed.



2.2.4.2 Flow chart (Source transceiver)



2.3 INSTALLATION

2.3.1 Connection Cable (KCT-60: Option)

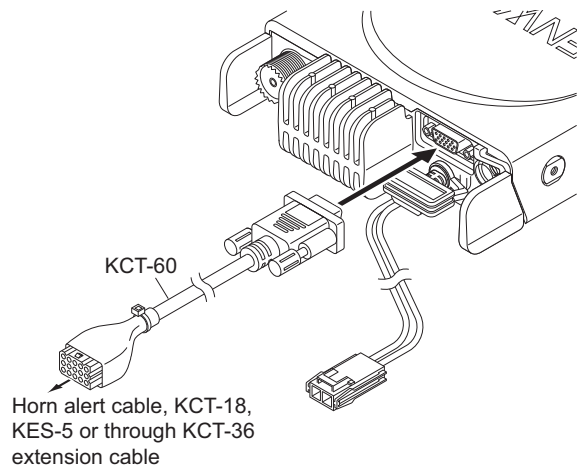
The KCT-60 connection cable kit is used to connect the transceiver to a Horn alert cable, KCT-18 (Ignition sense cable), KES-5 (External speaker), or through the KCT-36 extension cable.

2.3.1.1 Installing the KCT-60 (Connection cable) in the transceiver

- (1) Remove the ACC. cap on the rear of the transceiver.
- (2) Connect the D-sub connector of the KCT-60 to the D-sub 15-pin terminal of the transceiver.
- (3) Connect the 15-pin connector of the KCT-60 to a Horn alert cable, KCT-18, KES-5, or through a KCT-36 extension cable.

Note:

You must set up using the KPG-175D.



2.3.1.2 Terminal function

D-sub 15-pin Pin No.	Name	Molex 15-pin Pin No.
1	SB	1
2	IGN	2
3	PA or External SP	12
4	DETO	4
5	DATAI	5

D-sub 15-pin Pin No.	Name	Molex 15-pin Pin No.
6	FNC1	9
7	FNC2	11
8	FNC3	7
9	FNC4	6
10	FNC5	8
11	FNC6	10
12	5MS	NC
13	HR1	13
14	HR2	14
15	GND	3

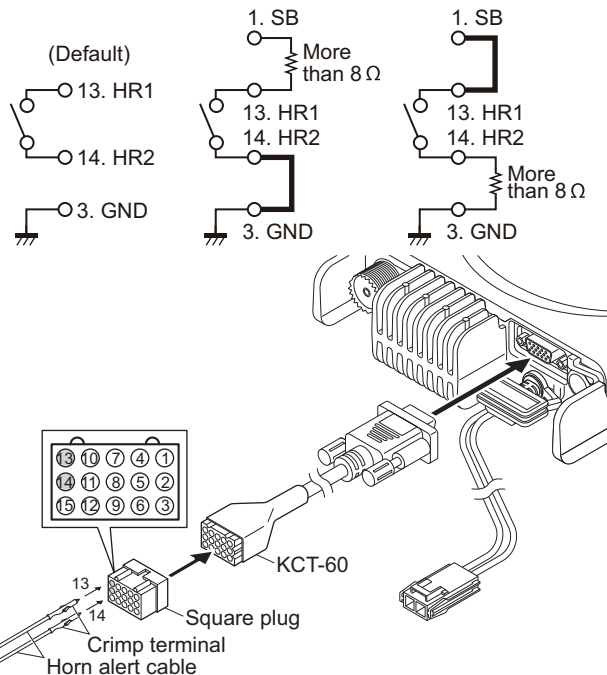
2.3.2 Horn Alert Function

The Horn alert function (max. 2A drive) is enabled by installing the KCT-60 in the transceiver.

2.3.2.1 Installation Procedure

- (1) Remove the ACC. cap on the rear of the transceiver.
- (2) Connect the D-sub connector of the KCT-60 to the D-sub 15-pin terminal of the transceiver.
- (3) Insert the two crimp terminals of the Horn alert cable to pins 13 and 14 of the square plug.
- (4) Connect the square plug to the 15-pin connector of the KCT-60.
- (5) Connect the remaining two Horn alert cables to your car Horn alert signal control.

The internal FET switch can be controlled by turning the HA function on/off and by using a signaling decode output. The maximum current of HA is 2A. This FET switch is the open drain circuit. Therefore, a DC power supply is necessary to use the HR1. The voltage range is from 5V to 16V.



2.3.3 Ignition Sense Cable (KCT-18: Option)

The KCT-18 is an optional cable for enabling the ignition function. The ignition function lets you turn the transceiver power on and off with the car ignition key.

2.3.3.1 Installing the KCT-18 (Ignition sense cable) in the transceiver

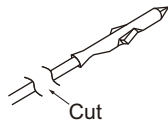
- (1) The KCT-18 can be installed in the transceiver by the following two methods (Method A, Method B).

Method A: The KCT-18 is soldered to the "IGN" pad on the TX-RX unit.

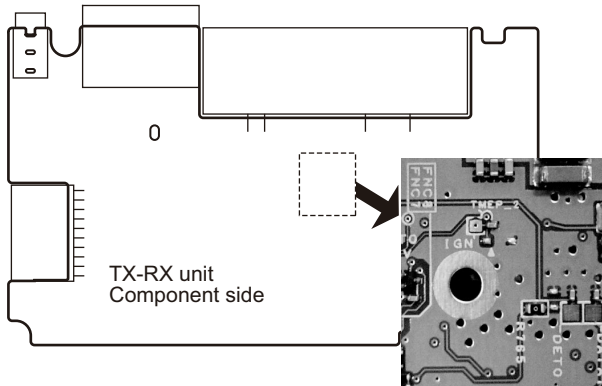
Method B: The KCT-18 is connected to the 15-pin connector of the KCT-60 connected to the transceiver.

■Installation Procedure: Method A

- (1) Remove the two screws on both the right and left sides of the transceiver, then remove the cabinet and top packing from the transceiver.
- (2) Cut the crimp terminal side of the KCT-18 using a pair of nippers or similar tool.

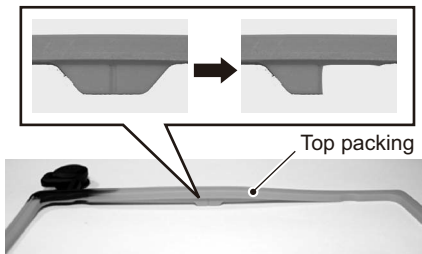


- (3) Solder the cable side cut in the above step 3 to the "IGN" pad on the TX-RX unit.



- (4) Dress the KCT-18 cable as shown in the figure. The KCT-18 cable needs to pass through one of two indentations located on the rear panel of the transceiver.
- (5) Cut off the projection of the top packing using a pair of nippers or similar tool.

If the KCT-18 cable is dressed to be routed through the indentations on the right side in step 5, the right side of the projection needs to be cut off. If the KCT-18 cable is dressed to be routed through the indentations on the left side, the left side of the projection needs to be cut off. Following is a figure presenting an example for when the right side of the projection is cut off.



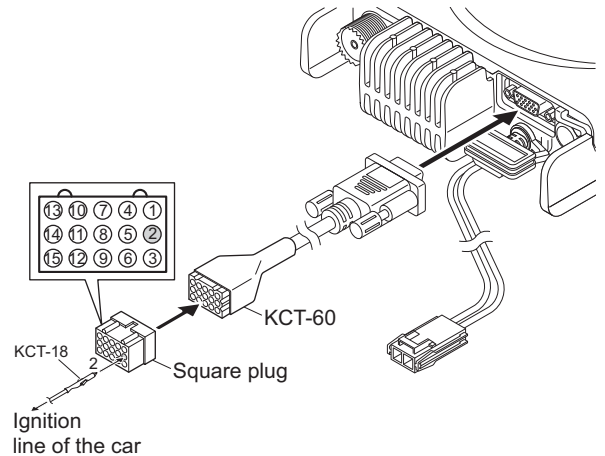
- (6) Reinstall the top packing. Check the correct fitting of the top packing, then reinstall the cabinet and two screws for the right and left sides.
- (7) Connect the other side of the KCT-18 to the ignition line of the car.

■Installation Procedure: Method B

- (1) Remove the ACC. cap on the rear of the transceiver.
- (2) Connect the D-sub connector of the KCT-60 to the D-sub 15-pin terminal of the transceiver.
- (3) Insert the crimp terminal side of the KCT-18 to pin 2 of the square plug.
- (4) Connect the square plug to the 15-pin connector of the KCT-60.
- (5) Connect the other side of the KCT-18 to the ignition line of the car.

Note:

You must set up using the KPG-175D.



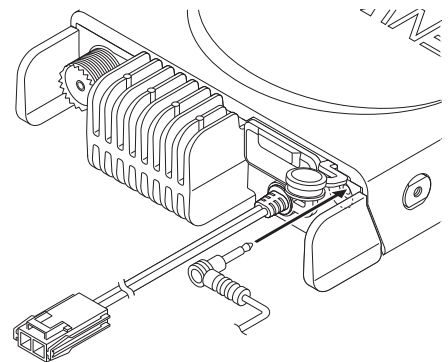
2.3.4 External Speaker (Option)

2.3.4.1 KES-3

The KES-3 is an external speaker for the 3.5-mm-diameter speaker jack.

■Connection procedure

- (1) Remove the speaker-jack cap on the rear of the transceiver.
- (2) Connect the KES-3 to the 3.5-mm-diameter speaker jack on the rear of the transceiver.



2.3.4.2 KES-5

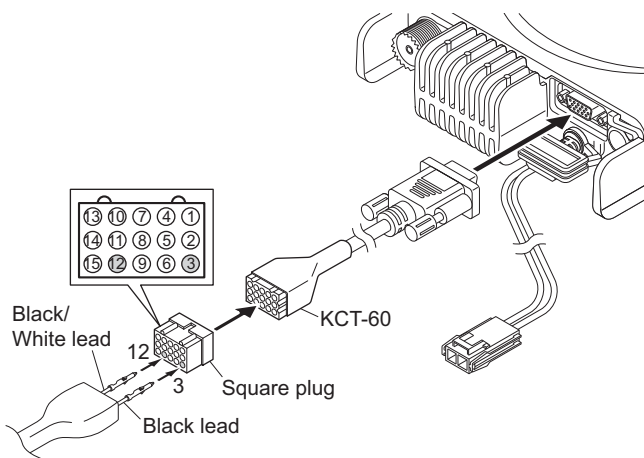
External speaker KES-5 can be installed for KCT-60.

■Connection procedure

- (1) Remove the ACC. cap on the rear of the transceiver.
- (2) Connect the D-sub connector of the KCT-60 to the D-sub 15-pin terminal of the transceiver.
- (3) Insert the two crimp terminals of the KES-5 to pins 3 and 12 of the square plug.
- (4) Connect the square plug to the 15-pin connector of the KCT-60.

Note:

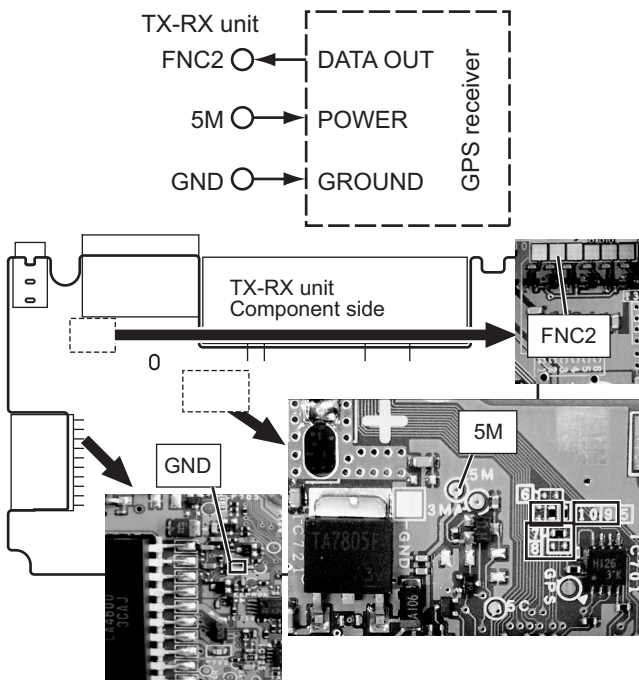
- You must set up using the KPG-175D.
Before the external speaker can be used, you must assign one of the keys as "External Speaker", using the KPG-175D.
- This also applicable to public address provide you must assign one of the keys as "Public Address", using the KPG-175D.



2.3.5 GPS Receiver Connection

2.3.5.1 Connecting the GPS receiver

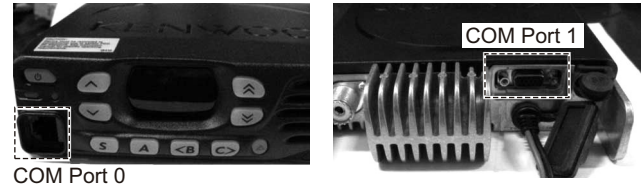
- (1) Soldering position



- (2) Refer to "2.7.3 Function Port Assignment".

2.3.6 Extended Function: COM Port 0 and COM Port 1

Location of COM Port 0 and COM Port 1 of the transceiver is shown below.



You must configure the transceiver COM Port 0 and COM Port 1 using the KPG-175D.

When you set as "Data", the Function port 1 and 2 will be automatically fixed as Input ports.

The reason for this is because function port 1 (TXD) and 2 (RXD) share the same circuit path of TXD and RXD line.

2.3.7 Changing Serial Port Level

2.3.7.1 Change FNC1 (TXD) and FNC2 (RXD) of D-SUB 15-pin connector from TTL level to RS-232C level

FNC1 (TXD /6pin) and FNC2 (RXD /7pin) of D-SUB 15-pin connector are configured at the TTL level as the default value. But you can change these serial port level to RS-232C level through the RS-232C level converter IC (IC704) by configuring the port.

■FNC1 (TXD)

Remove the R761 chip jumper and solder the clip jumper to R767.

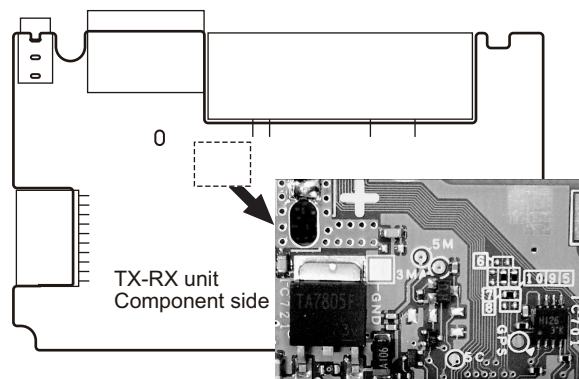
	TTL level	RS-232C level
R761[5] *1	0Ω chip jumper.	open
R767[6] *1	open	0Ω chip jumper.

■FNC2 (RXD)

Remove the R724 and R762 chip jumpers and solder the chip jumpers to R725 and R756.

	TTL level	RS-232C level
R724[7], R762[9] *1	0Ω chip jumper.	open
R725[8], R756[10] *1	open	0Ω chip jumper.

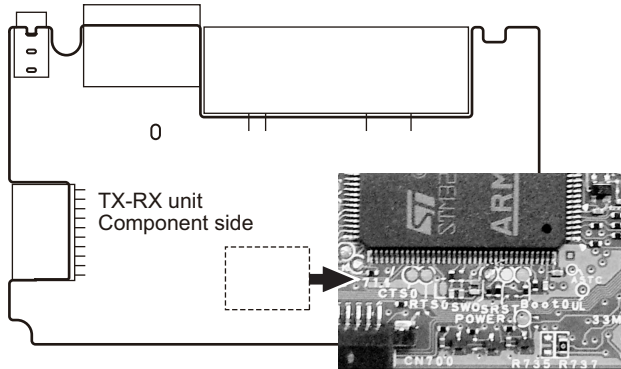
*1: The value in square bracket [] is the silk print number on the TX-RX unit.



2.3.8 Changing of Signal Type

2.3.8.1 Change signal input of D-SUB connector from DI to MIC

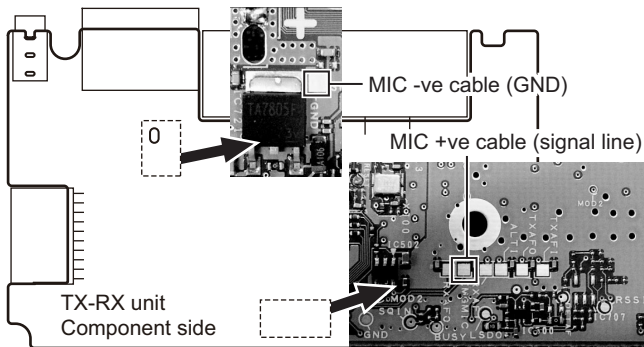
The input (5pin) of D-SUB 15-pin connector is configured at the DI as the default value.
Remove the R737 chip jumper and solder the chip jumper to R735.



2.3.9 Emergency MIC

2.3.9.1 Installation Procedure

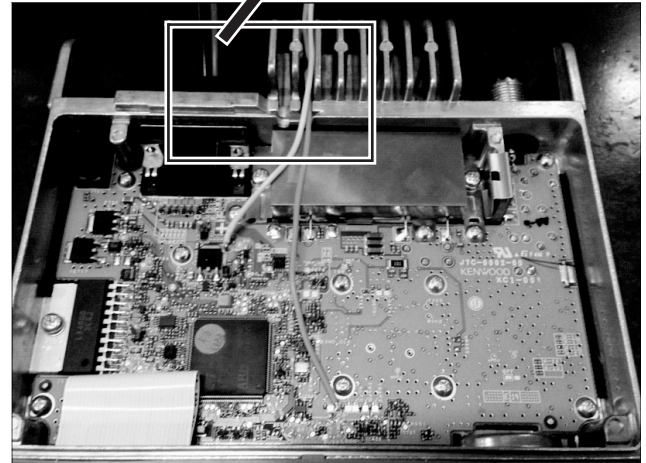
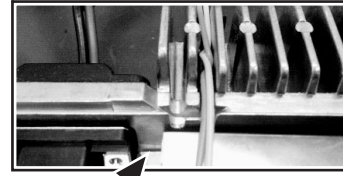
- (1) Remove the two screws on both the right and left sides of the transceiver, then remove the cabinet and top packing from the transceiver.
- (2) solder the MIC connection cable as below
 - a) solder the MIC +ve cable (signal line) to EMGMIC solder pad
 - b) solder the -ve cable (GND) to GND solder pad



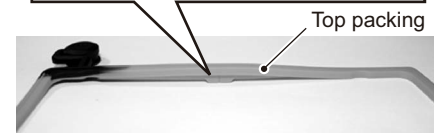
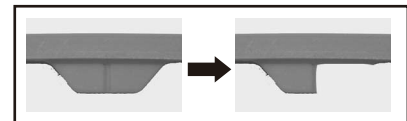
Note:

You must set up using the KPG-175D.

- (3) Dress the cable as shown in the figure. The cable needs to pass through one of two indentations located on the rear panel of the transceiver.



- (4) Cut off the projection of the top packing using a pair of nippers or similar tool.
If the cable is dressed to be routed through the indentations on the right side in step 3, the right side of the projection needs to be cut off.
If the cable is dressed to be routed through the indentations on the left side, the left side of the projection needs to be cut off.
Following is a figure presenting an example for when the right side of the projection is cut off.



- (5) Reinstall the top packing. Check the correct fitting of the top packing, then reinstall the cabinet and two screws for the right and left sides.

2.4 CIRCUIT DESCRIPTION

2.4.1 Frequency Configuration

The receiver utilizes double conversion. The first IF is 49.95MHz and the second IF is 450kHz. The first local oscillator signal is supplied from the PLL circuit.

The PLL circuit in the transmitter generates the necessary frequencies. Figure 1 shows the frequencies.

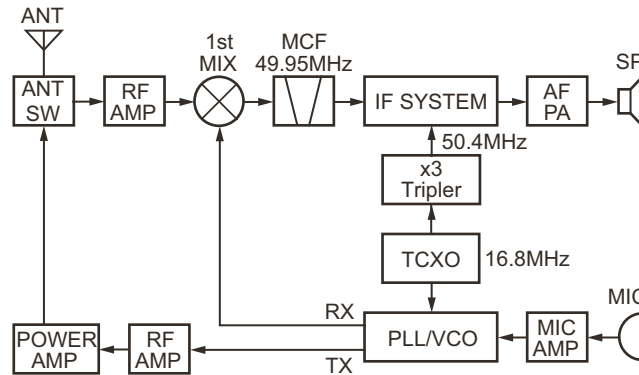


Fig.1 Frequency configuration

2.4.2 Receiver System

The receiver is double conversion superheterodyne.

The frequency configuration is shown in Figure 1.

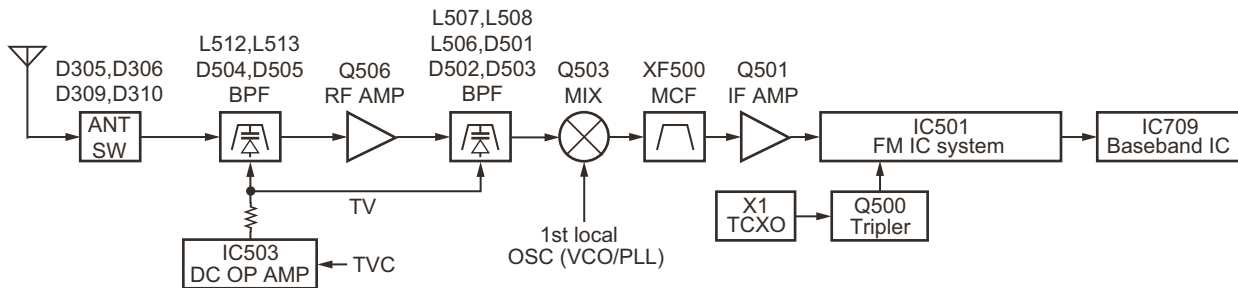


Fig.2 Receiver system

2.4.2.1 Front-end RF Amplifier

An incoming signal from the antenna is applied to an RF amplifier (Q506) after passing through a transmit/receive switch circuit (D305, D306, D309 and D310), BPF (L512, L513, and varactor diodes: D504, D505).

After the signal is amplified (Q506), the signal is filtered by a BPF (L506, L507, L508 and varactor diodes: D501, D502, D503) to eliminate unwanted signals before it is passed to the first mixer.

The voltage of these diodes are controlled by the TVC according to the channel frequency. (See Figure 2)

2.4.2.2 First Mixer

The signal from the RF amplifier is heterodyned with the first local oscillator signal from the PLL frequency synthesizer circuit at the first mixer (Q503) to create a 49.95MHz first intermediate frequency (1st IF) signal. The first IF signal is then fed through one pair of monolithic crystal filter (MCF: XF500) to further remove spurious signals.

2.4.2.3 IF Amplifier Circuit

The first IF signal is amplified by Q501, and enters IC501 (FM processing IC). The signal is heterodyned again with a second local oscillator signal within IC501 to create a 450kHz second IF signal. The second IF signal is fed through a 450 kHz filter in IC501 to further eliminate unwanted signals before it is amplified and FM detected in IC501.

2.4.2.4 AF Signal System

The detection signal from FM IC (IC501) goes to the baseband IC (IC709) DISC input (pin 16) after pass through IC500 for characterizing the signal.

- **Analog signal**

The demodulated AF signal pass through IC709 with the signal processing and output at AUDIO (pin26) to the E-Vol before the Audio amplifier (IC719). The AF signal from IC719 switches between the internal speaker and speaker jack (J701) output.

- **NXDN (Digital signal)**

The recovered 4L-FSK signal obtained from IC501 is decoded in the baseband IC (IC709).

Decoded signal is fed into microprocessor (IC714) and converted to PCM audio signal, and return to baseband IC (IC709) for converting to audio signal.

This audio signal will output at AUDIO (pin26) to the E-Vol before the Audio amplifier (IC719).

The AF signal from IC719 switches between the internal speaker and speaker jack (J701) output.

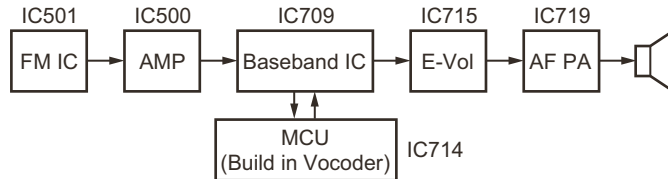


Fig.3 AF signal system

2.4.2.5 Squelch Circuit

Part of the AF signal goes a rectifier circuit to produce a DC voltage corresponding to the noise level after filtering and amplification. There are 2 Noise filters, use for analog mode and NXDN mode respectively. If A_D is high, Analog noise filter is selected and vice-versa. The selection of different noise filter is by 2 multiplexer (IC727 and IC728).

This DC voltage send to MCU (IC714) SQIN input pin.

The MCU controls squelch according to the voltage (SQIN) level.

The signal from the RSSI pin of IC501 is monitored. The electric field strength of the receive signal can be known before the SQIN voltage is input to the MCU, and the scan stop speed is improved.

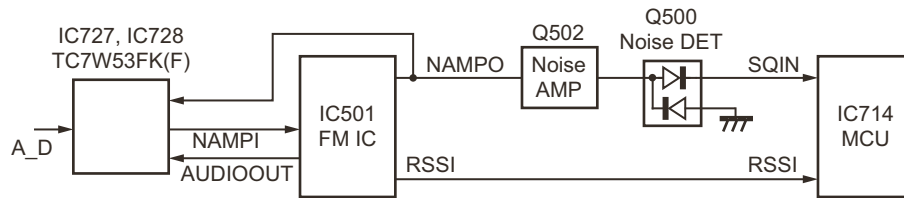


Fig.4 Squelch circuit

2.4.3 Transmitter System

2.4.3.1 Outline

The transmitter circuit produces and amplifies the desired frequency directly. It FM-modulates the carrier signal by means of a varicap diode.

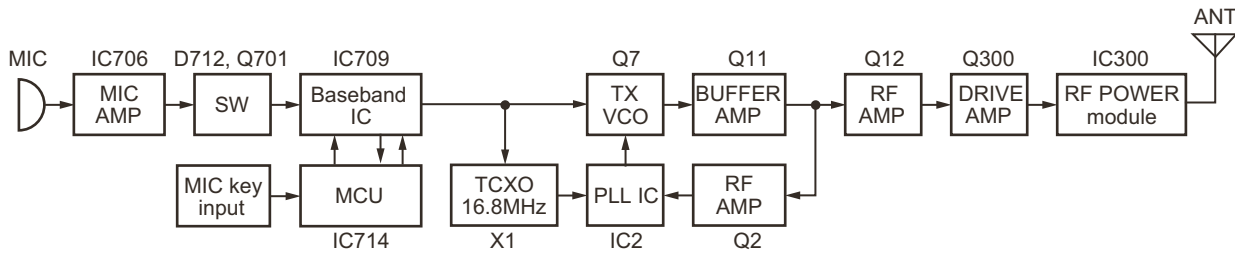


Fig.5 Transmitter system

2.4.3.2 Power Amplifier Circuit

The transmit output signal from the VCO passes through the transmission/reception selection diode (D16) and amplified by Q300. The amplified signal goes to the RF power module (IC300) through a low-pass filter.

The lowpass filter removes unwanted high-frequency harmonic components, and the resulting signal is goes the antenna terminal.

2.4.3.3 APC Circuit

The automatic transmission power control (APC) circuit detects part of a final amplifier output with a coupler circuit and applies a voltage to IC301.

IC301 compares the APC control voltage (APCC) generated by the baseband IC (IC709) and DC amplifier (IC503) with the detection output voltage.

IC301 generates the voltage to control IC300 and stabilizes transmission output.

The APC circuit is configured to protect over current of Q300 and IC300 due to fluctuations of the load at the antenna end and to stabilize transmission output at voltage and temperature variations.

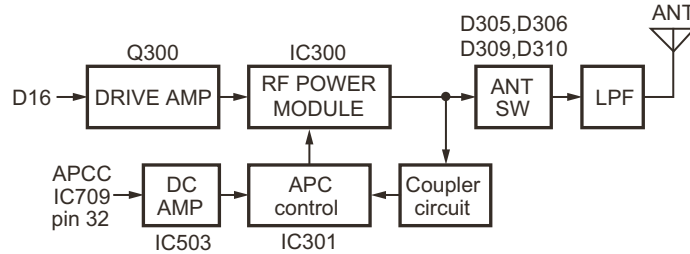


Fig.6 APC circuit and Power amplifier circuit

2.4.4 PLL Frequency Synthesizer

The PLL circuit generates the first local oscillator signal for reception and the RF signal for transmission.

2.4.4.1 PLL Circuit

The frequency step of the PLL is 2.5, 5.0, 6.25 or 7.5 kHz. A 16.8MHz reference signal is divided at IC2 by a fixed counter to produce the reference frequency.

The voltage controlled oscillator (VCO) feedback output is divided by a programmable counter in IC2.

The 2 signals are phase compared, filtered through a low pass filter and passed to VCO to control the oscillator frequency.

2.4.4.2 VCO Circuit

The operating frequency is generated by Q7 in transmit mode and Q5 in receive mode.

The oscillator frequency is controlled by applying the control voltage, which is obtained from the phase comparator, to varactor diodes (D5, D6, D9, D10, D11 in transmit mode and D7, D8, D12, D13, D14 in receive mode).

The TX/RX pin is set "High" in receive mode causing turn on Q9. And T/R pin is set "Low" in transmit mode causing turn on Q6.

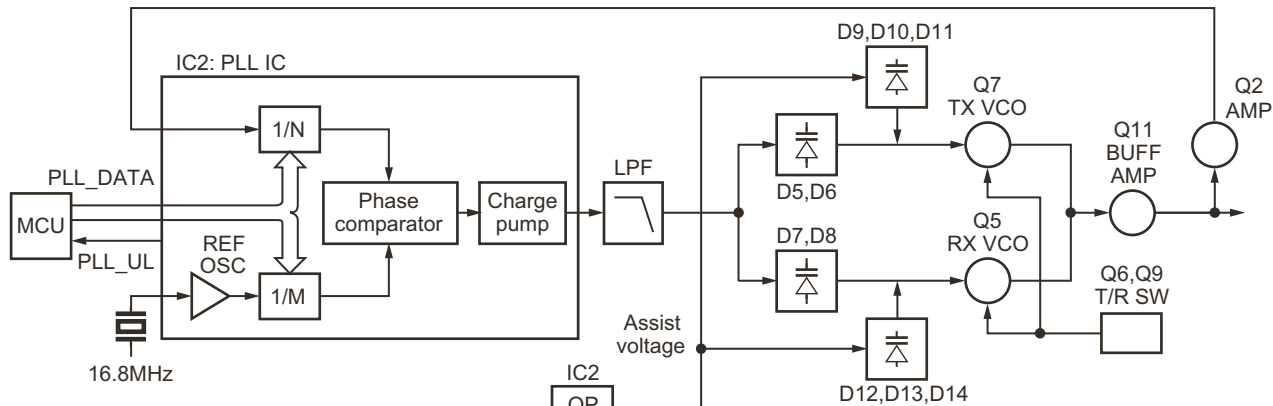


Fig.7 PLL and VCO circuit

2.4.4.3 Unlock Circuit

If low level appears at the "LD" pin of IC2, then PLL an unlock condition occurs.

It causes the voltage applied to the "PLL_UL" pin of the microprocessor to go low.

When the microprocessor detects this condition, the transmitter is disabled by ignoring the push-to-talk switch input signal.

2.4.5 Control Circuit

The MCU carries out the following tasks:

- (1) Controls the FM IC (IC501).
- (2) Controls the baseband IC (IC709).
- (3) Controls the PLL (IC2) & TX/RX outputs.
- (4) Controls IO expender through I2C level converter.
- (5) Controls the display unit.

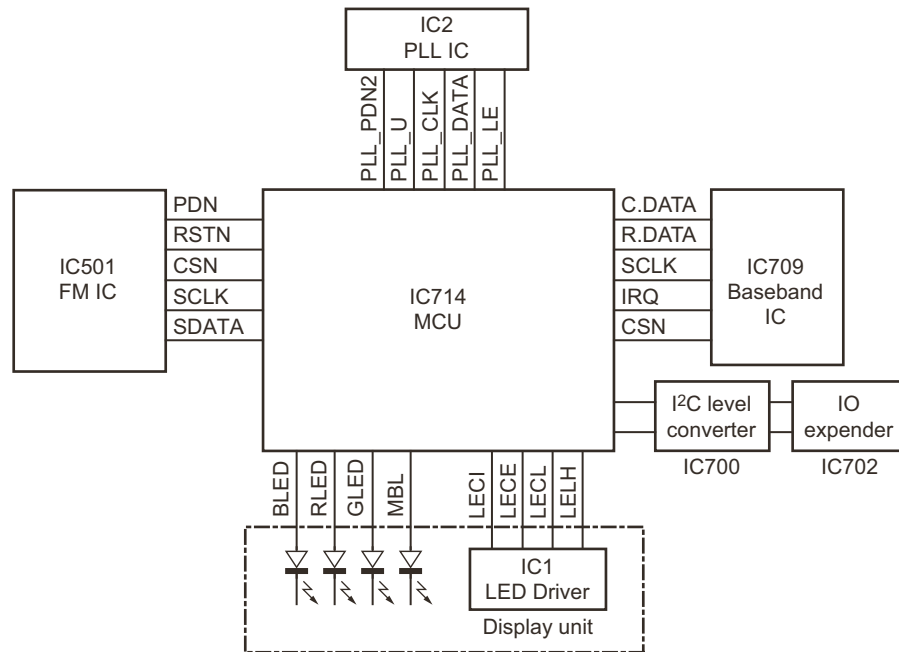


Fig.8 Control circuit

2.4.5.1 Memory Circuit

The transceiver has an 256k-bit EEPROM (IC716). The EEPROM contains adjustment data. The MCU (IC714) controls the EEPROM through three serial data lines.

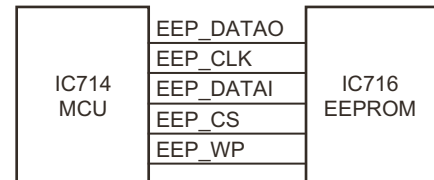


Fig.9 Memory circuit

2.4.5.2 Display Circuit

The MCU (IC714) controls the Display 7-segment LED and LEDs.

When power is on, the MCU will use the MBL line to control the key backlight LEDs.

When the transceiver is busy, the GLED line goes high, Q1 turns on and the green LED (D23) lights after Q4 turn on. In transmit mode, the RLED line goes high, Q2 and Q8 turns on and the red LED (D23) lights.

BLED will be set high when the function select (FPU setting) is on, Q6 turn on and the blue LED (D22) lights.

The dimmer function is controlled by the switch Q5. The LED driver (IC1) controls the functions of the 7-segment LED through the LEDI, LECE, LECL, LELH lines from the MCU.

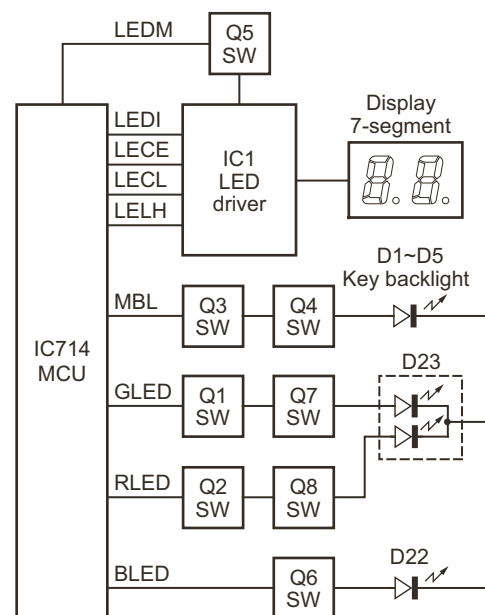


Fig.10 Display circuit

2.4.5.3 Key Matrix Circuit

The front panel has function keys. Each of them is connected to a cross point of a matrix of the KMI1 to KMO3 ports of the micro-processor.

The KMO1 to KMO3 ports are always high, while the KMI1 to KMI3 ports are always low.

The microprocessor monitors the status of the KMI1 to KMO3 ports. If the state of one of the ports changes, the microprocessor assumes that the key at the matrix point corresponding to that port has been pressed.

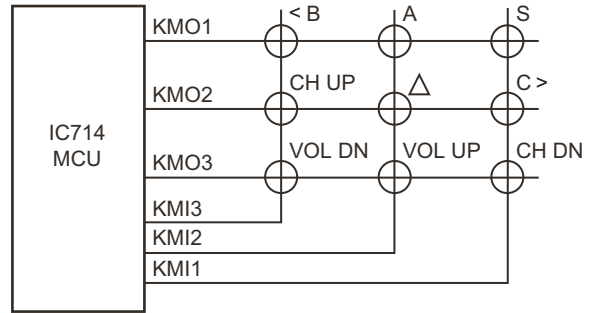


Fig.11 Key matrix circuit

2.4.6 Signaling Circuit

2.4.6.1 Encode

(1) Low-speed data (QT, DQT)

Low-speed data is output from pin 40 (LSDO) of the MCU (IC714).

The signal passes through a low-pass CR filter. The signal is mixed with the audio signal and goes to the VCO and TCXO (X1) modulation input after signal processing in the baseband IC (IC709).

(2) High-speed data (2-tone)

High-speed data (HSD) is output from pin 41 (HSDO) of the MCU.

The signal passes through a low-pass CR filter. TX deviation making an adjustment by microprocessor is applied to the baseband IC (IC709).

The signal is mixed with the audio signal and goes to the VCO and TCXO.

The side tone is audio output of baseband IC(IC709) at the same time to audio power amplifier and then to the speaker.

(3) MSK / DTMF

MSK and DTMF signal is self generated by the baseband IC (IC709).

The TX deviation adjustment is done by the output gain of baseband IC (IC709), and is routed to the VCO.

When encoding MSK/DTMF, the microphone-input signal is muted.

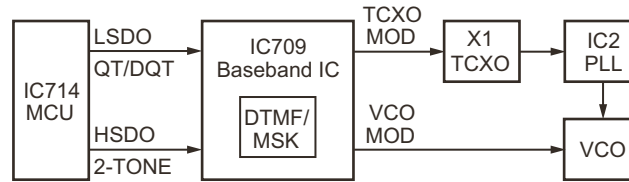


Fig.12 Encode

2.4.6.2 Decode

(1) Low-speed data (QT, DQT)

The demodulated signal from the FM IC (IC501) will input to baseband IC(IC701) to remove frequency above 300Hz.

The signal is input to pin 26 (LSDI) of the MCU.

The MCU digitizes this signal, performs processing such as DC restoration, and decodes the signal.

(2) High-speed data (2-tone)

The demodulated signal from the FM IC (IC501) is amplified by baseband IC and passes through a band-pass filter in IC709 to remove frequency of 3kHz or above and 300Hz or below.

The MCU digitizes this signal and decodes the signal after receive the signal at pin 27(HSDI).

(3) MSK / DTMF

The demodulated signal from the FM IC(IC501) will input to baseband IC(IC709), then the baseband IC will decode and send the decode information to MCU by the data line.

The MCU then processes the decoded information.

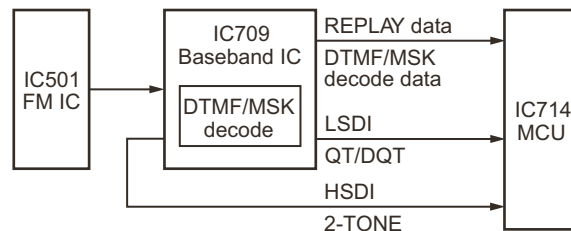


Fig.13 Decode

2.4.6.3 NXDN Receiving

- For Digital Data Mode:
The demodulated signal from IC501 (Pin13) feed into baseband IC (Pin 16) for NXDN decoding.
The decoded digital data will pass to MCU through C-BUS. MCU determines whether or not to output sound from speaker by checking if the data match.
- For Digital Voice Mode:
If the digital data match in MCU, the digital voice payload data will goes into Vocoder in MCU for conversion to PCM.
The PCM data will go to baseband IC through SPI input, where it will be converted to analog by DAC.
Analog voice will be filtered and finally send to audio amplifier.

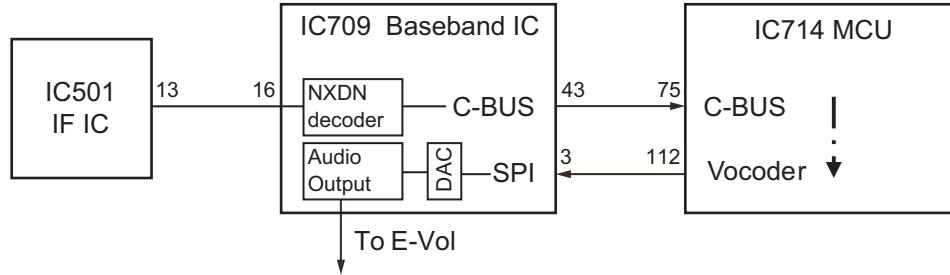


Fig.14 NXDN receiver system

2.4.6.4 NXDN Transmitting

- For Digital Data Mode:
The digital data will be generated by MCU, where it will pass to baseband IC, through C-BUS for encoding process. The encoded data finally will be sent for TCXO and VCO modulation.
- For Digital Voice Mode:
The analog voice from microphone will go to ADC (after audio filter) to convert to PCM data.
The PCM data will be sent to Vocoder through SPI output.
Vocoder will convert the PCM to NXDN protocol, where it will be sent to baseband through C-BUS.
In baseband IC, the data will be encoded and finally transmit through TCXO and VCO modulation.

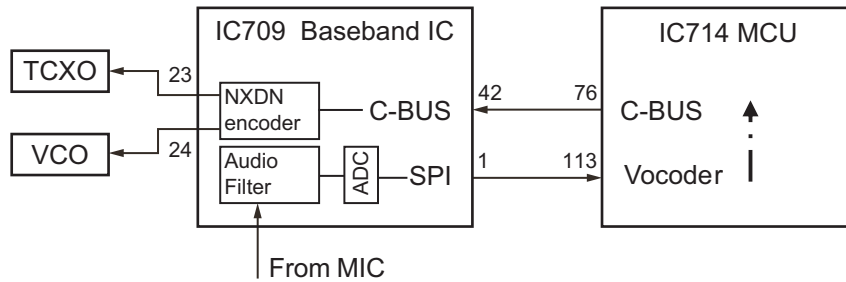


Fig.15 NXDN transmit system

2.4.7 Power Supply Circuit

- When the power switch on the display unit is pressed, the power port on the display unit which is connected pin 140 (POWKEY), goes low, then pin88 (SBC) goes high, Q718 turns on, SB SW (Q719) turns on and power (SB) is supplied to the transceiver.
- When the DC power supplied to the transceiver, the voltage regulator IC (IC720 & IC718) and supply into the MCU VDD and reset voltage detect IC (IC723). IC723 will generate signal (RESET) into the reset terminal on the MCU (IC714) to carry out a power on reset.
- When the DC power voltage decreases from normal voltage, the INT voltage detector IC (IC722) will set to high on MCU port 141 (BATT_INT). If B line becomes less than about 8.5V, MCU will send the backup data to EEPROM (IC716) and go into STOP mode. This circuit has an overvoltage protection circuit. If a DC voltage of 18V or higher is applied to the base of Q712, this voltage turns Q712 on and sets port 141 (BATT_INT) to low. As a result port 88 (SBC) is low, and turns Q719 and Q720 (SB) off.

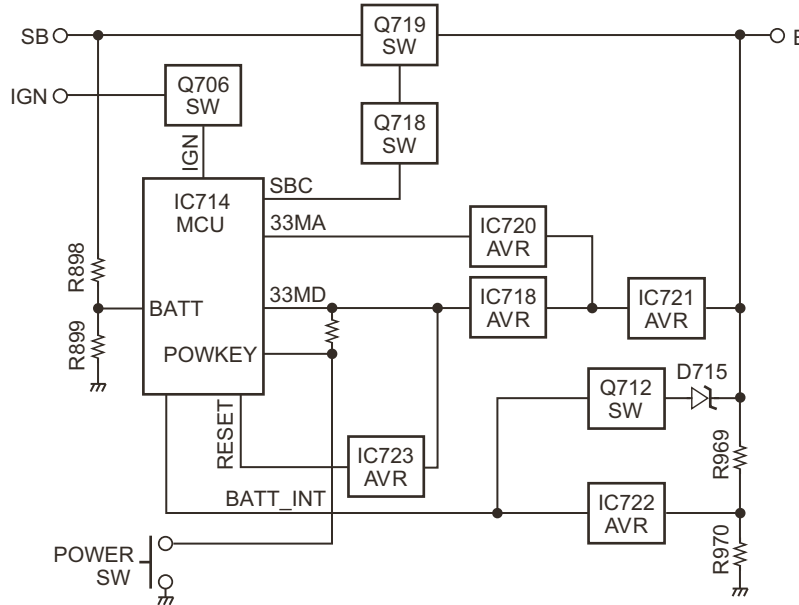


Fig.16 Power supply circuit

2.5 SEMICONDUCTOR DATA

2.5.1 MCU: 2F405ZGT6KFKB (TX-RX unit IC714)

Pin No.	Port Name	I/O	Function
1	LEDI	O	LED Data
2	LECE	O	LED Enable
3	LECL	O	LED Clock
4	LELH	O	LED Latch
5	LERE	O	LED Reset
6	VBAT	-	3.3V
7	ACR_SW	O	ACR switch for HSDO(D/A) port
8	NC	I	No connection
9	NC	I	No connection
10	I2CDT	I/O	Function P8/I/O Expander I2C Data
11	I2CCK	I/O	Function P7/I/O Expander I2C Data
12	EVOL_DACCE	O	Function P6CE for EVOL
13	EVOL_SCLK	O	Function P5CLK for EVOL
14	EVOL_DATA	O	Function P4DATA for EVOL
15	/INT15P	I/O	Function P3/I/O Expander Interrupt
16	VSS	-	GND
17	VDD	-	33MD
18	BEEP	O	Beep for Side Tone
19	A_D	O	Analog/Digital Squelch Switch (Analog:Hi,Digital:Lo)

Pin No.	Port Name	I/O	Function
20	PCBVER	I	PCB version identification
21	IFDET	I	IFDET for ACR improvement
22	DISC_DC (optional)	I	for Discriminator DC Level Adjustment
23	XIN	I	Crystal (19.2MHz)
24	NC	I	No connection
25	RESET	I	MCU Reset pin
26	LSDI	I	Low-Speed Data Input
27	HSDI	I	High-Speed Data Input
28	TEMP_1	I	Temperature 1
29	TEMP_2	I	Temperature 2
30	VDD	-	33MD
31	VSSA	-	GND
32	VREF+	-	33MA
33	VDDA	-	33MA
34	TEST_TX	I/O	UART_TX for Debug
35	TEST_RX	I/O	UART_RX for Debug
36	FNC_1(TXD0)	I/O	Function P1(TXD)
37	FNC_2(RXD0)	I/O	Function P2(RXD)
38	VSS	-	GND

Pin No.	Port Name	I/O	Function
39	VDD	-	33MD
40	LSDO	O	Low-Speed Data Output
41	HSDO	O	High-Speed Data Output
42	3CC	O	33C Control
43	5CC	O	5C Control
44	SQIN	I	Squelch Input
45	RSSI	I	RSSI Input
46	BATT	I	Battery Voltage
47	CVIN	I	VCO Lock Voltage
48	BOOT_1	I	Test Point to enable Bootloader
49	TEST_1	I/O	Test Point 1 for Debug
50	TEST_2	I/O	Test Point 2 for Debug
51	VSS	-	GND
52	VDD	-	33MD
53	PLL_DATA	O	PLL IC Data
54	PLL_LE	O	PLL IC Enable
55	PLL_CLK	O	PLL IC Clock
56	PLL_PDN2	O	PLL IC Power Down
57	PA	O	Public Address
58	AMP_SW	O	AF amplifier
59	9TC	O	9T Control
60	BSFT	O	Beat Shift
61	VSS	-	GND
62	VDD	-	33MD
63	KMO3	O	Key Matrix Output 3
64	KMO2	O	Key Matrix Output 2
65	KMO1	O	Key Matrix Output 1
66	KMI1	I	Key Matrix Input 1
67	KMI2	I	Key Matrix Input 2
68	KMI3	I	Key Matrix Input 3
69	TXD2	O	2nd UART for DE15
70	RXD2	I	2nd UART for DE15
71	VCAP_1	-	2.2uF
72	VDD	-	33MD
73	CML_CSN	O	C-BUS Chip Select
74	CML_SCLK	O	C-BUS Clock
75	CML_RDATA	I	C-BUS Reply Data
76	CML_CDATA	O	C-BUS Command Data
77	EEP_DATAO	O	EEPROM Data Out
78	EEP_DATAI	I	EEPROM Data In
79	EEP_SCLK	O	EEPROM Clock
80	EEP_CS	O	EEPROM Chip Select
81	EEP_WP	O	EEPROM Write Protect
82	BUCNT1	O	PTT/TXD buffer control
83	VSS	-	GND

Pin No.	Port Name	I/O	Function
84	VDD	-	33MD
85	MKEYI	I	DTMF MIC key input
86	BUCNT2	O	DTMF MIC buffer control
87	PLL_UL	I	PLL Unlock Detect
88	SBC	O	SB Control
89	MKEYO	O	DTMF MIC Key output
90	9RC	O	9R Control
91	PTT	I	PTT
92	HOOK	I	Hook
93	ASTSW	O	Assist Speed-up Switch
94	VSS	-	GND
95	VDD	-	33MD
96	FM_SDATA	O	FM IC Data In/Out
97	FM_RSTN	O	FM IC Hardware Reset
98	FM_SCLK	O	FM IC Clock
99	FM_CSN	O	FM IC Chip Select
100	FM_PDN	O	FM IC Power Down
101	TXD	O	Serial Data to Mic Jack
102	RXD	I	Serial Data from Mic Jack
103	TXRX	O	TX / RX Switch
104	HORN	O	Horn Alert
105	SWDIO	I/O	SWDIO for SWD
106	VCAP2	-	2.2uF
107	VSS	-	GND
108	VDD	-	33MD
109	SWCLK	I/O	SWCLK for SWD
110	CML_SSOUT	I	Audio Codec Chip Select
111	CML_EPSCLK	I	Audio Codec Clock
112	CML_EPSO	O	Audio Codec Data Out
113	CML_EPSI	I	Audio Codec Data In
114	NC	I	No connection
115	LEDM	O	LED Dimmer
116	MBL	O	Panel and Mic Key Backlight
117	FNC_4 (CTS0)	I	Function P4(CTS)
118	FNC_3(RTS0)	O	Function P3(RTS)
119	NC	I	No connection
120	VSS	-	GND
121	VDD	-	33MD
122	33BC	O	BB IC power supply control
123	MIC1MUTE	O	Internal Mic Mute
124	MIC2MUTE	O	External Mic Mute
125	SP_MUTE	O	Speaker Mute
126	SIM1	I	Shimuke port 1
127	SIM2	I	Shimuke port 2
128	NC	O	No connection

Pin No.	Port Name	I/O	Function
129	NC	O	No connection
130	VSS	-	GND
131	VDD	-	33MD
132	NC	O	No connection
133	SWO	O	SWO for SWD
134	SRST	O	SRST for SWD
135	BLULED	O	Blue LED
136	GRNLED	O	Green LED
137	REDLED	O	Red LED
138	BOOT_0		Test Point to enable Bootloader
139	IGN	I	Ignition Sense
140	POWKEY	I	Power Key Input
141	BATT_INT	I	MCU Stop
142	CML_IRQ	I	Baseband IC IRQ (CML_pin8)
143	Power On Reset	I	33MD
144	VDD	-	33MD

2.6 COMPONENTS DESCRIPTION

2.6.1 Display Unit (X54-3890-20)

Ref. No.	Part Name	Description
IC1	IC	LED driver
Q1, Q2	Transistor	TX/RX indication LED switch
Q3, Q4	Transistor	KEY backlight control switch
Q5, Q9	Transistor	LED dimmer control switch
Q6	Transistor	Indication LED switch
Q7, Q8	Transistor	TX/Busy indication LED switch
D1~D5	LED	KEY backlight
D20	Diode	Voltage protection
D22	LED	Indication
D23	LED	TX/Busy indication
D33	Zener diode	Surge protection
D37	LED	LED display

2.6.2 TX-RX Unit (XC1-0010-XX)

Ref. No.	Part Name	Description
IC2	IC	PLL IC
IC3	IC	Assist Filter
IC300	IC	Power module
IC301	IC	APC DC amplifier
IC500	IC	DETAFA amplifier
IC501	IC	FM IC
IC502	IC	Voltage regulator (33MS)
IC503	IC	RF BPF DC amplifier
IC700	IC	I2C level shifter
IC701	IC	3.3V level shifter (RXDO/CTSO)

Ref. No.	Part Name	Description
IC702	IC	IO expander
IC703	IC	5V level shifter (TXDO/RTSO)
IC704	IC	RS-232C driver
IC705	IC	5V level shifter (TXD2)
IC706	IC	MIC/AFO amplifier
IC709	IC	Baseband IC
IC710	IC	Voltage regulator (33BD)
IC713	IC	19.2MHz amplifier
IC714	IC	MCU
IC715	IC	Evol/DAC
IC716	IC	EEPROM
IC717	IC	Voltage regulator (9R)
IC718	IC	Voltage regulator (33MD)
IC719	IC	AF amplifier
IC720	IC	Voltage regulator (33MA)
IC721	IC	Voltage regulator (5M)
IC722	IC	Voltage detection (INT)
IC723	IC	Voltage detection (Reset)
IC724	IC	5V level shifter (MKEY/PTT)
IC725	IC	3.3V level shifter (MKEY/PTT)
IC726	IC	Voltage regulator (33C)
IC727, IC728	IC	Analog/Digital Multiplexer
Q2	Transistor	PLL Fin amplifier
Q3, Q4	FET	Assist filter control switch
Q5	FET	RX VCO
Q6	FET	TX/RX VCO switch
Q7	FET	TX VCO
Q9	Transistor	TX/RX VCO switch
Q10	Transistor	Ripple filter
Q11	Transistor	VCO buffer amp
Q12	Transistor	VCO common amp
Q300	FET	Drive amp
Q500	Transistor	RX 2nd local amp
Q501	Transistor	1st IF amp
Q502	Transistor	Squelch amp
Q503	FET	Mixer
Q504	Transistor	Squelch input switch
Q505	Transistor	Squelch input control switch
Q506	FET	Front-end LNA
Q700	FET	MIC mute switch
Q701	Transistor	MIC mute switch
Q702	Transistor	Horn alert control switch
Q703	FET	Horn alert switch
Q704	Transistor	DETO amp
Q705	FET	MOD2 switch

Ref. No.	Part Name	Description
Q706	Transistor	Ignition sense control switch
Q707	Transistor	9R control switch
Q708	Transistor	9T control switch
Q709	Transistor	9R switch
Q710	Transistor	9T switch
Q711	Transistor	5C control switch
Q712	Transistor	Overvoltage detect
Q713	Transistor	Overvoltage detect
Q714, Q715	FET	Speaker mute switch
Q716	Transistor	5C switch
Q717	FET	33BA switch
Q718	Transistor	SB control switch
Q719	FET	SB switch
Q720	Transistor	AF amp switch
Q721	FET	5MS switch
Q722	Transistor	5MS control switch
Q723	Transistor	33BA control switch
D5	Variable Capacitance Diode	TX VCO tune
D6	Variable Capacitance Diode	TX VCO tune
D7	Variable Capacitance Diode	RX VCO tune
D8	Variable Capacitance Diode	RX VCO tune
D9	Variable Capacitance Diode	TX VCO tune
D10	Variable Capacitance Diode	TX VCO tune
D11	Variable Capacitance Diode	TX VCO tune
D12	Variable Capacitance Diode	RX VCO tune
D13	Variable Capacitance Diode	RX VCO tune
D14	Variable Capacitance Diode	RX VCO tune
D15	Variable Capacitance Diode	Modulation Control (TX VCO)
D16, D17	Diode	TX/RX band switch
D300	Zener Diode	Voltage Protection
D301	Diode	TX power control
D304	Diode	Reverse power rectifier
D305, D306	Diode	ANT switch
D307, D308	Diode	Power rectifier
D309, D310	Diode	ANT switch

Ref. No.	Part Name	Description
D500	Diode	Squelch voltage rectifier
D502~D505	Variable Capacitance Diode	RF BPF tuning
D700~D702	Diode	Surge protection
D704~D711	Diode	Surge protection
D712, D713	Diode	MIC AGC detection
D714, D715	Diode	RXD2 level shifter
D717	Zener Diode	Voltage Protection
D718	Diode	5M stabilize
D719	Surge Absorber	Voltage Protection
D720	Diode	PTT
D721	Diode	Hook

2.7 TERMINAL FUNCTION

2.7.1 Display unit (X54-3890-20)

Pin No.	Name	I/O	Function
CN1			
1	POWER	O	Detection output of power switch
2	MKEY	I/O	MIC data detection
3	PTT/TXD	I/O	PTT/PC serial data
4	HOOK/RXD	I/O	HOOK/PC serial data
5	ME	-	MIC ground
6	MIC	O	MIC signal output
7	GND	-	Ground
8	NC	-	-
9	LELH	I	LED latch input
10	LECL	I	LED clock input
11	LECE	I	LED enable input
12	LEDI	I	LED data input
13	5C	I	5V DC power supply
14	KMI3	O	Key matrix output 3
15	KMI2	O	Key matrix output 2
16	KMI1	O	Key matrix output 1
17	KMO3	I	Key matrix input 3
18	KMO2	I	Key matrix input 2
19	KMO1	I	Key matrix input 1
20	BLED	I	Blue LED control signal input
21	GLED	I	Green LED control signal input
22	RLED	I	Red LED control signal input
23	MBL	I	MIC backlight control signal input
24	LEDM	I	LED dimmer input
25	SP-	I	Speaker input -

Pin No.	Name	I/O	Function
26	SP-	I	Speaker input -
27	SP+	I	Speaker input +
28	SP+	I	Speaker input +
29	SB	I	Battery voltage DC supply
30	SB	I	Battery voltage DC supply
J1(MIC Jack)			
1	MBL	O	Backlight of Microphone
2	SB	O	Battery voltage DC supply
3	GND	-	Ground
4	PTT	I	PTT/ PC serial data from radio
5	ME	-	MIC ground
6	MIC	I	MIC signal input
7	HOOK	I	HOOK/ PC serial data to radio
8	DM	I/O	MIC data detection

2.7.2 TX-RX unit (XC1-0010-XX)

Pin No.	Name	I/O	Function
CN700			
1	SB	O	Battery voltage DC supply
2	SB	O	Battery voltage DC supply
3	SP-	O	Speaker input -
4	SP-	O	Speaker input -
5	SP+	O	Speaker input +
6	SP+	O	Speaker input +
7	LEDM	O	LED dimmer output
8	MBL	O	MIC backlight control signal output
9	RLED	O	Red LED control signal output
10	GLED	O	Green LED control signal output
11	BLED	O	Blue LED control signal output
12	KMO1	O	Key matrix output 1
13	KMO2	O	Key matrix output 2
14	KMO3	O	Key matrix output 3
15	KMI1	I	Key matrix input 1
16	KMI2	I	Key matrix input 2
17	KMI3	I	Key matrix input 3
18	5MS	O	5V DC power supply
19	LEDI	O	LED data output
20	LECE	O	LED enable output
21	LECL	O	LED clock output
22	LELH	O	LED latch output
23	LERE	O	LED reset output
24	GND	-	Ground
25	MIC	I	MIC signal input

Pin No.	Name	I/O	Function
26	ME	-	MIC ground
27	HOOK/RXD	I/O	HOOK/PC serial data
28	PTT/TXD	I/O	PTT/PC serial data
29	MKEY	I/O	MIC data detection
30	POWER	I	Detection input of power switch
J700 (D-SUB 15pin)			
1	SB	O	Battery voltage DC supply DC 13.6V±15%,1.0A max
2	IGN	I	Ignition sens input,16.0V max
3	SP2	O	Speaker output
4	DETO	O	FM detector output, 500mVp-p
5	DATAI	I	External transmit signal input 200±50mVp-p
6	FNC1	I/O	Programable I/O (programmed by FPU) 1.0mA max.
7	FNC2	I/O	Programable I/O (programmed by FPU) 1.0mA max.
8	FNC3	I/O	Programable I/O (programmed by FPU) 1.0mA max.
9	FNC4	I/O	Programable I/O (programmed by FPU) 1.0mA max.
10	FNC5	I/O	Programable I/O (programmed by FPU) 1.0mA max.
11	FNC6	I/O	Programable I/O (programmed by FPU) 1.0mA max.
12	5MS	O	5V DC power supply, 100mA max
13	HR1	O	Horn alert signal output,16.0V/ 2.0A max.
14	HR2	O	Horn alert signal output,16.0V/ 2.0A max.
15	GND	-	Ground

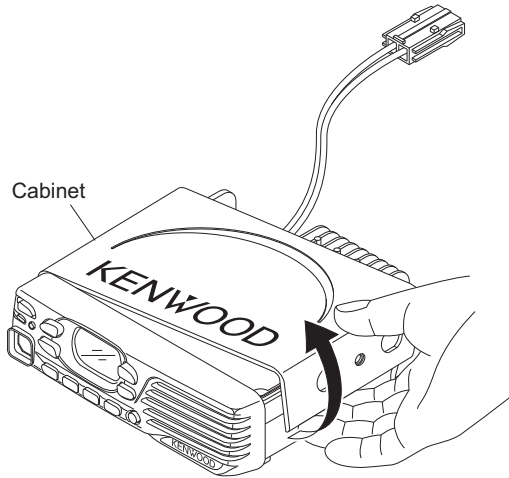
2.7.3 Function Port Assignment

	GPS (NMEA)	
	Name	I/O
FNC1	None	O
FNC2	GPS (NMEA Input)	I
FNC3	-	-
FNC4	-	-
FNC5	-	-
FNC6	-	-
FNC7	-	-
FNC8	-	-

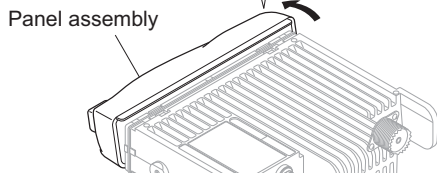
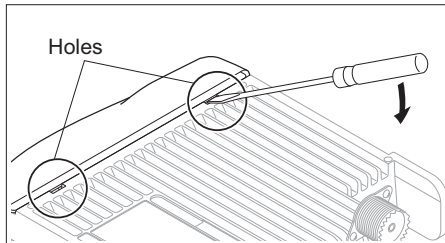
SECTION 3 DISASSEMBLY

3.1 Disassembly Procedure

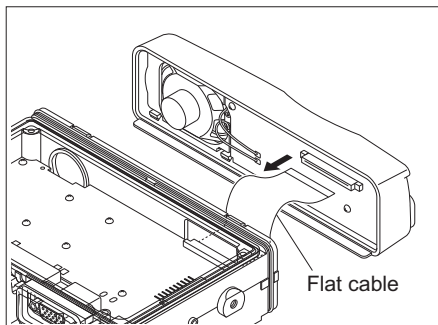
- (1) When removing the cabinet, first remove the two screws from the right and left with a phillips screwdriver. Then, hook your finger on the edge of the cabinet and pull it out until it is over the chassis protrusion. Remove the cabinet by prying the cabinet as shown below.



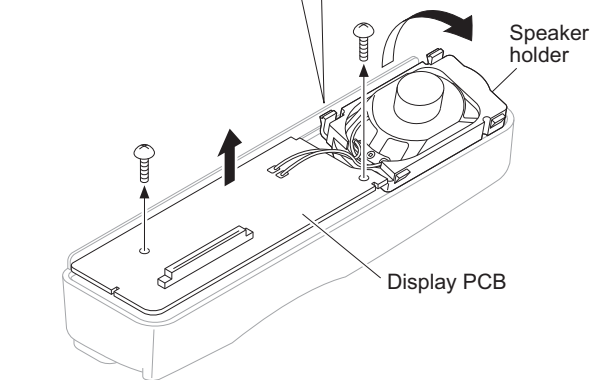
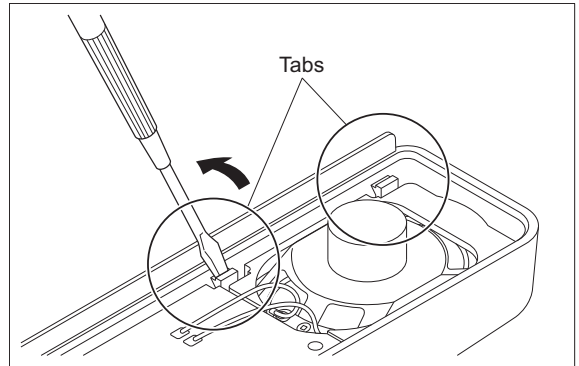
- (2) To remove the panel assembly, first turn the transceiver upside down. Then, insert a flat-head screwdriver into the holes of the chassis and tilt it in the direction as shown by the arrow.



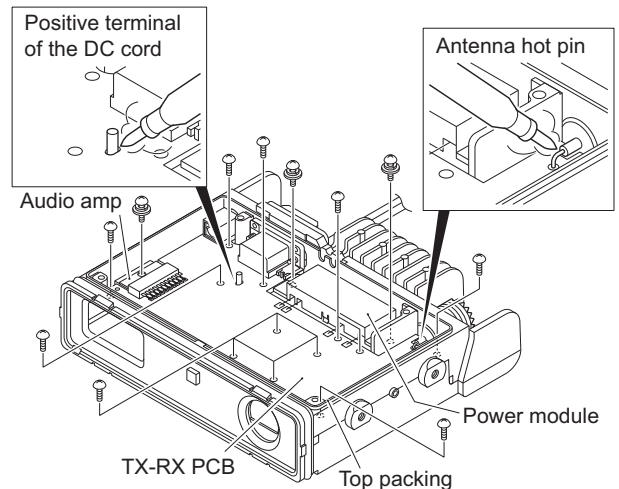
- (3) Disconnect the flat cable from connector of the panel assembly.



- (4) To remove the speaker holder, first remove the two screws from the display PCB using a phillips screwdriver. Then, insert a flat-head screwdriver under the tabs of the speaker holder and tilt it in the direction shown by the arrow.

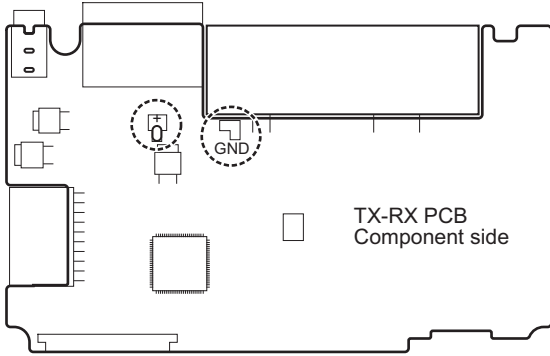


- (5) When removing the TX-RX PCB, first remove the top packing. Then, remove the solder of the antenna hot pin and positive terminal of the DC cord. Remove the 15 screws from the TX-RX PCB, power module, and audio amp.

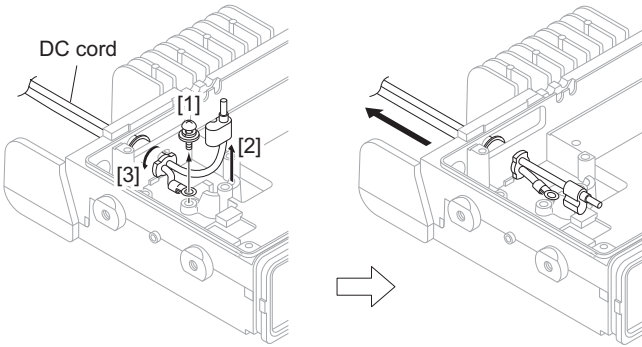


Note:

When you supply power to the TX-RX PCB after removing the TX-RX PCB from the chassis, solder the positive and ground terminals of the DC cord (Recommendation: E30-3448-25) to the + and GND terminals of the TX-RX PCB.

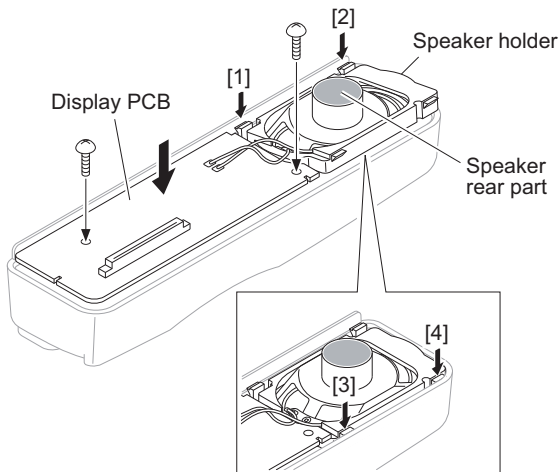


- (6) Pull it out behind the chassis by rotating the bush [3] of the DC cord 90 degrees in the direction of the arrow after the screw [1] in the negative terminal is removed, and the positive terminal [2] is removed from the chassis.

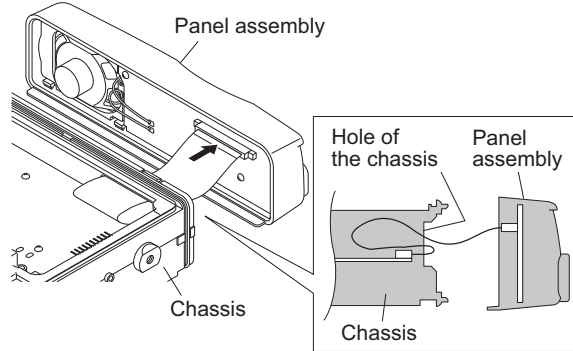


3.2 Precautions for Reassembly

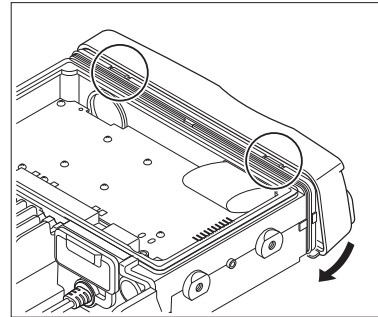
- (1) When mounting the speaker holder, while suppressing the speaker rear part (shaded area), fix the four tabs of the speaker holder into the hollows of the front panel in order ([1], [2], [3], and [4]). Then, tighten the two screws of the display PCB.



- (2) When mounting the panel assembly, pass the flat cable through the hole of the chassis as shown below then connect the flat cable to connector of the panel assembly.

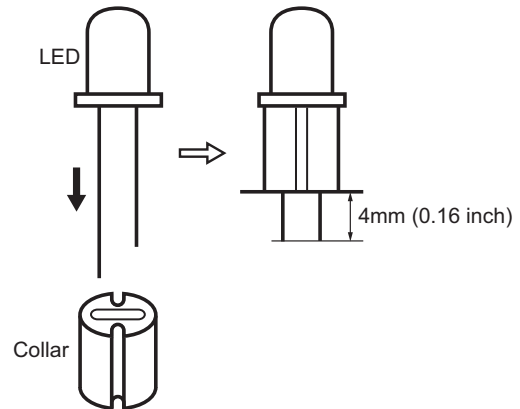


- (3) Fit the panel assembly into the two tabs of the chassis top side first. Then, fit the panel assembly into the two tabs of the chassis bottom side by turning the panel assembly.



3.3 Correspondence when replacing the LED (B30-2321-05)

When replacing the LED (B30-2321-05), cut the leg of the LED to 4mm (0.16 inch) after installing the Collar (J31-0565-15).



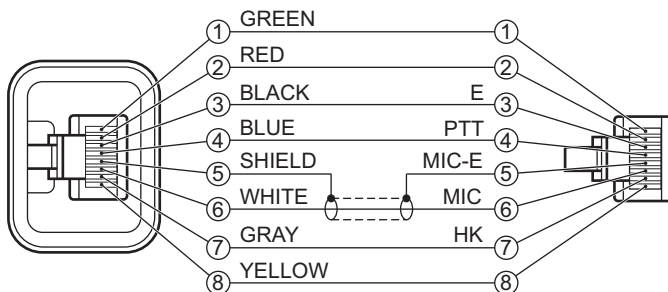
SECTION 4 ADJUSTMENT

4.1 Test Equipment Required for Alignment

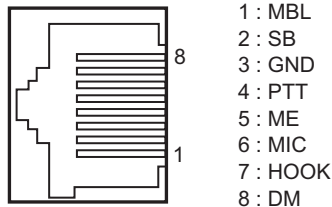
Test Equipment	Major Specifications	
1. Standard Signal Generator (SSG)	Frequency Range	100 to 520MHz
	Modulation	Frequency modulation and external modulation
	Output	-127dBm/0.1μV to greater than -7dBm/100mV
2. Power Meter	Input Impedance	50Ω
	Operation Frequency	100 to 520MHz
	Measuring Range	Vicinity of 100W
3. Deviation Meter	Frequency Range	100 to 520MHz
4. Digital Volt Meter (DVM)	Measuring Range	1V to 20V DC
	Input Impedance	High input impedance for minimum circuit loading
5. Oscilloscope		DC through 30MHz
6. High Sensitivity Frequency Counter	Frequency Range	10Hz to 1000MHz
	Frequency Stability	0.2ppm or less
7. Ammeter		20A
8. AF Volt Meter (AF VM)	Frequency Range	50Hz to 10kHz
	Voltage Range	1mV to 3V
9. Audio Generator (AG)	Frequency Range	20Hz to 20kHz or more
	Output	0 to 1V
10. Distortion Meter	Capability	3% or less at 1kHz
	Input Level	50mV to 10Vrms
11. 4Ω Dummy Load		Approx. 4Ω, 10W or more
12. Regulated Power Supply		13.6V, approx. 20A (adjusted from 9 to 17V) Useful if ammeter equipped

*The test equipment which is not used for adjustment is contained in this table.

■ Test cable for microphone input (E30-3360-28)



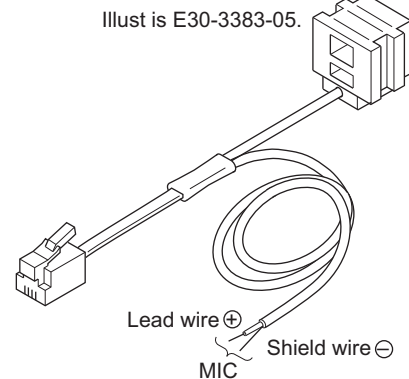
■ MIC connector (Front panel view)



- 1 : MBL
- 2 : SB
- 3 : GND
- 4 : PTT
- 5 : ME
- 6 : MIC
- 7 : HOOK
- 8 : DM

■ Tuning cable (E30-3383-05 or E30-7754-05)

Adapter cable (E30-3383-05) is required for injecting an audio if PC tuning is used.
See "PC Mode" section for the connection.



4.2 Frequency and Signaling

The transceiver has been adjusted for the frequencies shown in the following table. When required, re-adjust them following the adjustment procedure to obtain the frequencies you want in actual operation.

■Test frequency

Chan- nel No.	K, M		K2, M2	
	RX (MHz)	TX (MHz)	RX (MHz)	TX (MHz)
1	485.05000	485.10000	435.05000	435.10000
2	450.05000	450.10000	400.05000	400.10000
3	519.95000	519.90000	469.95000	469.90000
4	485.00000	485.00000	435.00000	435.00000
5	485.20000	485.20000	435.20000	435.20000
6	485.40000	485.40000	435.40000	435.40000
7~16	-	-	-	-

■Analog Signaling

Signaling No.	RX (Decode)	TX (Encode)
1	None	None
2	None	20Hz Square wave
3	QT 67.0Hz	QT 67.0Hz
4	QT 151.4Hz	QT 151.4Hz
5	QT 210.7Hz	QT 210.7Hz
6	QT 254.1Hz	QT 254.1Hz
7	DQT D023N	DQT D023N
8	DQT D754I	DQT D754I
9	DTMF Decode (Code: 159D)	DTMF Encode (Code: 159D)
10	None	DTMF (Code: 9)
11	None	MSK (1010)
12	FleetSync (100-1000)	FleetSync (100-1000)
13	None	Single Tone (1000Hz)
14	2-tone Decode A: 304.7Hz B: 3106.0Hz	2-tone Encode A: 304.7Hz B: 3106.0Hz
15	None	DTMF Tone (1477Hz)
16	Single Tone (979.9Hz)	Single Tone (979.9Hz)
17	None	MSK PN9
18	None	DTMF (Code: 3)

■Digital Signaling

Signaling No.	RX (Decode)	TX (Encode)
1	RAN 1	RAN 1
2	None	PN9
3	RAN 1	Maximum Deviation pattern
4	FSW +PN9	MOD Set-up *1
5	Tone Pattern (1031Hz)	Tone Pattern (1031Hz)

RAN: Radio Access Number

PN9: Pseudo-Random Pattern (for production only)

*1: To output 150Hz square wave (for production only)

4.3 Preparations for Tuning the Transceiver

Before attempting to tune the transceiver, connect the unit to a suitable power supply.

Whenever the transmitter is tuned, the unit must be connected to a suitable dummy load (i.e. power meter).

The speaker output connector must be terminated with a 4Ω dummy load and connected to an AC voltmeter and an audio distortion meter or a SINAD measurement meter at all times during tuning.

■5 reference level adjustments frequency

TEST CH	K, M		K2, M2	
	RX (MHz)	TX (MHz)	RX (MHz)	TX (MHz)
Low	450.05000	450.10000	400.05000	400.10000
Low'	467.55000	467.60000	417.55000	417.50000
Center	485.05000	485.10000	435.05000	435.10000
High'	502.55000	502.60000	452.55000	452.50000
High	519.95000	519.90000	469.95000	469.90000

4.4 Common Section

Item	Condition	Measurement			Adjustment			Specifications /Remarks
		Test-equipment	Unit	Terminal	Unit	Parts	Method	
1. Setting	1) Power supply voltage DC power supply terminal : 13.6V							
2. Receive Assist voltage	1) Select each tab CH : Low CH : Low' CH : Center CH : High' CH : High * Note 1					FPU	Voltage level indicated in PC window show VCO Lock voltage. Change the adjustment to get the VCO Lock Voltage within the limit of the specified voltage.	2.5 ±0.1V
3. Transmit Assist voltage	1) Select each tab CH : Low CH : Low' CH : Center CH : High' CH : High * Note 1							

***Note 1:**

Click [Tune Assist Voltage] button on Test Mode dialog box, these adjustment can be done by automatic.

When automatic adjustment is done, Lock Voltage can not be confirmed on the window.

If you would like to confirm it, please check the lock voltage on Receive Assist Voltage adjustment window and Transmit Assist Voltage adjustment window.

4.5 Transmitter Section

Item	Condition	Measurement			Adjustment			Specifications /Remarks
		Test-equipment	Unit	Terminal	Unit	Parts	Method	
1. Frequency	1) Test CH : Center (1 point) PTT ON (Transmit)	Frequency counter		ANT	TX-RX	FPU	485.1MHz (K, M) 435.1MHz (K2, M2)	±50Hz
2. High Transmit Power	NX-840H 1) Test CH : Low, Low', Center, High', High (5 point) 2) Battery Terminal voltage : 13.6V 3) PTT ON (Transmit)	Power meter Ammeter		ANT		FPU	Low, Low', Center, High' : 45.0W High : 45.0W (HK, HUK) High : 40.0W (HK2)	±2.0W(HK,HUK) ±1.0W(HK2) 14.0A or less
	NX-840 1) Test CH : Low, Low', Center, High', High (5 point) 2) Battery Terminal voltage : 13.6V 3) PTT ON (Transmit)	Power meter Ammeter		ANT		FPU	25.0W	±1.0W 8.0A or less
3. Mid Transmit Power	NX-840H 1) Test CH : Low, Low', Center, High', High (5 point) 2) Battery Terminal voltage : 13.6V 3) PTT ON (Transmit)	Power meter Ammeter		ANT		FPU	25.0W	±1.0W 8.0A or less

Item	Condition	Measurement			Adjustment			Specifications /Remarks
		Test-equipment	Unit	Terminal	Unit	Parts	Method	
4. Low Transmit Power	1) Test CH : Low, Low', Center, High', High (5 point) 2) Battery Terminal voltage : 13.6V 3) PTT ON (Transmit)	Power meter Ammeter		ANT		FPU	5.0W	±0.5W 4.0A or less
5. DQT Balance *Note 2	DQT Balance 1 1) Test CH : Low, Low', Center, High', High (5 point) 2) Deviation meter filter LPF : 15kHz HPF : OFF 3) PTT ON (Transmit)	Deviation Meter Oscilloscope		ANT		FPU	Make the demodulation wave into square wave	
	DQT Balance 2 1) Test CH : Low, Low', Center, High', High (5 point) 2) Deviation meter filter LPF : 3kHz HPF : OFF 3) PTT ON (Transmit)	Deviation Meter Oscilloscope		ANT		FPU	The Deviation of 20Hz frequency is fixed. Change the 1kHz adjustment value to become the same deviation of 20Hz within the specified range.	±15Hz
6. Maximum Deviation (Analog Narrow)	1) Test CH : Low, Low', Center, High', High (5 point) 2) Deviation meter filter LPF : 15kHz HPF : OFF 3) PTT ON (Transmit)	Deviation Meter Oscilloscope		ANT		FPU	2.0kHz (According to larger +, -)	±80Hz Note : FPU auto input 1kHz / 150mV
(Analog Wide)	1) Test CH : Low, Low', Center, High', High (5 point) 2) Deviation meter filter LPF : 15kHz HPF : OFF 3) PTT ON (Transmit)	Deviation Meter Oscilloscope		ANT		FPU	4.0kHz (According to larger +, -)	±80Hz Note : FPU auto input 1kHz / 150mV
7. Maximum Deviation (NXDN Very Narrow) *Note 3	1) Test CH : Low, Low', Center, High', High (5 point) 2) Deviation meter filter LPF : 15kHz HPF : OFF 3) PTT ON (Transmit)	Deviation Meter Oscilloscope		ANT		FPU	1.337kHz	±26Hz
(NXDN Narrow)	1) Test CH : Low, Low', Center, High', High (5 point) 2) Deviation meter filter LPF : 15kHz HPF : OFF 3) PTT ON (Transmit)	Deviation Meter Oscilloscope		ANT		FPU	3.056kHz	±59Hz
8. CW ID Deviation (NXDN Very Narrow) *Note 3	1) Test CH : Center (1 point) 2) Deviation meter filter LPF : 15kHz HPF : OFF 3) PTT ON (Transmit)	Deviation Meter Oscilloscope		ANT		FPU	1.00kHz	±100Hz

Item	Condition	Measurement			Adjustment			Specifications /Remarks
		Test-equipment	Unit	Terminal	Unit	Parts	Method	
9. DQT Deviation (Analog Narrow)	1) Test CH : Center (1 point) 2) Deviation meter filter LPF : 3kHz HPF : OFF 3) PTT ON (Transmit)	Deviation Meter Oscilloscope		ANT		FPU	0.35kHz	±50Hz
(Analog Wide)	1) Test CH : Center (1 point) 2) Deviation meter filter LPF : 3kHz HPF : OFF 3) PTT ON (Transmit)	Deviation Meter Oscilloscope		ANT		FPU	0.75kHz	±50Hz
10. QT Deviation (Analog Narrow)	1) Test CH : Center (1 point) 2) Deviation meter filter LPF : 3kHz HPF : OFF 3) PTT ON (Transmit)	Deviation Meter Oscilloscope		ANT		FPU	0.35kHz	±50Hz
(Analog Wide)	1) Test CH : Center (1 point) 2) Deviation meter filter LPF : 3kHz HPF : OFF 3) PTT ON (Transmit)	Deviation Meter Oscilloscope		ANT		FPU	0.75kHz	±50Hz
11. DTMF Deviation (Analog Narrow)	1) Test CH : Center (1 point) 2) Deviation meter filter LPF : 15kHz HPF : OFF 3) PTT ON (Transmit)	Deviation Meter Oscilloscope		ANT		FPU	1.5kHz	±50Hz
(Analog Wide)	1) Test CH : Center (1 point) 2) Deviation meter filter LPF : 15kHz HPF : OFF 3) PTT ON (Transmit)	Deviation Meter Oscilloscope		ANT		FPU	3.0kHz	±50Hz
12. MSK Deviation (Analog Narrow)	1) Test CH : Center (1 point) 2) Deviation meter filter LPF : 15kHz HPF : OFF 3) PTT ON (Transmit)	Deviation Meter Oscilloscope		ANT		FPU	1.5kHz	±50Hz
(Analog Wide)	1) Test CH : Center (1 point) 2) Deviation meter filter LPF : 15kHz HPF : OFF 3) PTT ON (Transmit)	Deviation Meter Oscilloscope		ANT		FPU	3.0kHz	±50Hz

***Note 2:**

Only 1 DQT Balance need to align(either DQT Balance1 or DQT Balance2).

***Note 3:**

(for the NXDN Very Narrow Maximum Deviation and CW ID Deviation alignment)

Must do the NXDN Very Narrow Maximum Deviation and CW ID Deviation alignment after the DQT Balance alignment and Maximum Deviation alignment (Analog).

4.6 Receiver Section

Item	Condition	Measurement			Adjustment			Specifications /Remarks
		Test-equipment	Unit	Terminal	Unit	Parts	Method	
1. Receive Sensitivity [Semiautomatic]	1) Test CH : Low, Low', Center, High', High (5 point) 2) SSG output : -90dBm AF Freq : 1kHz Mod Dev : ±1.5kHz	SSG Oscilloscope Audio Analyzer		ANT		FPU	Press [Start], [Auto tuning]	
2. Open Squelch (5)	[Analog Narrow] 1) Test CH : Low, Low', Center, High', High (5 point) 2) SSG output : -120dBm AF Freq : 1kHz Mod Dev : ±1.5kHz	SSG Oscilloscope Audio Analyzer		ANT		FPU	Press [Start], [Auto tuning]	
	[Analog Wide] 1) Test CH : Low, Low', Center, High', High (5 point) 2) SSG output : -121dBm AF Freq : 1kHz Mod Dev : ±3.0kHz	SSG Oscilloscope Audio Analyzer		ANT		FPU	Press [Start], [Auto tuning]	
	[NXDN Very Narrow] 1) Test CH : Low, Low', Center, High', High (5 point) 2) SSG output : -124dBm AF Freq : 400Hz Mod Dev : ±1.1kHz	SSG Oscilloscope Audio Analyzer		ANT		FPU	Press [Start], [Auto tuning]	
	[NXDN Narrow] 1) Test CH : Low, Low', Center, High', High (5 point) 2) SSG output : -122dBm AF Freq : 400Hz Mod Dev : ±2.2kHz	SSG Oscilloscope Audio Analyzer		ANT		FPU	Press [Start], [Auto tuning]	
3. Tight Squelch	[Analog Narrow] 1) Test CH : Low, Low', Center, High', High (5 point) 2) SSG output : -116dBm AF Freq : 1kHz Mod Dev : ±1.5kHz	SSG Oscilloscope Audio Analyzer		ANT		FPU	Press [Start], [Auto tuning]	
	[Analog Wide] 1) Test CH : Low, Low', Center, High', High (5 point) 2) SSG output : -117dBm AF Freq : 1kHz Mod Dev : ±3.0kHz	SSG Oscilloscope Audio Analyzer		ANT		FPU	Press [Start], [Auto tuning]	

Item	Condition	Measurement			Adjustment			Specifications /Remarks
		Test-equipment	Unit	Terminal	Unit	Parts	Method	
4. Low RSSI	[Analog Narrow] 1) Test CH : Low, Low', Center, High', High (5 point) 2) SSG output : -120dBm AF Freq : 1kHz Mod Dev : ±1.5kHz	SSG Oscilloscope Audio Analyzer		ANT		FPU	Press [Start], [Auto tuning]	
	[Analog Wide] 1) Test CH : Low, Low', Center, High', High (5 point) 2) SSG output : -120dBm AF Freq : 1kHz Mod Dev : ±3.0kHz	SSG Oscilloscope Audio Analyzer		ANT		FPU	Press [Start], [Auto tuning]	
	[NXDN Very Narrow] 1) Test CH : Low, Low', Center, High', High (5 point) 2) SSG output : -120dBm AF Freq : 1kHz Mod Dev : ±1.5kHz	SSG Oscilloscope Audio Analyzer		ANT		FPU	Press [Start], [Auto tuning]	
	[NXDN Narrow] 1) Test CH : Low, Low', Center, High', High (5 point) 2) SSG output : -120dBm AF Freq : 1kHz Mod Dev : ±1.5kHz	SSG Oscilloscope Audio Analyzer		ANT		FPU	Press [Start], [Auto tuning]	
5. High RSSI	[Analog Narrow] 1) Test CH : Low, Low', Center, High', High (5 point) 2) SSG output : -80dBm AF Freq : 1kHz Mod Dev : ±1.5kHz	SSG Oscilloscope Audio Analyzer		ANT		FPU	Press [Start], [Auto tuning]	
	[Analog Wide] 1) Test CH : Low, Low', Center, High', High (5 point) 2) SSG output : -80dBm AF Freq : 1kHz Mod Dev : ±3.0kHz	SSG Oscilloscope Audio Analyzer		ANT		FPU	Press [Start], [Auto tuning]	
	[NXDN Very Narrow] 1) Test CH : Low, Low', Center, High', High (5 point) 2) SSG output : -80dBm AF Freq : 1kHz Mod Dev : ±1.5kHz	SSG Oscilloscope Audio Analyzer		ANT		FPU	Press [Start], [Auto tuning]	
	[NXDN Narrow] 1) Test CH : Low, Low', Center, High', High (5 point) 2) SSG output : -80dBm AF Freq : 1kHz Mod Dev : ±1.5kHz	SSG Oscilloscope Audio Analyzer		ANT		FPU	Press [Start], [Auto tuning]	

SECTION 5 TROUBLESHOOTING

5.1 Replacing TX-RX Unit

■TX-RX unit Information

Model Name	Original TX-RX unit Number	For Service TX-RX unit Number
NX-840H K	XC1-0010-11	XC2-0130-10
NX-840H K2	XC1-0010-12	XC2-0130-12
NX-840HU K	XC1-0010-11	XC2-0130-11
NX-840 M	XC1-0010-22	XC2-0130-21
NX-840 M2	XC1-0010-23	XC2-0130-22

* Refer to the PRINTED CIRCUIT BOARD [TX-RX UNIT] for type information on PCB.

■Supplied Accessories of "Service TX-RX unit"

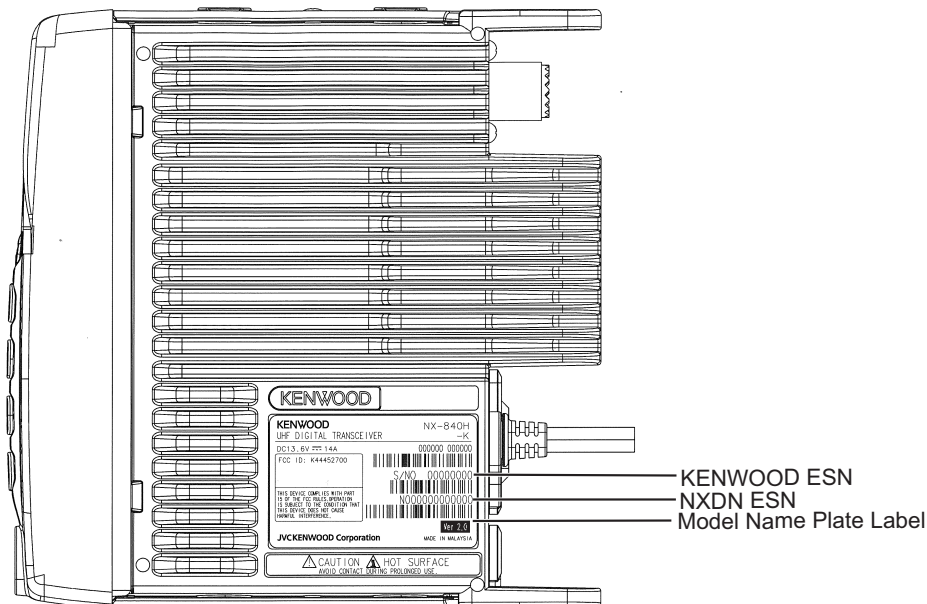
Item (Including Parts Number)	Quantity
TX-RX Unit (XC2-013)	1
Model Name Plate	1

■"Service TX-RX unit" Data

The following data is written on the service TX-RX unit:

Data Type	Description
Firmware	NX-740H/840H Firmware.
FPU Data (PC programming mode)	XC2-013 (NX-840H/840HU) K type data. XC2-013 (NX-840) M type data.
KENWOOD ESN	Model name: NX-840H, NX-840HU or NX-840 Type: K or M The same number as the Model Name Plate label is written.
NXDN ESN	The same number as the Model Name Plate label is written.

■Model Name Plate Label Layout



■After Changing the PCB

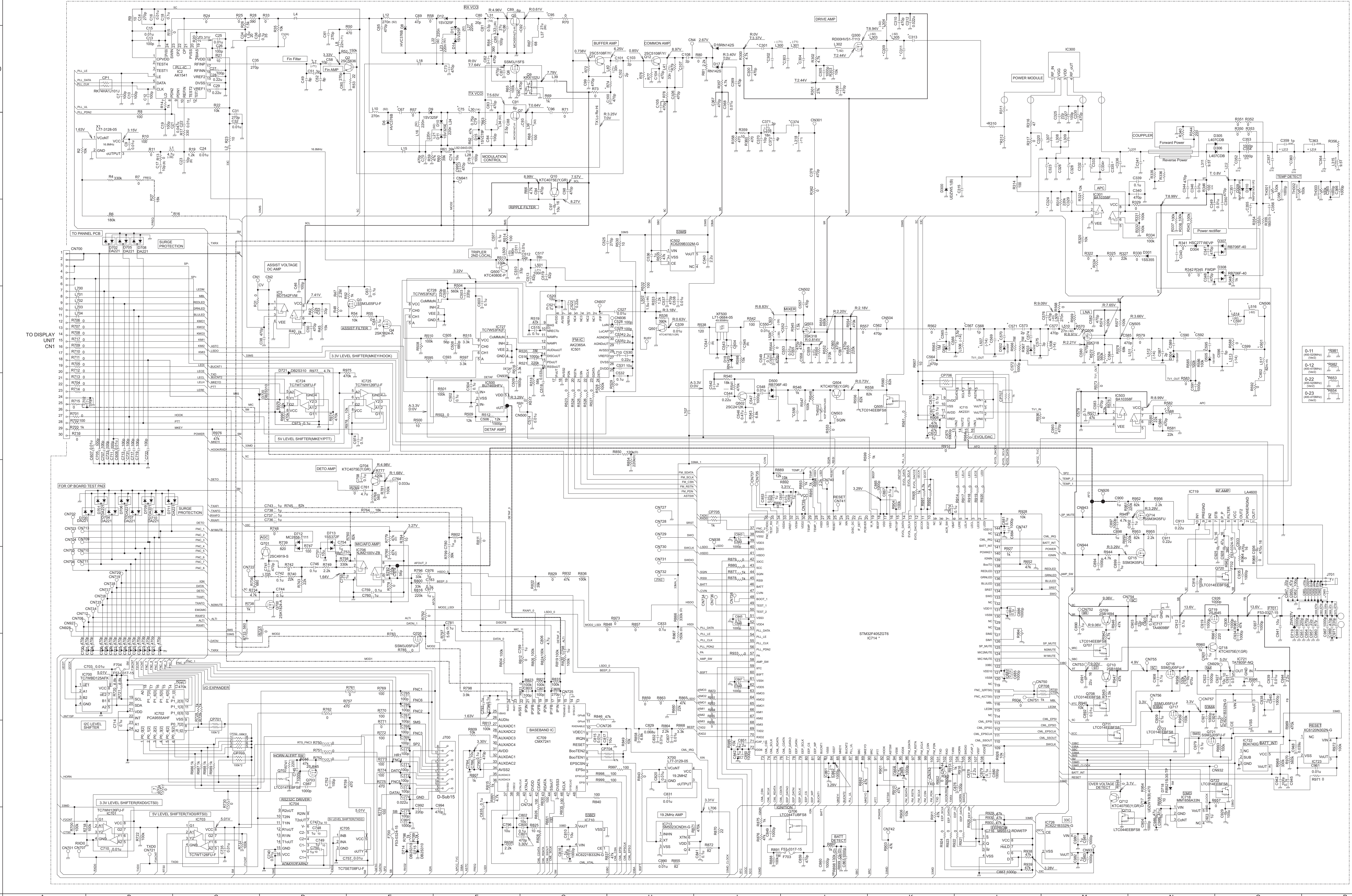
- (1) Using the KPG-175D, select your desired item (Model Name and Frequency) from the Model> Product Information menu, then use Program> Write Data to the Transceiver to write the FPU data (PC Programming mode). When writing to the transceiver, a Warning Message, corresponding to the item selected, appears. Click [OK] to continue writing the data.
- (2) Enter Program> Test Mode, then adjust the various adjustment data (PC Test Mode) as described in the "SECTION 4 ADJUSTMENT"
- (3) Attach the new labels corresponding to the new printed circuit board. (Refer to the images right for label placement.)
- (4) If necessary, write the FPU data used by the customer with the KPG-175D.

Note:

- When a new printed circuit board is used, the KENWOOD ESN changes, as does the Transceiver Information display of the KPG-175D, but this does not have any effect on the operation of the transceiver.
- If changing to the original ESN, please contact our service center.

SCHEMATIC DIAGRAM

■ TX-RX UNIT (XC1-0010-11 (NX-840H(K), NX-840H(U)(K)), XC1-0010-12 (NX-840H(K2)), XC1-0010-22 (NX-840(M)), XC1-0010-23 (NX-840(M2)))

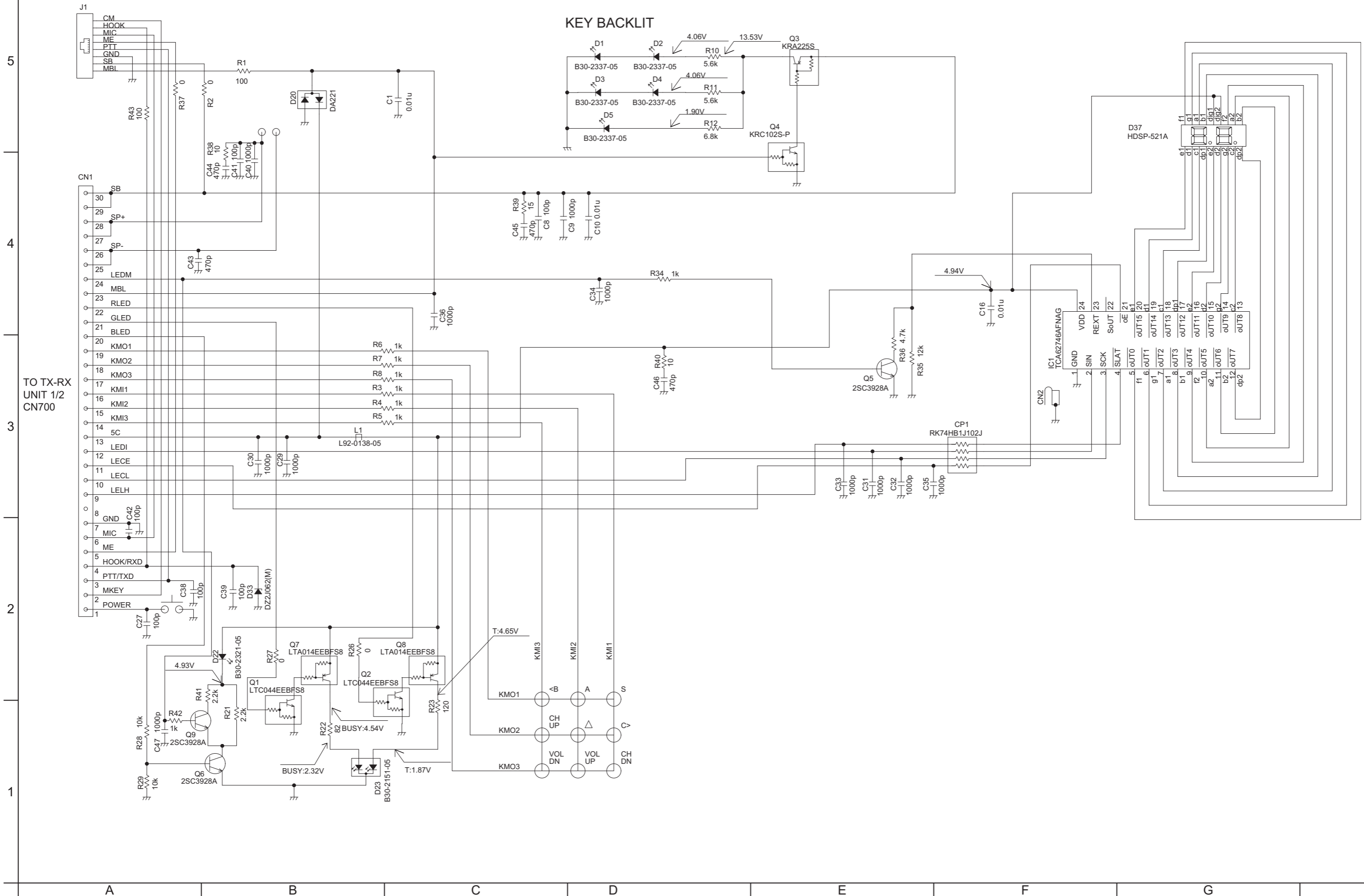


		L7	L30	L31	L34	L36	L300	L301	L302	L303	L304	L305	L318	L503	L504	L506	L507	L508	L511	L512	L513	L514	L516	R16	R63	R72	R74	R76	R77	R78	R79	R310	R311	R312	R335	R336	R340	R508	R548	R550	R569	R570	R572	R595	R653	R664	R783	R881	R882	R994
UHFF(HK) 450-520M (Vw2)	0-11	6.8n	22n	22n	270n	220n	8.2n	4.7n	10n	15n	12n	12n	39n	270n	680n	6.8n	6.8n	6.8n	-	6.8n	6.8n	18n	18n	270k	560	120k	100	68k	100	100	1k	22	22	47	56	-	82	3.3k	270k	390	2.7k	0	120	120k	-	-	33k	0	-	0
UHFF(HK2) 450-520M (Vw2)	0-12	15n	27n	27n	220n	270n	6.8n	6.8n	3.3n	39n	18n	27n	18n	470n	470n	8.2n	8.2n	8.2n	100n	8.2n	8.2n	-	27n	180k	390	100k	150	100k	10	10	390	10	10	120	180	180	100	1.5k	470k	150	1.5k	10	150	39k	-	-	56k	-	0	0
UHFF(M) 450-520M (Vw2)	0-22	6.8n	22n	22n	270n	220n	8.2n	4.7n	10n	15n	12n	12n	39n	270n	680n	6.8n	6.8n	6.8n	-	6.8n	6.8n	18n	18n	270k	560	120k	100	68k	100	100	1k	22	22	47	56	-	82	3.3k	270k	390	2.7k	0	120	120k	0	-	33k	-	-	-
UHFF(M2) 450-520M (Vw2)	0-23	15n	27n	27n	220n	270n	6.8n	6.8n	3.3n	39n	18n	27n	18n	470n	470n	8.2n	8.2n	8.2n	100n	8.2n	8.2n	-	27n	180k	390	100k	150	100k	10	10	390	10	10	120	180	180	100	1.5k	470k	150	1.5k	10	150	39k	-	0	56k	-	-	-

		C67	C75	C86	C92	C93	C95	C96	C101	C107	C301	C302	C303	C304	C309	C311	C313	C315	C320	C323	C324	C326	C328	C329	C332	C333	C334	C335	C336	C341	C343	C351	C352	C355	C356	C357	C360	C363	C364	C374	C541	C543	C545	C552	C556	C565	C567	C569	C570	C571
UHFF(HK) 450-520M (Vw2)	0-11	47p	10p	3.5p	4p	6p	0.3p	0.5p	4p	-	11p	6p	11p	24p	11p	1.5p	270p	100p	470p	-	-	-	-	0.15u	-	-	-	-	3p	-	470p	3.5p	1p	9p	15p	2p	4p	1p	3p	12p	2p	10p	2p	9p	0.22u	15p	0.75p	4.5p	15p	1.5p
UHFF(HK2) 450-520M (Vw2)	0-12	68p	12p	5p	5p	5p	0.5p	0.3p	6p	0.5p	12p	7p	15p	12p	12p	3.5p	47p	470p	-	220p	100p	1000p	100p	0.1u	100p	470p	47p	22p	5p	4p	-	4p	3p	7p	15p	4p	7p	1.5p	4p	4p	5p	13p	0.15u	24p	1p	4p	24p	2p		
UHFF(M) 450-520M (Vw2)	0-22	47p	10p	3.5p	4p	6p	0.3p	0.5p	4p	-	11p	6p	11p	24p	11p	1.5p	270p	100p	470p	-	-	-	-	0.15u	-	-	-	3p	-	470p	3.5p	1p	9p	15p	2p	4p	1p	3p	12p	2p	10p	2p	9p	0.22u	15p	0.75p	4.5p	15p	1.5p	
UHFF(M2) 450-520M (Vw2)	0-23	68p	12p	5p	5p	5p	0.5p	0.3p	6p	0.5p	12p	7p	15p	12p	12p	3.5p	47p	470p	-	220p	100p	1000p	100p	0.1u	100p	470p	47p	22p	5p	4p	-	4p	3p	7p	15p	4p	7p	1.5p	4p	4p	5p	13p	0.15u	24p	1p	4p	24p	2p		

		C574	C584	C588	C589	C590	C592	C593	C595	C597	C599	C601	C602	C604
UHFF(HK) 450-520M (Vw2)	0-11	15p	-	-	15p	3.5p	3.5p	100p	15p	-	30p	4p	1.5p	10p
UHFF(HK2) 450-520M (Vw2)	0-12	24p	470p	470p	24p	4p	4.5p	56p	24p	56p	20p	8p	2p	9p
UHFF(M) 450-520M (Vw2)	0-22	15p	-	-	15p	3.5p	3.5p	100p	15p	-	30p	4p	1.5p	10p
UHFF(M2) 450-520M (Vw2)	0-23	24p	470p	470p	24p	4p	4.5p	56p	24p	56p	20p	8p	2p	9p

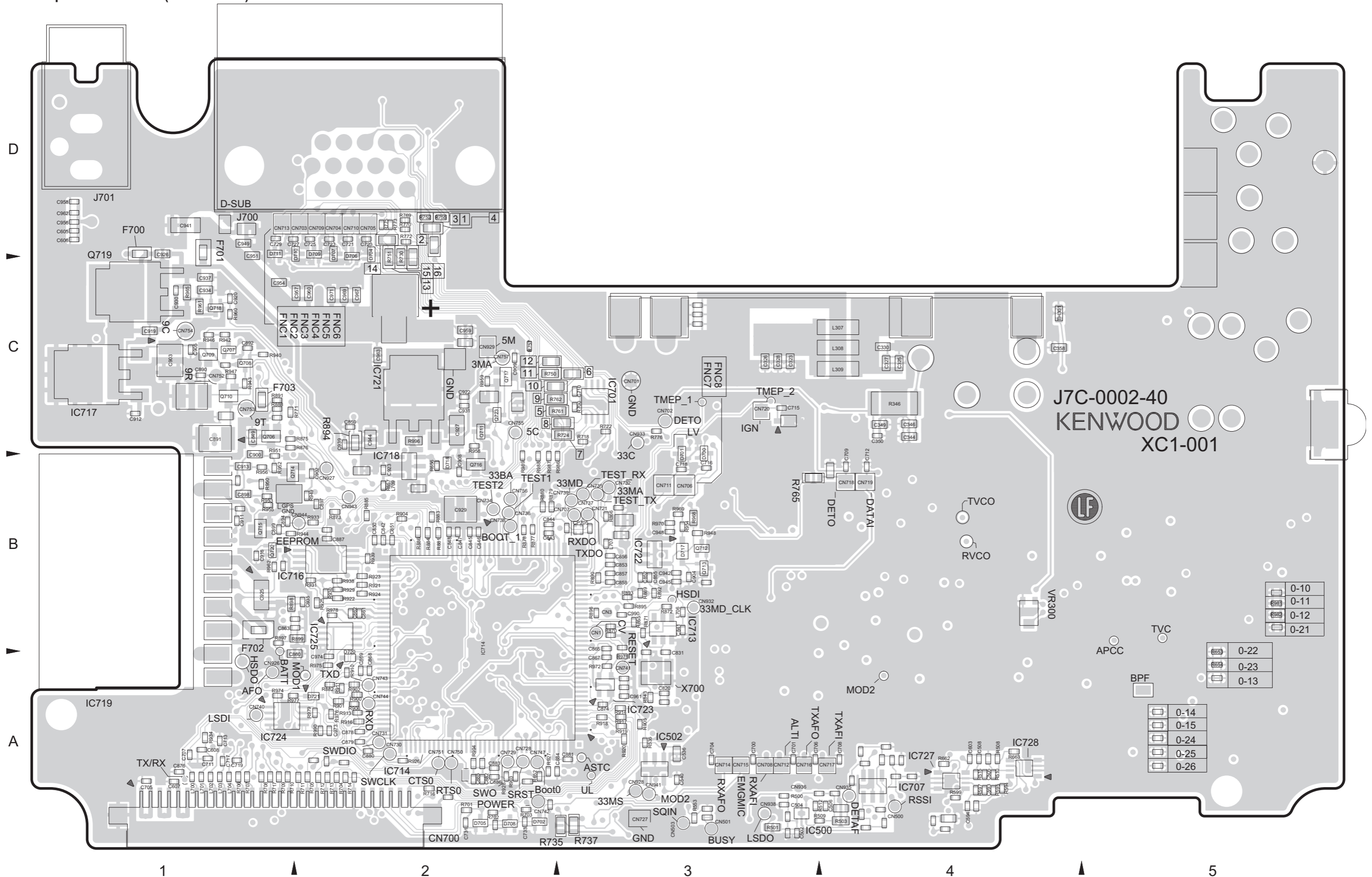
■ DISPLAY UNIT (X54-3890-20)



PRINTED CIRCUIT BOARD

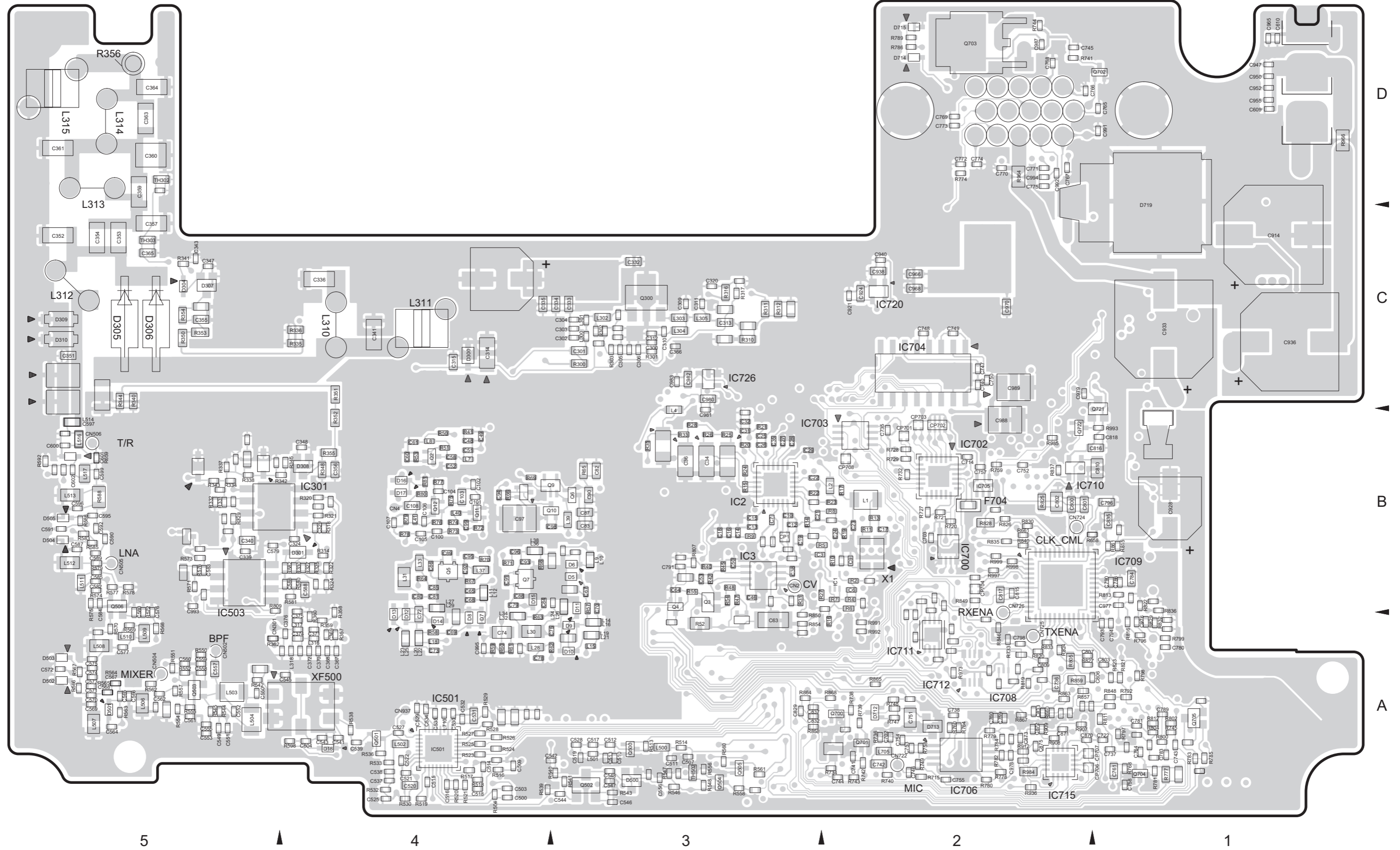
■ TX-RX UNIT (XC1-0010-11 (NX-840H(K), NX-840H(U)(K)), XC1-0010-12 (NX-840H(K2)), XC1-0010-22 (NX-840(M)), XC1-0010-23 (NX-840(M2)))

--- Component side view (J7C-0002-40) ---



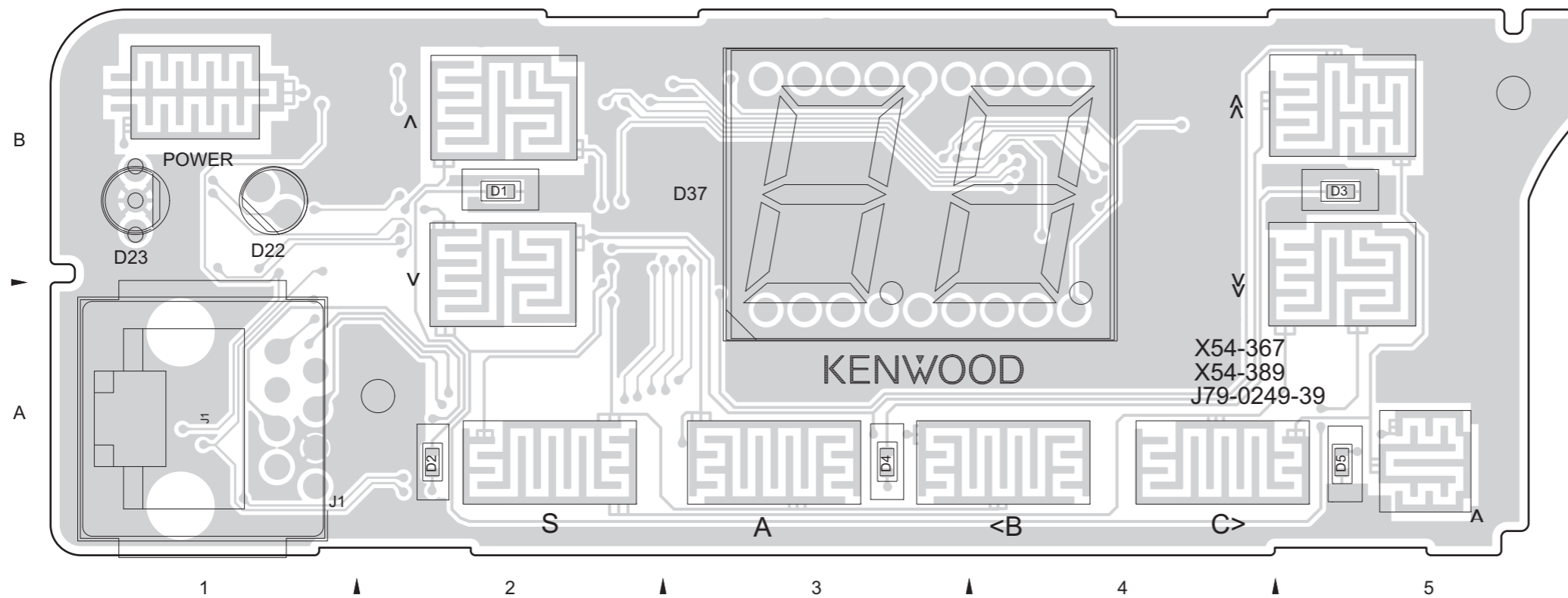
■ TX-RX UNIT (XC1-0010-11 (NX-840H(K), NX-840H(U)(K)), XC1-0010-12 (NX-840H(K2)), XC1-0010-22 (NX-840(M)), XC1-0010-23 (NX-840(M2)))

--- Foil side view (J7C-0002-40) ---



■ DISPLAY UNIT (X54-3890-20)

--- Component side view (J79-0249-39) ---



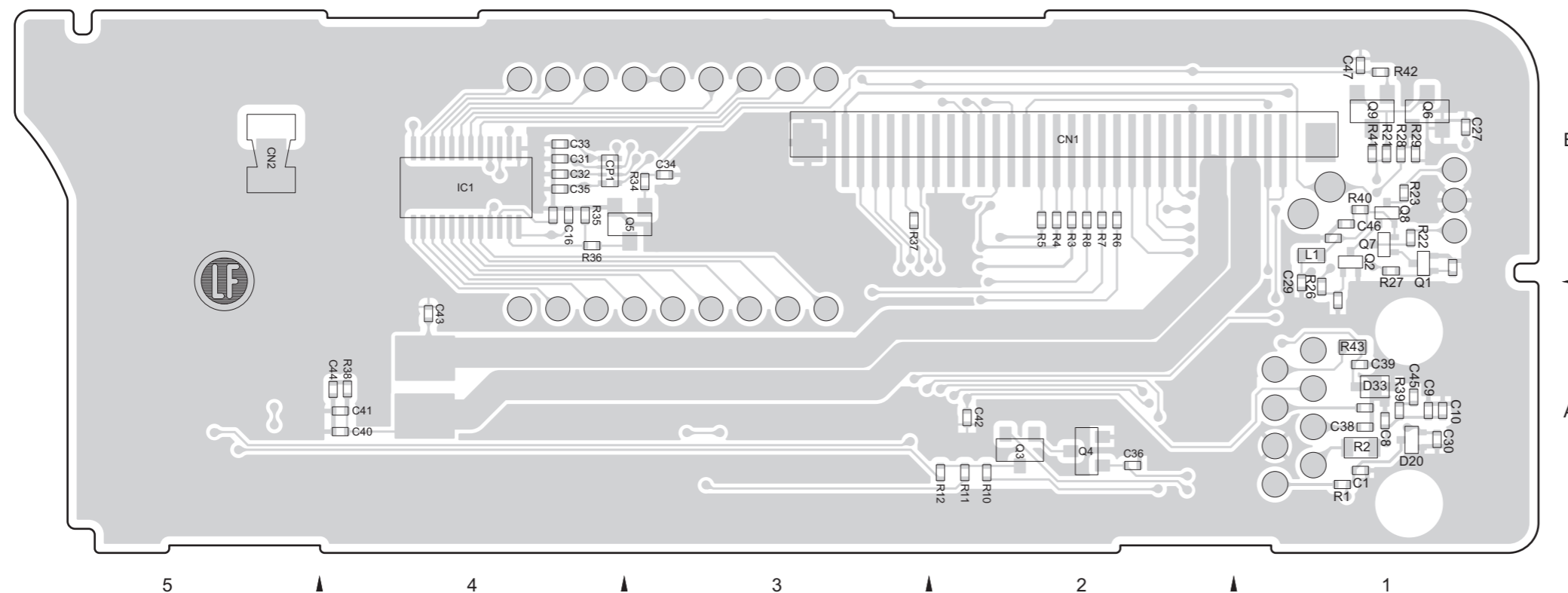
● ADDRESS TABLE OF BOARD PARTS

Each address may have an address error by one interval.



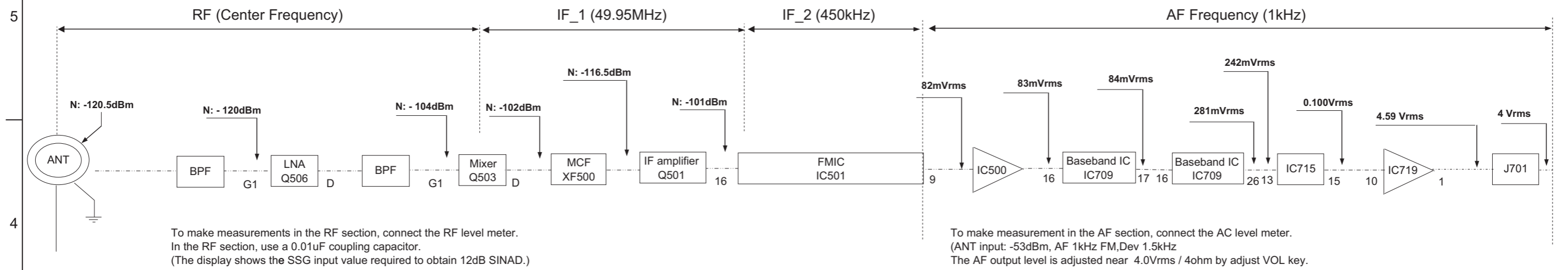
REF.NO.	LOCATION	REF.NO.	LOCATION	REF.NO.	LOCATION	REF.NO.	LOCATION
IC		RESISTOR		R41	B-1B	C45	B-1A
IC1	B-4B	R1	B-1A	R42	B-1B	C46	B-1B
		R2	B-1A	R43	B-1A	C47	B-1B
TRANSISTOR		R3	B-2B			CAPACITOR	
Q1	B-1B	R4	B-2B			C1	B-1A
Q2	B-1B	R5	B-2B			C8	B-1A
Q3	B-2A	R6	B-2B			C9	B-1A
Q4	B-2A	R7	B-2B			C10	B-1A
Q5	B-3B	R8	B-2B			C16	B-4B
Q6	B-1B	R10	B-2A			C27	B-1B
Q7	B-1B	R11	B-2A			C29	B-1A
Q8	B-1B	R12	B-2A			C30	B-1A
Q9	B-1B	R21	B-1B			C31	B-4B
		R22	B-1B			C32	B-4B
DIODE		R23	B-1B			C33	B-4B
D1	A-2B	R26	B-1A			C34	B-3B
D2	A-2A	R27	B-1B			C35	B-4B
D3	A-5B	R28	B-1B			C36	B-2A
D4	A-3A	R29	B-1B			C38	B-1A
D5	A-5A	R34	B-3B			C39	B-1A
D20	B-1A	R35	B-4B			C40	B-4A
D22	A-1B	R36	B-4B			C41	B-4A
D23	A-1B	R37	B-3B			C42	B-2A
D33	B-1A	R38	B-4A			C43	B-4A
D37	A-3B	R39	B-1A			C44	B-4A
		R40	B-1B				
						OTHER	
						CN1	B-2B
						CN2	B-5B
						CP1	B-4B
						J1	A-1A
						L1	B-1B

--- Foil side view (J79-0249-39) ---

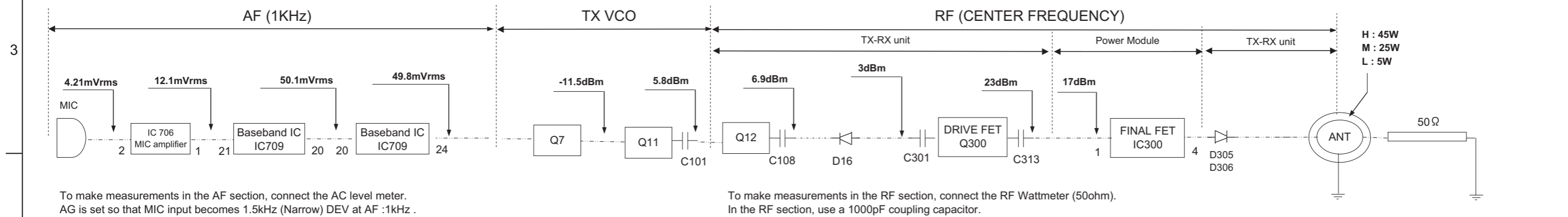


LEVEL DIAGRAM

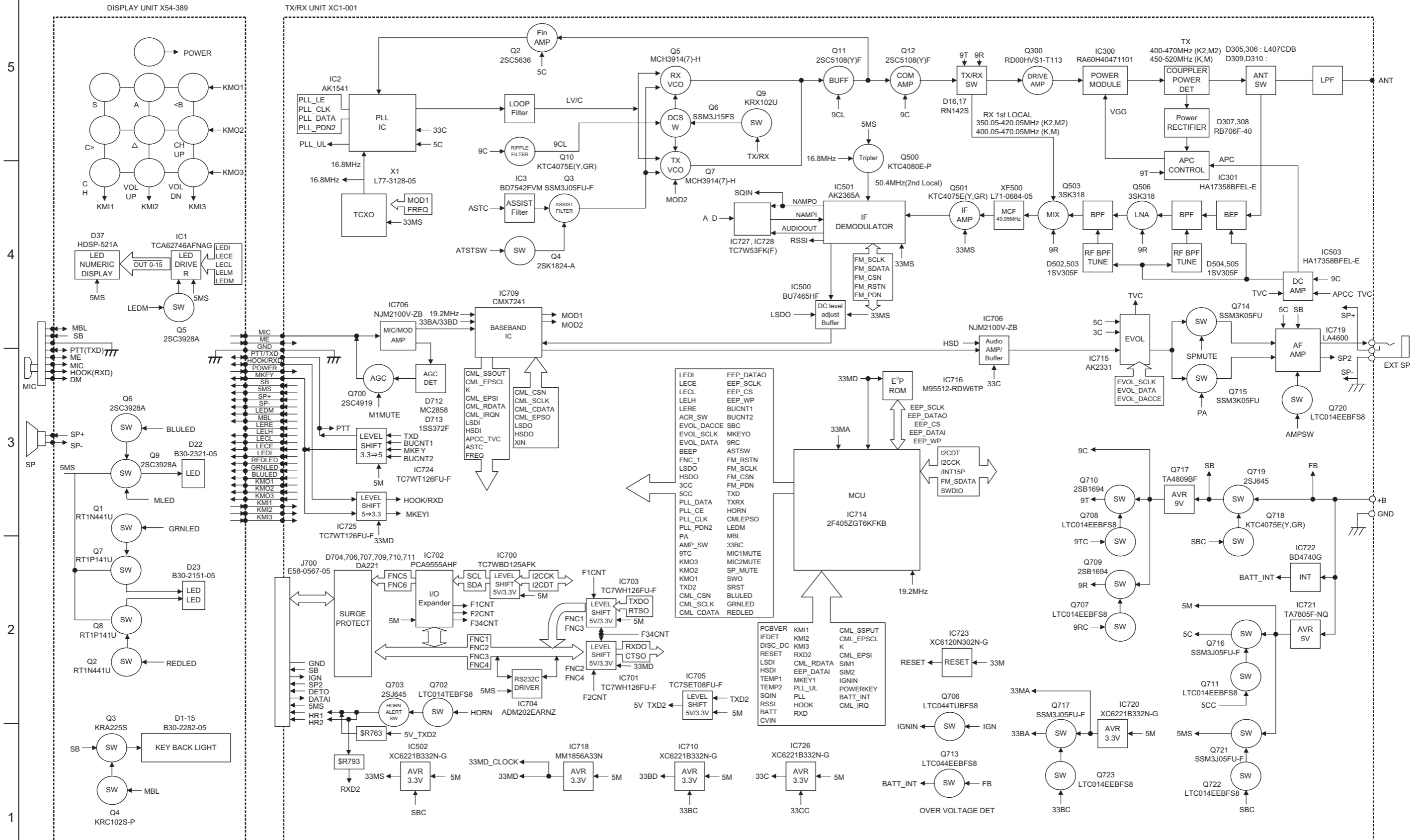
Receiver Section



Transmitter Section



BLOCK DIAGRAM



MEMO

PARTS LIST

[NX-840H,NX-840H(U),NX-840]

* SAFETY PRECAUTION

Parts identified by the \triangle symbol are critical for safety. Replace only with specified part numbers.

* BEWARE OF BOGUS PARTS

Parts that do not meet specifications may cause trouble in regard to safety and performance. We recommend that genuine parts be used.

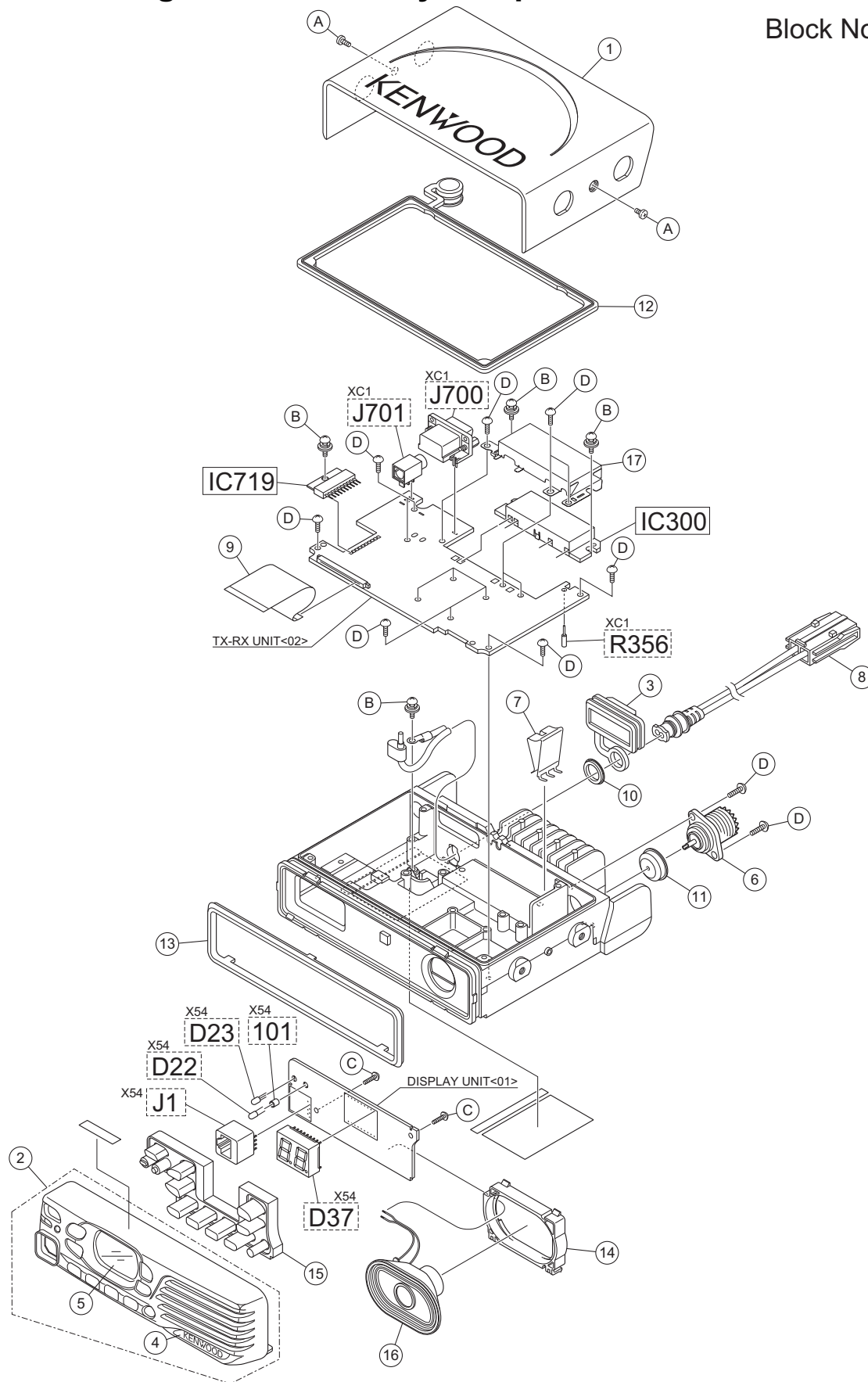
* (x_) in a description column shows the number of the used part.

- Contents -

Exploded view of general assembly and parts list	3-2
Electrical parts list	3-4
Packing materials and accessories parts list	3-13

Exploded view of general assembly and parts list

Block No.M1MM



General assembly

Block No. [M][1][M][M]

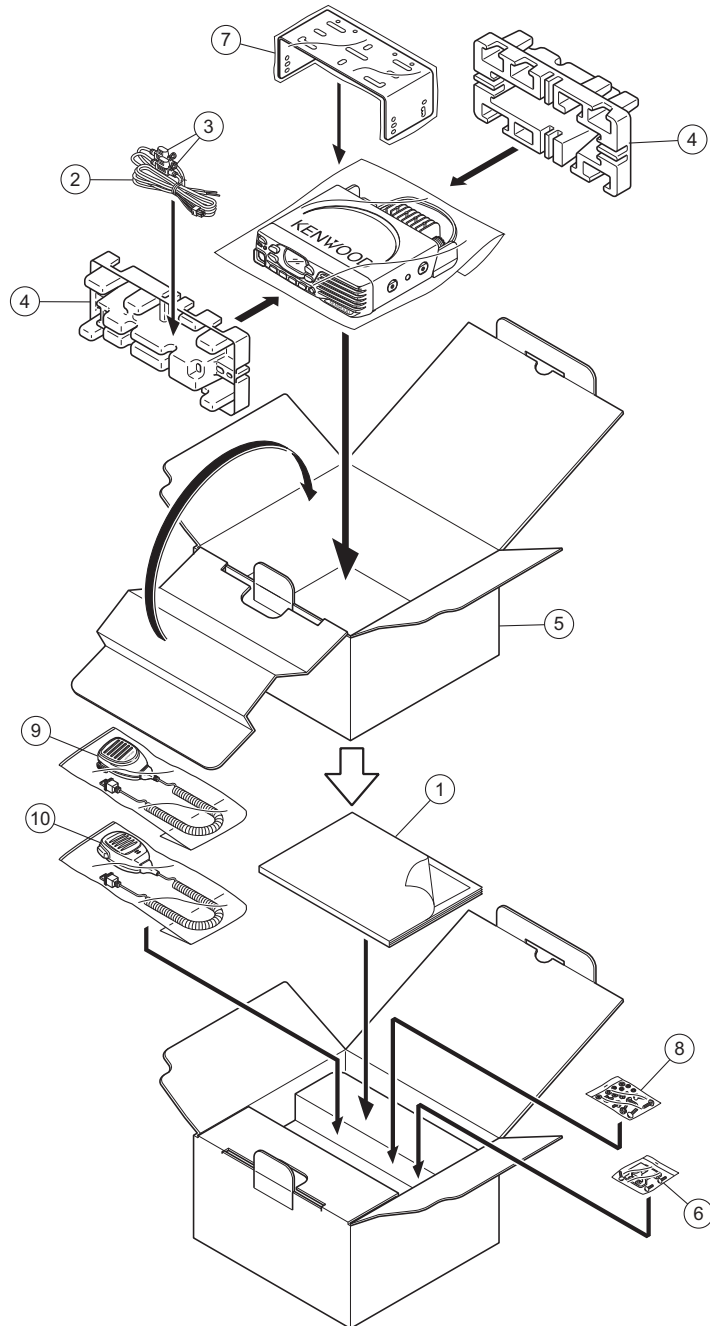
△ Symbol No.	Part No.	Part Name	Description	Local
1	A02-4073-31	PLASTIC CABINET		
2	A6A-0004-00	PANEL ASSEMBLY		
3	B09-0732-03	CAP(D-SUB)		
4	B4D-0003-00	BADGE		
5	B10-2794-03	FRONT GLASS		
6	E04-0167-15	RF COAXIAL RECEPTACLE(M)		
7	E23-1346-04	GROUND TERMINAL		
8	E30-7684-25	DC CORD		
9	E37-1461-05	FLAT CABLE(30P PANEL)		
10	G53-1643-04	PACKING(DC CORD)		
11	G53-1662-04	PACKING(RECEPTACLE)		
12	G53-1819-21	PACKING(CHASSIS)		
13	G53-1820-03	PACKING (PANEL)		
14	J19-5528-03	HOLDER(SPEAKER)		
15	K29-9448-01	KEY TOP		
16	T07-0785-15	SPEAKER		
17	F10-3140-13	SHIELDING COVER		
A	N35-2604-43	BINDING HEAD MACHINE SCREW	(x2)	
B	N67-3008-48	PAN HEAD SEMS SCREW	(x4)	
C	N80-2008-48	PAN HEAD TAPTITE SCREW	(x2)	
D	N87-2608-48	BRAZIER HEAD TAPTITE SCREW	(x14)	
-	XC2-0130-10	SERVICE TX-RX UNIT		840H_K
-	XC2-0130-11	SERVICE TX-RX UNIT		840H_U_K
-	XC2-0130-21	SERVICE TX-RX UNIT		840_M
-	XC2-0130-12	SERVICE TX-RX UNIT		840H_K2
-	XC2-0130-22	SERVICE TX-RX UNIT		840_M2
-	X54-3890-20	DISPLAY UNIT		

△ Symbol No.	Part No.	Part Name	Description	Local	△ Symbol No.	Part No.	Part Name	Description	Local
Q300	RD00HVS1-T113	FET			D720	DB2S310	SB DIODE		
Q500	KTC4080E-P	TRANSISTOR			D721	DB2S310	SB DIODE		
Q501	KTC4075E(Y,GR)	TRANSISTOR							
Q502	2SC2412K	TRANSISTOR			C7	CC73HCH1H101J	C CAPACITOR	100PF J	
Q503	3SK318	FET			C9	CK73HBB1E103K	C CAPACITOR	0.01UF K	
Q504	KTC4075E(Y,GR)	TRANSISTOR			C10	CC73HCH1H101J	C CAPACITOR	100PF J	
Q505	LTC014EEBFS8	DIGI TRANSISTOR			C12	CC73HCH1H101J	C CAPACITOR	100PF J	
Q506	3SK318	FET			C13	CC73HCH1H101J	C CAPACITOR	100PF J	
Q700	2SK1824-A	FET			C14	CK73HB1H271K	C CAPACITOR	270PF K	
Q701	2SC4919-S	TRANSISTOR			C15	CK73HB1H103K	C CAPACITOR	0.010UF K	
Q702	LTC014TEBFS8	DIGI TRANSISTOR			C16	CK73HBB1E103K	C CAPACITOR	0.01UF K	
Q703	2SJ645	FET			C17	CC73HCH1H100B	C CAPACITOR	10PF B	
Q704	KTC4075E(Y,GR)	TRANSISTOR			C18	CK73HBB1A104K	C CAPACITOR	0.10UF K	
Q705	SSM3J05FU-F	FET			C19	CC73HCH1H101J	C CAPACITOR	100PF J	
Q706	LTC044TUBFS8	DIGI TRANSISTOR			C21	CK73HB1A473K	C CAPACITOR	0.047UF K	
Q707	LTC014EEBFS8	DIGI TRANSISTOR			C22	CK73HBB1E103K	C CAPACITOR	0.01UF K	
Q708	LTC014EEBFS8	DIGI TRANSISTOR			C23	CC73HCH1H100B	C CAPACITOR	10PF B	
Q709	2SB1694	TRANSISTOR			C24	CK73HBB1E103K	C CAPACITOR	0.01UF K	
Q710	2SB1694	TRANSISTOR			C25	CK73HBB1E103K	C CAPACITOR	0.01UF K	
Q711	LTC014EEBFS8	DIGI TRANSISTOR			C26	CC73HCH1H101J	C CAPACITOR	100PF J	
Q712	KTC4075E(Y,GR)	TRANSISTOR			C27	CC73HCH1H101J	C CAPACITOR	100PF J	
Q713	LTC044EEBFS8	DIGI TRANSISTOR			C28	CK73HB1A224K	C CAPACITOR	0.22UF K	
Q714	SSM3K05FU	FET			C29	CK73HB1A224K	C CAPACITOR	0.22UF K	
Q715	SSM3K05FU	FET			C31	CK73HB1H271K	C CAPACITOR	270PF K	
Q716	SSM3J05FU-F	FET			C32	CK73HBB1E103K	C CAPACITOR	0.01UF K	
Q717	SSM3J05FU-F	FET			C34	CS77MA1D1R5M	TA E CAPACITOR	0.15UF 20WV	
Q718	KTC4075E(Y,GR)	TRANSISTOR			C35	CK73HB1H271K	C CAPACITOR	270PF K	
Q719	2SJ645	FET			C36	CS77MA1V0R1M	TA E CAPACITOR	0.1UF 35WV	
Q720	LTC014EEBFS8	DIGI TRANSISTOR			C38	CK73HBB1H471K	C CAPACITOR	470PF K	
Q721	SSM3J05FU-F	FET			C46	CC73HCH1H101J	C CAPACITOR	100PF J	
Q722	LTC014EEBFS8	DIGI TRANSISTOR			C49	CC73HCH1H080B	C CAPACITOR	8.0PF B	
Q723	LTC014EEBFS8	DIGI TRANSISTOR			C50	CK73HBB1H471K	C CAPACITOR	470PF K	
D6	1SV325F	VARIABLE CAPACITANCE DIODE			C51	CC73HCH1H030B	C CAPACITOR	3.0PF B	
D8	1SV325F	VARIABLE CAPACITANCE DIODE			C52	CC73HCH1H080B	C CAPACITOR	8.0PF B	
D9	1SV325F	VARIABLE CAPACITANCE DIODE			C53	CC73HCH1H330J	C CAPACITOR	33PF J	
D10	1SV325F	VARIABLE CAPACITANCE DIODE			C58	CC73HCH1H100B	C CAPACITOR	10PF B	
D11	1SV325F	VARIABLE CAPACITANCE DIODE			C60	CK73HB1H271K	C CAPACITOR	270PF K	
D12	1SV325F	VARIABLE CAPACITANCE DIODE			C61	CK73HB1H271K	C CAPACITOR	270PF K	
D13	1SV325F	VARIABLE CAPACITANCE DIODE			C63	CC730AD1H104J	C CAPACITOR	0.1UF J	
D14	1SV325F	VARIABLE CAPACITANCE DIODE			C64	CK73HBB1A104K	C CAPACITOR	0.10UF K	
D15	1SV278F	VARIABLE CAPACITANCE DIODE			C65	CK73HBB1H471K	C CAPACITOR	470PF K	
D16	RN142S	DIODE			C67	CC73HCH1H470J	C CAPACITOR	47PF J	840H_K,840HU_K,840_M
D17	RN142S	DIODE			C69	CC73HCH1H680J	C CAPACITOR	68PF J	840H_K2,840_M2
D18	RN142S	DIODE			C69	CC73HCH1H470J	C CAPACITOR	47PF J	
D300	UDZW5.1(B)	ZENER DIODE			C70	CK73HBB1H471K	C CAPACITOR	470PF K	
D301	1SS355	DIODE			C73	CK73HBB1H471K	C CAPACITOR	470PF K	
D304	HSC277	DIODE			C74	CK73FB0J106K	C CAPACITOR	10UF K	
D305	L407CDB	DIODE	50V		C75	CC73HCH1H100C	C CAPACITOR	10PF C	840H_K,840HU_K,840_M
D306	L407CDB	DIODE	50V		C75	CC73HCH1H120J	C CAPACITOR	12PF J	840H_K2,840_M2
D307	RB706F-40	DIODE			C78	CC73HCH1H101J	C CAPACITOR	100PF J	
D308	RB706F-40	DIODE			C79	CC73HCH1HR75B	C CAPACITOR	0.75PF B	
D309	RKP351KW-1P2	DIODE			C80	CC73HCH1H200J	C CAPACITOR	20PF J	
D310	RKP351KW-1P2	DIODE			C81	CC73HCH1H0R5B	C CAPACITOR	0.5PF B	
D500	RB706F-40	DIODE			C82	CK73GB1H471K	C CAPACITOR	470PF K	
D501	1SV305F	VARIABLE CAPACITANCE DIODE			C83	CK73GB1H471K	C CAPACITOR	470PF K	
D502	1SV305F	VARIABLE CAPACITANCE DIODE			C84	CC73HCH1H101J	C CAPACITOR	100PF J	
D503	1SV305F	VARIABLE CAPACITANCE DIODE			C85	CC73HCH1H101J	C CAPACITOR	100PF J	
D504	1SV305F	VARIABLE CAPACITANCE DIODE			C86	CC73HCH1H3R5B	C CAPACITOR	3.5PF B	840H_K,840HU_K,840_M
D505	1SV305F	VARIABLE CAPACITANCE DIODE			C86	CC73HCH1H050B	C CAPACITOR	5.0PF B	840H_K2,840_M2
D700	DA221	MULTIPLE DIODE			C87	CK73GB1H471K	C CAPACITOR	470PF K	
D701	DA221	MULTIPLE DIODE			C88	CC73HCH1H2R5B	C CAPACITOR	2.5PF B	
D702	DA221	MULTIPLE DIODE			C89	CC73HCH1H060B	C CAPACITOR	6.0PF B	
D704	DA221	MULTIPLE DIODE			C90	CK73GB1H471K	C CAPACITOR	470PF K	
D705	DA221	MULTIPLE DIODE			C91	CC73HCH1H080B	C CAPACITOR	8.0PF B	
D706	DA221	MULTIPLE DIODE			C92	CC73HCH1H040B	C CAPACITOR	4.0PF B	840H_K,840HU_K,840_M
D707	DA221	MULTIPLE DIODE			C92	CC73HCH1H050B	C CAPACITOR	5.0PF B	840H_K2,840_M2
D708	DA221	MULTIPLE DIODE			C93	CC73HCH1H060B	C CAPACITOR	6.0PF B	840H_K,840HU_K,840_M
D709	DA221	MULTIPLE DIODE			C93	CC73HCH1H050B	C CAPACITOR	5.0PF B	840H_K2,840_M2
D710	DA221	MULTIPLE DIODE			C94	CK73HBB1H471K	C CAPACITOR	470PF K	
D711	DA221	MULTIPLE DIODE			C95	CC73HCH1H0R3B	C CAPACITOR	0.3PF B	840H_K,840HU_K,840_M
D712	MC2858-T111	DIODE			C95	CC73HCH1H0R5B	C CAPACITOR	0.5PF B	840H_K2,840_M2
D713	1SS372F	SB DIODE			C96	CC73HCH1H0R5B	C CAPACITOR	0.5PF B	840H_K,840HU_K,840_M
D714	DB2S310	SB DIODE			C96	CC73HCH1H0R3B	C CAPACITOR	0.3PF B	840H_K2,840_M2
D715	DB2S310	SB DIODE			C97	CS77BB21C100M	TA E CAPACITOR	10UF 16WV	
D717	UDZW18(B)	ZENER DIODE			C98	CK73HBB1H471K	C CAPACITOR	470PF K	
D718	RB521S-30-TP	DIODE			C99	CK73HBB1H471K	C CAPACITOR	470PF K	
D719	Z5W27V	SURGE ABSORBER			C100	CK73HBB1H471K	C CAPACITOR	470PF K	
					C101	CC73HCH1H040B	C CAPACITOR	4.0PF B	840H_K,840HU_K,840_M

Symbol No.	Part No.	Part Name	Description	Local
L318	LK73H0AM39NJ	M.CHIP INDUCTOR	39NH	840H_K,840HU_K,840_M
L318	LK73H0AM18NJ	M.CHIP INDUCTOR	18NH	840H_K2,840_M2
L500	L41-1885-53	M.CHIP INDUCTOR	0.18UH	
L501	L40-1085-71	M.CHIP INDUCTOR	100NH	
L502	L40-1591-86	M.CHIP INDUCTOR	1.5UH	
L503	LR7720AER27J	M.CHIP INDUCTOR	270NH	840H_K,840HU_K,840_M
L503	L41-4785-39	M.CHIP INDUCTOR	0.47UH	840H_K2,840_M2
L504	L41-6885-39	M.CHIP INDUCTOR	0.68UH	840H_K,840HU_K,840_M
L504	L41-4785-39	M.CHIP INDUCTOR	0.47UH	840H_K2,840_M2
L506	L41-6868-14	M.CHIP INDUCTOR	6.8NH	840H_K,840HU_K,840_M
L506	L41-8268-14	M.CHIP INDUCTOR	8.2NH	840H_K2,840_M2
L507	L41-6868-14	M.CHIP INDUCTOR	6.8NH	840H_K,840HU_K,840_M
L507	L41-8268-14	M.CHIP INDUCTOR	8.2NH	840H_K2,840_M2
L508	L41-6868-14	M.CHIP INDUCTOR	6.8NH	840H_K,840HU_K,840_M
L508	L41-8268-14	M.CHIP INDUCTOR	8.2NH	840H_K2,840_M2
L509	L41-2788-45	M.CHIP INDUCTOR	270NH	
L510	LK73G0AF27NJ	M.CHIP INDUCTOR	27NH	
L511	LK73G0AFR10J	M.CHIP INDUCTOR	100NH	840H_K2,840_M2
L512	L41-6868-14	M.CHIP INDUCTOR	6.8NH	840H_K,840HU_K,840_M
L512	L41-8268-14	M.CHIP INDUCTOR	8.2NH	840H_K2,840_M2
L513	L41-6868-14	M.CHIP INDUCTOR	6.8NH	840H_K,840HU_K,840_M
L513	L41-8268-14	M.CHIP INDUCTOR	8.2NH	840H_K2,840_M2
L514	LK73G0AF18NJ	M.CHIP INDUCTOR	18NH	840H_K,840HU_K,840_M
L516	LK73G0AF18NJ	M.CHIP INDUCTOR	18NH	840H_K,840HU_K,840_M
L516	LK73G0AF27NJ	M.CHIP INDUCTOR	27NH	840H_K2,840_M2
L517	L41-8275-45	M.CHIP INDUCTOR	82NH	
L700	LB73H0AV-003	CHIP FERRITE BEADS		
L701	LB73H0AV-003	CHIP FERRITE BEADS		
L702	LB73H0AV-003	CHIP FERRITE BEADS		
L703	LB73H0AV-003	CHIP FERRITE BEADS		
L704	LB73H0AV-003	CHIP FERRITE BEADS		
L705	L92-0443-05	CHIP FERRITE		
L706	LB73H0AV-003	CHIP FERRITE BEADS		
L707	LB73H0AV-003	CHIP FERRITE BEADS		
L710	L92-0161-05	BEADS CORE		
CN700	E40-6847-05	FLAT CABLE CONNECTOR	30PIN	
CP1	RK74HA1J101J	NET RESISTOR	100 J 1/16W	
CP701	RK74HA1J104J	NET RESISTOR	100K J 1/16W	
CP702	RK74HB1J104J	NET RESISTOR	100K J 1/16W	
CP703	RK74HA1J104J	NET RESISTOR	100K J 1/16W	
CP704	RK75HA1J473J	NET RESISTOR	47K J 1/16W	
CP705	RK74HA1J102J	NET RESISTOR	1.0K J 1/16W	
CP706	RK75HA1J473J	NET RESISTOR	47K J 1/16W	
CP707	RK74HA1J102J	NET RESISTOR	1.0K J 1/16W	
CP708	RK74HA1J102J	NET RESISTOR	1.0K J 1/16W	
F700	F53-0324-15	FUSE	2.5A	
F701	F53-0327-15	FUSE	4A	
F702	F53-0324-15	FUSE	2.5A	
F703	F53-0317-15	FUSE	500MA	
F704	F53-0317-15	FUSE	500MA	
J700	E58-0567-05	SUB SOCKET(D)		
J701	E1B-0001-00	3.5D PHONE JACK		
TH301	B57331V2104J	THERMISTOR		
TH302	B57331V2104J	THERMISTOR		
TH303	B57331V2104J	THERMISTOR		
TH502	NCP18WM474J0S	NEGATIVE TEMP THERMISTOR		
X1	L77-3128-05	TCXO	16.8MHZ	
X700	L77-3129-05	TCXO	19.2MHZ	
XF500	L71-0684-05	MCF	49.95MHZ	

Packing materials and accessories parts list

Block No.M2MM



Packing and accessories

Block No. [M][2][M][M]
Local

△ Symbol No.	Part No.	Part Name	Description	Block No. [M][2][M][M] Local
1	B5A-0031-20	INSTRUCTION MANUAL		840H_K,840HU_K,840H_K2
1	B5A-0030-10	INSTRUCTION MANUAL		840_M,840_M2
1	B5A-0747-00	INSTRUCTION MANUAL		840H_K2
2	E30-7523-65	DC CORD ASSEMBLY		
3	F52-0024-05	FUSE(BLADE TYPE)	15A(x2)	840H_K,840HU_K,840H_K2
3	F52-0023-05	FUSE(BLADE TYPE)	10A(x2)	840_M,840_M2
4	H12-3178-05	PACKING FIXTURE	(x2)	
5	H52-2674-22	ITEM CARTON CASE		
6	J19-1584-15	HOLDER(MIC HANGER)		840H_K,840HU_K,840H_K2
7	J29-0726-03	BRACKET		
8	N99-2039-05	SCREW SET		
9	T91-0624-65	MICROPHONE(KMC-30)		840H_K,840H_K2
10	T91-0639-65	MICROPHONE(KMC-35)		840HU_K



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