

### **Document Copyrights**

Copyright 2006 by Kenwood Corporation. All rights reserved.

No part of this manual may be reproduced, translated, distributed, or transmitted in any form or by any means, electronic, mechanical, photocopying, recording, or otherwise, for any purpose without the prior written permission of Kenwood.

### **Disclaimer**

While every precaution has been taken in the preparation of this manual, Kenwood assumes no responsibility for errors or omissions. Neither is any liability assumed for damages resulting from the use of the information contained herein. Kenwood reserves the right to make changes to any products herein at any time for improvement purposes.

# TK-390 SERVICE MANUAL

# KENWOOD

© 1998-3 PRINTED IN JAPAN B51-8424-00 (N) 1349



Photo is TK-390 K4 type. Does not come with antenna. Antenna is available as an option.

### **CONTENTS** -

GENERAL	2
SYSTEM SET-UP	2
OPERATING FEATURES	3
REALIGNMENT 1	0
CIRCUIT DESCRIPTION 1	16
SEMICONDUCTOR DATA 2	23
DESCRIPTION OF COMPONENTS 2	26
PARTS LIST 2	28
EXPLODED VIEW 3	
PACKING 3	
DISASSEMBLY FOR REPAIR 3	37
ADJUSTMENT 3	38
PC BOARD VIEWS	
CONTROL UNIT (X53-3780-XX) 4	19
TX-RX UNIT (X57-5400-10) 5	
SCHEMATIC DIAGRAM 5	59
BLOCK DIAGRAM 6	7
LEVEL DIAGRAM 6	
TERMINAL FUNCTION 7	
KNB-17A (Ni-Cd BATTERY) 7	
KMC-25/26 (SPEAKER MICROPHONE) 7	
KSC-19 (CHARGER) 7	
KSC-20 (RAPID CHARGER) 7	
KPG-36 (PROGRAMMING INTERFACE CABLE) 7	
KRA-14 (ANTENNA) 7	
SDECIFICATIONS BACK COVE	D

#### **CAUTION**

When using an external power connector, please use with maximum final module protection of 10V

### **GENERAL / SYSTEM SET-UP**

# INTRODUCTION SCOPE OF THIS MANUAL

This manual is intended for use by experienced technicians familiar with similar types of commercial grade communications equipment. It contains all required service information for the equipment and is current as of the publication data. Changes which may occur after publication are covered by either Service Bulletins or Manual Revisions. These are issued as required.

#### ORDERING REPLACEMENT PARTS

When ordering replacement parts or equipment information, the full part identification number should be included. This applies to all parts: components, kits, or chassis. If the part number is not known, include the chassis or kit number of which it is a part, and a sufficient description of the required component for proper identification.

#### PERSONNEL SAFETY

The following precautions are recommended for personnel safety:

- DO NOT transmit until all RF connectors are verified secure and any open connectors are properly terminated.
- SHUT OFF and DO NOT operate this equipment near electrical blasting caps or in an explosive atmosphere.
- This equipment should be serviced by a qualified technician only.

#### **SERVICE**

This radio is designed for easy servicing. Refer to the schematic diagrams, printed circuit board views, and alignment procedures contained within.

#### NOTE

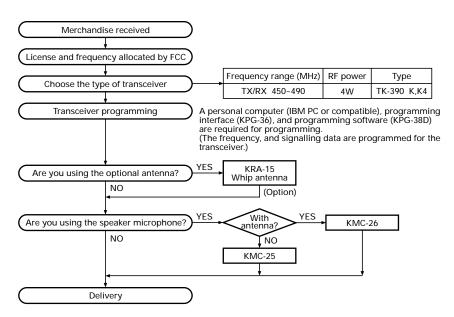
WE CANNOT guarantee oscillator stability when using channel element manufactured by other than KENWOOD or its authorized agents.

#### FCC COMPLIANCE AND TYPE NUMBERS

Model	Type acceptance number	Frequency range	Compliance
TK-390	ALH21903110	450~490MHz	Parts 22,74,90,95

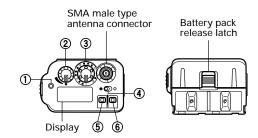
Model &	Unit	X57-540X-XX	X53-378X-XX		Frequency range	Remarks	Charger	Battery	Antenna	Keypad
destina	ition	0-10	0-12 0-13							
TK-390	K	0	0		450 400N4Hz	IF1: 44.85MHz	OP	OP	OP	_
1K-390	K4	0		0	450~490MHz	LOC: 44.395MHz	OP	OP	OP	0

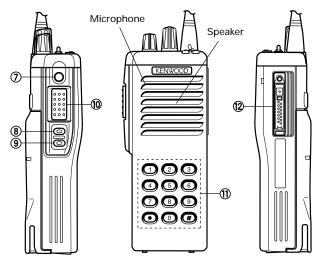
#### SYSTEM SET-UP



### **OPERATING FEATURES**

#### 1. Getting Acquainted





#### 1-1. Key Descriptions

#### 1) TX/Busy/Battery low indicator

Lights red while transmitting. Lights green while receiving. Flashes red when the battery power is low while transmitting; replace or recharge the battery.

Note: This indicator can be disabled by your dealer.

#### 2 Power switch/Volume control

Turn clockwise to switch ON the transceiver. Turn counterclockwise, until a click sounds, to switch OFF the transceiver. Rotate to adjust the volume level.

#### ③ Selector

Rotate this control to activate its programmable function (Page 8).

#### (4) Toggle switch

Switch the toggle position to activate its programmable function (Page 8).

- **5** Top 1
- **6** Top 2
- (7) Orange
- Orange
- 9 Side 2

Press these PF (programmable function) keys to activate their programmable func-

#### (8) Side 1 tions (Page 8)

#### 10 PTT (Push-To-Talk) switch

Press this switch, then speak into the microphone to call a station.

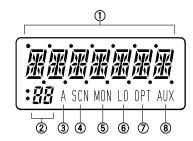
#### ① DTMF keypad (keypad models only)

Press the keys on the telephone keypad to send DTMF tones.

#### 12 Universal connector

Connect the external speaker/microphone (optional) here. Otherwise, keep the supplied cover in place.

#### 1-2. Display



#### 1 Alphanumeric display

Displays the operating group or channel number, or the group or channel name. When making a DTMF or 2 Tone call, the display will alternate between CALL and the channel. Also displays various menu functions.

#### 2 7 Segment display

Displays the operating group or channel number. Also displays tA (Talk Around), P1 (Priority1), P2 (Priority2), or HC (Home Channel); depending on the function being used.

#### 3 A (Add) indicator

Appears when a channel is added to the scanning sequence.

#### 4 SCN (Scan) indicator

Appears when Scan mode is active.

#### 5 MON (Monitor) indicator

Appears when the monitor function is active.

#### 6 LO (Low) indicator

Appears when low power is selected.

#### (7) OPT indicator

Appears when Operator Selectable Tone is enabled.

#### **8 AUX (Auxiliary) indicator**

Appears when Aux is ON. Appears and blinks when the optional scrambler board is enabled.

**Note**: The alphanumeric and 7 segment displays can be inverted if a PF key or the toggle switch is programmed with Invert Display (Page 8).

### **OPERATING FEATURES**

#### 2. Scan Operating

#### 2-1. Scan Types

#### Single Group Scan

You can scan all valid (ADD) channels in the displayed group that can be selected with the group selector.

#### Multiple Group Scan

You can scan all valid (ADD) channels in the all valid (ADD) group.

#### 2-2. Scan Start Condition

One or more non-priority channels must be added to all channels that can be scanned. The transceiver must be in normal receive mode (PTT off).

When you activate the key or the toggle switch (to right position) programmed to the scan function, the scan starts. The scan icon "SCN" lights and "SCAN" or revert channel (programmable) is indicated on 7-digit alphanumeric display.

#### 2-3. Scan Stop Condition

The scan stops temporarily if the following conditions are satisfied.

- A carrier is detected, then signalling matches on channels for which receive the signalling is set by the programming software.
- 2) A carrier is detected on the channel for which receiving signalling is not set by the programming software or when the monitor (signalling cancel) function is activated.

#### 2-4. Scan Channel Types

- Priority channel 1 is the most important channel for the scan, and always detects a signal during scan and when the scan stops temporarily.
- 2) Priority channel 2 is the next important channel for the scan, and always detects a signal during scan and when the scan stops temporarily at a channel other than priority channel 1
- 3) Non-priority channels detects a signal during scan. For the channels that can be selected with the group or channel selector when the scan does not occur, adds an indicator "A" lights.

#### 2-5. Priority Channel Setting

Priority channels 1 or 2 can be set as follows with the programming software (KPG-38D).

- 1) Specify priority channels 1 or 2 as fixed priority channels.
- 2) Make selected channels, priority channels.
- 3) Operator delectable

Specify the initial channel before the operator changes it.

#### 2-6. Scan Type According to the Priority Channel

1) When no priority channels are set: Only the non-priority channels are scanned.

If a non-priority channel stops temporarily, it stops until there is no signal on the channel. 2) When one priority channel is set: Either priority channel 1 or 2 is scanned.

If a non-priority channel stops temporarily, a priority channel signal is detected at certain intervals.

If a priority channel stops temporarily, it stops until there is no signal on the priority channel.

3) When two priority channels are set: The non-priority channel, priority channels 1 and 2 are scanned.

If a non-priority channel stops temporarily, priority chan-

nel 1 and 2 signals are detected at certain intervals. If priority channel 2 stops temporarily, the priority channel 1 signal is detected at certain intervals.

If priority channel 1 stops temporarily, it stops until there is no signal on priority channel 1.

#### 2-7. Revert Channel

The revert channel is used to transmit during scanning and set by the programming software (KPG-38D).

1) Priority 1

The transceiver reverts to the priority channel 1.

2) Priority 1 with talkback

The transceiver reverts to the priority channel 1.

If you press PTT during a resume timer (dropout delay time, TX dwell time) or calling, you can transmit on current channel to answer to the call however revert channel is set to priority channel 1.

After resume time, scan re-starts and transmission channel is return to priority channel 1.

3) Priority 2

The transceiver reverts to the priority channel 2.

4) Priority 2 with talkback

The transceiver reverts to the priority channel 2.

If you press PTT during resume timer (dropout delay time, TX dwell time) or calling, you can transmit on current channel to answer to the call however revert channel is set to priority channel 2.

After resume time, scan re-starts and transmission channel is return to priority channel 2.

5) Selected channel

The transceiver reverts to the channel before scanning or the channel that you changed during scan.

6) Last called channel

The transceiver reverts to the last called channel during the scan.

7) Last used channel

The transceiver reverts to the last used (transmitted) channel during scan. "Last used" revert channel includes talkback function.

8) Selected with talkback

The transceiver reverts to the channel before scanning or the channel that you changed during scan.

#### 2-8. Scan End

When you reactivate the key or the toggle switch (to left position) programmed to the scan function during scan mode, the scan ends.

The scan icon "SCN" and "SCAN" or revert channel (programmable) display goes off.

### **OPERATING FEATURES**

#### 2-9. Temporarily Delete/Add

It is possible to delete or add channel temporarily during scan. When scan stops on unnecessary channel for example by interference of the other party, activate the delete/ add function (for example press the key), then that channel is deleted temporarily and scan re-start immediately.

When you would like to add the deleted channel temporarily to scan sequence, select the desired (deleted) channel during scan, activate the delete/add function (for example press the key) before scan re-start.

That channel is added temporarily to scan sequence. The temporary deleted or added channels are returns to pre-set delete/add, when the transceiver exits from scan mode.

#### 3. Optional Features

You can use these features using the programming software (KPG-38D).

#### 3-1. Alphanumeric Display (Group/Channel Name)

The programming software (KPG-38D) enables you to set the alphanumeric display for group/channel name. The total text size of group and channel name are 7-digits.

For example, If you set 2-digits for group name, then you can use 5-digits for channel name. The characters can be used as shown in Figure 1.



### 3-2. Beep Tones

The beep tones (power on tone, control tone, warning tone, alert tone) are individually programmable to the fixed level 0 to 31 or follow the mechanical volume position.

#### 3-3. Minimum Volume

The minimum volume is programmable (0 to 31). The transceiver remains the minimum volume level however the mechanical volume position is set to zero.

#### 3-4. BCL (Busy Channel Lockout) Override

You can transmit in spite of Busy Channel Lockout situation. For example : To make an emergency voice call.

To transmit under busy channel lockout situation, press PTT once more within approx. 500ms after the PTT release.

#### 3-5. Selective Call Alert LED

You can select whether or not the LED on the transceiver flashes in an orange color when Selective call was occurred.

#### 3-6. Battery Warning

This transceiver has battery warning feature. If the low voltage is detected during transmission, the transceiver warns it by flashing red "LED".

Then more low voltage is detected during transmission, the transceiver stops transmission and warns it by flashing red "LED" and beep.

Please notice "standard" for the battery exchange, charging time by flashing red LED and beep.

#### 3-7. Busy LED

You can program the enable or disable the busy "LED" function when a carrier is detected. "Disable" saves battery life.

#### 3-8. TX LED

You can program the enable or disable the transmission "LED" function.

#### 3-9. 2-Digit 7-Segment Display

You can use 2-digit 7-segment the display to display the channel number or group number. It is useful when the main (7-digit 13-segment) display indicates group or channel name.

#### 3-10. Invert Display

Main (7-digit 13-segment) display and sub (2-digit 7-segment) display can be programmed to invert display.

It is easy to read the display when the operator suspended the transceiver on a waste belt. The operator also can change the display between normal and invert using key. Refer the invert display function of key function.

#### 3-11. Emergency Channel Display

The transceiver can be programmed to display "EMER-GENCY" channel name when it is in emergency mode.

If you set to "off" by KPG-38D the transceiver shows selected group/channel/status before entering to the emergency mode however the transceiver is in an emergency mode.

#### 3-12. Clear to Transpond

The transceiver waits the transpond of 2-Tone/DTMF if channel is busy until channel open. This feature prevents the interference to other party.

#### 3-13. External Speaker

It can be selected if the receive sound is made by SP-Mic SP or the main body SP at a SP-Mic mount.

### **OPERATING FEATURES**

#### 3-14. Mode (Enable/Disable)

The transceiver has many special modes mainly for maintenance.

- · Self Programming mode
- · Panel Test mode
- · Clone mode
- · Firmware Programming mode

It is possible to set enable/disable for each mode. We recommend to set these mode to Disable after set up to save contents.

#### 3-15. ID

The transceiver is capable to have ID. The format is DTMF. The timing that the transceiver sends ID is programmable.

Connect ID: Connect ID is send on beginning of transmission.

Disconnect ID: Disconnect ID is send on end of transmission.

Both: Connect ID is send on beginning of transmission and disconnect ID is send on end of transmission.

Off: Sending ID function is disabled.

There is also "PTT ID" setting for each channel. Refer "PTT ID" of channel feature.

#### 3-16. OST (Operator Selectable Tone)

The transceiver is capable to have "OST" function and 16 tone pair (QT/DQT) with max 7-digit name for each tone pair.

#### "OST" Back Up

The transceiver is programmable the selected "OST" code is memorized or not. If you set to Disable (no memorized), the "OST" function always starts at "off".

#### · Direct "OST"

It is possible to call "OST" number directory using keypad. In this case, keypad is used for "OST", then "DTMF Auto PTT" "DTMF Auto Dial" functions by keypad are not usable.

#### 3-17. Radio Password (Keypad Model Only)

The radio password prevent unauthorized users operation. Every time the power on, transceiver is locked and unusable until entering correct password.

Enter pre-programmed password by FPU and [#] key causes the transceiver unlocked.

#### 3-18. Data Password (Keypad Model Only)

The data password prevents unauthorized reading of the programmed transceiver data by FPU. Enter pre-programmed password in FPU reading process. This password also protects the clone.

Enter pre-programmed password by FPU and [#] key to clone.

#### 4. Group Features

You can use these features using the programming software (KPG-38D).

#### 4-1. "TOT" (Time-Out Timer)

The transceiver has the "TOT". This parameter selects the period of time users can continuously transmit.

When the selected period passes, the transceiver generates an warning tone and stops the transmission.

#### 4-2. "TOT" Pre-Alert

The transceiver has "TOT" pre-alert timer. This parameter selects the time at which the transceiver generates "TOT" pre-alert tone before "TOT" is expired.

"TOT" will be expired when the selected time passes from a TOT pre-alert tone.

#### 4-3. "TOT" Re-Key Time

The transceiver has "TOT" re-key timer. This timer is the time you can not transmit after "TOT" exceeded. After "TOT" re-key time expired you can transmit again.

#### 4-4. "TOT" Reset Time

The transceiver has "TOT" reset timer. This timer is the minimum wait time allowed during a transmission that will reset the "TOT" count.

"TOT" reset time causes the "TOT" to continue even after PTT is released unless the "TOT" reset timer has expired.

#### 4-5. Group Delete/Add

The transceiver can set the delete/add in each group. If "Delete" is selected, the transceiver does not scan the deleted group in multi group scan.

#### 4-6. Battery Save

This is the automatic battery saver during a standby mode operation. The receiver circuit is repeated on and off to conserve the battery life.

#### 4-7. Signalling

Signalling "AND/OR" sets the audio unmute condition for any channel programmed with the option signalling (2-Tone/DTMF).

AND: "AND" requires both the valid option signalling and the programmed QT/DQT to be received for audio to unmute (and initiate an option signalling decode alert).

OR: "OR" requires either the valid option signalling or the programmed QT/DQT to be received for audio to unmute (an option signalling decode alert is only initiated if the proper option signalling is decoded).

### **OPERATING FEATURES**

#### 5. Channel Features

You can use these features using the programming software (KPG-38D).

#### 5-1. Option Signalling

The transceiver is programmable to the option signalling (2-Tone decode program 1, 2-Tone decode program 2, 2-Tone decode program 3, DTMF decode) to each channel. It is useful to receive an individual call.

Receive format is selectable "AND" or "OR" with QT/DQT for each group. The radio response of option signalling is programmable "(Call) Alert tone" or "Transpond" for each option signalling (2-Tone decode program 1, 2-Tone decode program 2, 2-Tone decode program 3, DTMF).

#### 5-2. PTT ID

PTT ID provides a DTMF ANI to be sent with every time PTT (connect ID at beginning of transmission, disconnect ID at end of transmission, or both).

You can program PTT ID "on" or "off" for each channel. The contents of ID are programmed for each transceiver.

#### 5-3. Busy Channel Lockout

Transmission is inhibited when the channel is busy. It is able to set this feature "Yes" or "No" for each channel.

#### 5-4. Beat Shift

This is the feature that the microprocessor shifts its system clock frequency slightly to prevent the receive interference. This transceiver can program this feature "Yes" or "No" for each channel.

#### 5-5. TX Power

You can set the transmission power "High" or "Low" for each channel. The each power setting is tuned at factory.

However, you can re-tune the power, using PC Tuning Mode of KPG-38D.

#### 5-6. Wide/Narrow

You can set the occupied band width mode "Wide" or "Narrow" for each channel. It is useful for the operator to use the transceiver on various sites.

#### 5-7. Scan Delete/Add

Scanning "delete/add" is programmable for each channel. Set the currently selected channel required to include in the scan sequence to "add".

The operator can change the "delete/add" information using the key programmed to "delete/add" function.

#### 6. Key Functions

You can use these features, using the programming software (KPG-38D). Selector function is selectable channel select or group select.

The functions for Toggle switch are listed page 8 (Fig. 2). Right position is active for programmed function on toggle switch except "group select".

The functions for the top key are listed page 8 (Fig.2). Hold action and shift action are programmable.

The functions for side key are listed page 8 (Fig.2). Hold action and shift action are programmable.

The functions for microphone key are listed page 8 (Fig.2). Hold action is programmable.

#### 6-1. No Function

Sounds error operation beep, and no action will occur. Use this function when the transceiver is required to be more simple operated.

#### 6-2. AUX.

This function can be programmed when the voice scrambler board is not installed.

If this key is pressed, "AUX" icon lights on the LCD and AUX port which is inside of the transceiver turns to the high level. If pressed again, the "AUX" icon goes off and the AUX ports turns to the lower level.

#### 6-3. Channel Down

If this key is pressed once, the channel number decreases by one step. If this key holds down for 500ms (approximate), the channel number decreases continuously.

This key works as the squelch level adjuster in a squelch level adjust mode. This key works as the OST (operator selectable tone) number selector in the OST mode. This key works as the SCR (voice scrambler) code selector in the voice scrambler code select mode.

#### 6-4. Channel Up

If this key is pressed once, the channel number increases by one step. If this key holds down for 500ms (approximate), channel number increases continuously.

This key works as the squelch level adjuster in squelch level adjust mode. This key works as the OST (operator selectable tone) number selector in the OST mode. This key works as the SCR (voice scrambler) code selector in the voice scrambler code select mode.

#### 6-5. Channel Name

This key switches the LCD display between the group/ channel number and the group/channel name.

#### 6-6. Delete/Add

This key switches the currently displayed channel between "Delete" and "Add".

The "Add" channel contained in the scan sequence, and "Delete" channel is not contained. In the scan mode, this key switches the channel delete or add temporarily.

### **OPERATING FEATURES**

Function Name  Selector  (a)  (b)  (c)  (c)  (d)  (d)  (d)  (d)  (e)  (e)  (e)  (e			Toggle	PF Keys	Speaker/
Aux 1 Channel Down Channel Name Channel Select Channel Up Delete/Add Emergency Call 2 Group Down Group Scan Group Select  Group Up Home Channel Invert Display Key Lock Lamp Low Power Monitor Monitor Momentary No Function Operator Selectable Priority 1 Operator Selectable Priority 2 Scan Squelch Level Squelch Momentary Squelch Group Squelch Momentary Squelch Momentary Squelch Momentary Channel	Function Name	Selector	Switch	567	Microphone
Channel Down Channel Name Channel Select Channel Up Delete/Add Emergency Call 2 Group Down Group Scan Group Select  Group Up Home Channel Invert Display Key Lock Lamp Low Power Monitor Monitor Momentary No Function Operator Selectable Trone Operator Selectable Priority 1 Operator Selectable Priority 2 Scan Squelch Level Squelch Momentary Squelch Momentary Squelch Momentary Squelch Momentary Channel		(3)			PF Keys
Channel Name Channel Select Channel Up Delete/Add Emergency Call 2 Group Down Group Scan Group Select Group Up Home Channel Invert Display Key Lock Lamp Low Power Monitor Monitor Momentary No Function Operator Selectable Trone Operator Selectable Priority 1 Operator Selectable Priority 2 Scan Squelch Level Squelch Momentary Squelch Momentary Squelch Momentary Squelch Momentary Squelch Momentary Channel	Aux <sup>1</sup>		✓	✓	✓
Channel Select Channel Up Delete/Add Emergency Call 2 Group Down Group Scan Group Select Group Up Home Channel Invert Display Key Lock Lamp Low Power Monitor Monitor Momentary No Function Operator Selectable Priority 1 Operator Selectable Priority 2 Scan Scrambler 3 Shift SP Attenuation Squelch Momentary	Channel Down			✓	1
Channel Up Delete/Add  Emergency Call 2 Group Down Group Scan Group Select Group Up Home Channel Invert Display Key Lock Lamp Low Power Monitor Monitor Momentary No Function Operator Selectable Priority 1 Operator Selectable Priority 2 Scan Scrambler 3 Shift SP Attenuation Squelch Momentary Squelch Momentary  V  V  V  V  V  V  V  V  V  V  V  V  V	Channel Name			✓	✓
Delete/Add  Emergency Call 2  Group Down  Group Scan  Group Select  Group Up  Home Channel  Invert Display  Key Lock  Lamp  Low Power  Monitor  Monitor Momentary  No Function  Operator Selectable  Tone  Operator Selectable  Priority 1  Operator Selectable  Priority 2  Scan  Scrambler 3  Shift  Squelch Level  Squelch Momentary  V  V  V  V  V  V  V  V  V  V  V  V  V	Channel Select	✓			
Emergency Call 2 Group Down Group Scan Group Select Group Up Home Channel Invert Display Key Lock Lamp Low Power Monitor Monitor Moneton Selectable Tone Operator Selectable Priority 1 Operator Selectable Priority 2 Scan Scrambler 3 Shift Sp Attenuation Squelch Der Squelch Momentary	Channel Up			✓	✓
Group Down Group Scan Group Select  Group Up Home Channel Invert Display Key Lock Lamp Low Power Monitor Monitor Monetary No Function Operator Selectable Priority 1 Operator Selectable Priority 2 Scan Scrambler 3 Shift SP Attenuation Squelch Level Squelch Momentary  Group Down	Delete/Add			✓	✓
Group Scan Group Select Group Up Home Channel Invert Display Key Lock Lamp Low Power Monitor Monitor Momentary No Function Operator Selectable Tone Operator Selectable Priority 1 Operator Selectable Priority 2 Scan Scrambler 3 Shift SP Attenuation Squelch Level Squelch Momentary V V V V V V V V V V V V V V V V V V V	Emergency Call <sup>2</sup>			✓	✓
Group Select Group Up Home Channel Invert Display Key Lock Lamp Low Power Monitor Monitor Momentary No Function Operator Selectable Tone Operator Selectable Priority 1 Operator Selectable Priority 2 Scan Scrambler 3 Shift SP Attenuation Squelch Level Squelch Momentary V V V V V V V V V V V V V V V V V V V	Group Down			1	1
Group Up  Home Channel  Invert Display  Key Lock  Lamp  Low Power  Monitor  Monitor Momentary  No Function  Operator Selectable Tone  Operator Selectable Priority 1  Operator Selectable Priority 2  Scan  Scrambler 3  Shift  SP Attenuation  Squelch Level Squelch Momentary  V   V   V   V   V   V   V   V   V   V	Group Scan		✓		
Home Channel Invert Display  Key Lock  Lamp  Low Power  Monitor  Monitor Momentary  No Function  Operator Selectable Tone  Operator Selectable Priority 1  Operator Selectable Priority 2  Scan  Scrambler 3  Shift  SP Attenuation  Squelch Level Squelch Momentary  V  V  V  V  V  V  V  V  V  V  V  V  V	Group Select	✓	✓		
Invert Display  Key Lock  Lamp  Low Power  Monitor  Monitor  Moruction  Operator Selectable Priority 1  Operator Selectable Priority 2  Scan  Scrambler 3  Shift  SP Attenuation  Squelch Level Squelch Momentary  V  V  V  V  V  V  V  V  V  V  V  V  V	Group Up			1	1
Key Lock  Lamp  Low Power  Monitor  Monitor  Monitor Momentary  No Function  Operator Selectable Tone  Operator Selectable Priority 1  Operator Selectable Priority 2  Scan  Scrambler 3  Shift  SP Attenuation  Squelch Level  Squelch Momentary  V  V  V  V  V  V  V  V  V  V  V  V  V	Home Channel			1	1
Lamp Low Power  V Monitor  Monitor  Monitor Momentary  No Function  Operator Selectable Tone  Operator Selectable Priority 1  Operator Selectable Priority 2  Scan  Scrambler 3  Shift  SP Attenuation  Squelch Level  Squelch Momentary  V V V V V V V V V V V V V V V V V V	Invert Display		1	1	1
Low Power  Monitor  Monitor  Monitor Momentary  No Function  Operator Selectable Tone  Operator Selectable Priority 1  Operator Selectable Priority 2  Scan  Scrambler 3  Shift  SP Attenuation  Squelch Level  Squelch Momentary  V  V  V  V  V  V  V  V  V  V  V  V  V	Key Lock		1	1	1
Monitor  Monitor Momentary  No Function  Operator Selectable Tone  Operator Selectable Priority 1  Operator Selectable Priority 2  Scan  Scrambler 3  Shift  SP Attenuation  Squelch Level  Squelch Momentary  V  V  V  V  V  V  V  V  V  V  V  V  V	Lamp			1	1
Monitor Momentary  No Function  Operator Selectable Tone  Operator Selectable Priority 1  Operator Selectable Priority 2  Scan  Scrambler 3  Shift  SP Attenuation  Squelch Level Squelch Momentary  V  V  V  V  V  V  V  V  V  V  V  V  V	Low Power		1	1	1
No Function Operator Selectable Tone Operator Selectable Priority 1 Operator Selectable Priority 2 Scan Scrambler 3 Shift SP Attenuation Squelch Level Squelch Momentary  V  V  V  V  V  V  V  V  V  V  V  V  V	Monitor		1	1	1
Operator Selectable Tone Operator Selectable Priority 1 Operator Selectable Priority 2 Scan Scrambler 3 Shift SP Attenuation Squelch Level Squelch Momentary  V  V  V  V  V  V  V  V  V  V  V  V  V	Monitor Momentary			1	1
Tone Operator Selectable Priority 1 Operator Selectable Priority 2 Scan Scrambler 3 Shift SP Attenuation Squelch Level Squelch Momentary  V  V  V  V  V  V  V  V  V  V  V  V  V	No Function		1	1	1
Operator Selectable Priority 1 Operator Selectable Priority 2 Scan Scrambler 3 V Shift SP Attenuation Squelch Level Squelch Momentary V V V V V SP Squelch Momentary	Operator Selectable			1	1
Priority 1  Operator Selectable Priority 2  Scan  Scrambler 3  Shift  SP Attenuation  Squelch Level  Squelch OFF  Squelch Momentary	Tone				
Operator Selectable Priority 2  Scan	Operator Selectable			1	
Priority 2  Scan  Scrambler 3  Shift  SP Attenuation  Squelch Level  Squelch OFF  Squelch Momentary  Scan	Priority 1				
Priority 2  Scan  Scrambler 3  Shift  SP Attenuation  Squelch Level  Squelch OFF  Squelch Momentary  Scan	Operator Selectable			1	
Scrambler 3					
Shift  SP Attenuation  Squelch Level  Squelch OFF  Squelch Momentary  V  V	Scan		1	1	1
SP Attenuation  Squelch Level  Squelch OFF  Squelch Momentary	Scrambler <sup>3</sup>		1	1	1
Squelch Level	Shift		1	1	
Squelch OFF	SP Attenuation				1
Squelch OFF	Squelch Level			1	1
Squelch Momentary    ✓   ✓			1	1	1
Talk Around	Squelch Momentary			1	1
			1	1	1

- 1 This function can be selected when the scrambler board has not been installed.
- 2 This function can be selected when the ANI board has been installed.
- 3 This function can be selected when the scrambler board has been installed.

**Note:** If "Shift" is pregrammed onto one of the PF keys <u>or</u> the toggle switch, the remaining PF keys can be programmed with two different functions. If "Shift" is programmed onto a PF key <u>and</u> the toggle switch, an error will occur and the function will not operate.

Fig. 2 Programmable functions

#### 6-7. Group Down

If this key is pressed once, the group number decreases by one step. If this key holds down for 500ms (approximate), the group number decreases continuously.

This key works as the squelch level adjuster in squelch level adjust mode. This key works as the OST (operator selectable tone) number selector in the OST mode. This key works as the SCR (voice scrambler) code selector in the voice scrambler code select mode.

#### 6-8. Group Up

If this key is pressed once, the group number increases by one step. If this key holds down for 500ms (approximate), the group number increases continuously.

This key works as the squelch level adjuster in the squelch level adjust mode. This key works as the OST (operator selectable tone) number selector in the OST mode. This key works as the SCR (voice scrambler) code selector in the voice scrambler code select mode.

#### 6-9. Home Channel

Press this key once, the channel switches to the pre-programmed home channel. Press this key again, the channel goes back to the previous channel.

#### 6-10. Invert Display

Press this key once, the displayed the group/channel number or group/channel name are inverted. Press this key again, the display returns to the normal.

For the operator who does not change the display and needs "Invert" only, refer "Invert Display" setting of optional feature.

#### 6-11. Key Lock

Pressing this key causes the transceiver to accept an entry of only the [Shift], [KeyLock], [PTT], [Emergency] keys, [Selector switch], [Volume], [Toggle], [Lamp], [Moni], [Moni momentary], [SQ off] and [SQ momentary].

"Lock" is used to prevent users from unexceptable key press which might cause a transceiver malfunction. The display does not change while the key is being locked.

Switching the transceiver off and on or pressing Key Lock again cancels the key lock. Key locked transceiver can still receive. Pressing this key while scanning, keys are locked but a scanning continues.

#### 6-12. Lamp

Press this key, the transceiver illuminates the display and keypad back lit approximate 5 seconds. Press this key again, the transceiver stops the illuminating.

Pressing any key except the LAMP key while the illuminated restarts the 5 second timer.

#### 6-13. Low Power

Press this key, the transmission power of all channel changes to Low. Press this key again, the transmission power returns to programmed value.

### **OPERATING FEATURES**

#### 6-14. Monitor

Monitor the channel before a transmission.

Press this key once, "MON" appears and unmutes speaker if a carrier is present, regardless of the specified signalling (including option signalling). Press this key again, "MON" disappears and mutes speaker.

Press this key after the Option Signalling is matched, the Option Signaling is reset and monitor is activated. DBD (Dead Beat Disable) mode is not reset by this operation.

#### 6-15. Monitor Momentary

While pressing this key, the monitor function (refer 6-14) is activated. Release this key, the monitor function is deactivated.

#### 6-16. Operator Selectable Tone

This key switches the pre-set decode QT/DQT and encode QT/DQT to OST (Operator Selectable Tone) tone pair.

Press this key, the transceiver enters to OST select mode. In this mode, the display shows "OFF" and the operator can select one of the OST tone pair using the channel up/down key or the group up/down key. The display shows "TONE \* \* " and tone pair No. \* \* is selected.

Press OST key again, the transceiver exits from the OST select mode, and returns to the group/channel mode with "OPT" icon. "OPT" icon means that the OST tone pair is selected. OST tone pair number or OFF can be memorized for each channel.

16 kinds of tone pair for OST can be programmed by KPG-38D. OST is useful to access the repeater with same radio frequency and different tone (QT/DQT).

#### 6-17. Operator Selectable P1

If priority channel 1 is set as "Fixed" and "None" in the scan information. The operator can select the priority channel 1, using this key (operator selectable fixed P1).

Press this key on normal channel, the channel becomes to priority channel 1. Previous priority channel 1 returns to the normal channel. Press this key on the priority channel 1, the priority 1 will be lost (no priority 1).

#### 6-18. Operator Selectable P2

If priority channel 2 is set as "Fixed" and "None" in the scan information. The operator can select the priority channel 2, using this key (operator selectable fixed P2).

Press this key on the normal channel, the channel becomes to the priority channel 2. Previous priority channel 2 returns to the normal channel. Press this key on priority channel 2, the priority 2 will be lost (no priority 2).

#### 6-19. Scan

Press this key starts scanning. Pressing this key stops scanning.

#### 6-20. Shift

This key activates "Shift + [Key]" function. It is useful when the numbers or more of the functions are necessary.

#### 6-21. Squelch Level

The preset squelch level is varied in user mode (0 to 15). Press the key programmed to "squelch level", the transceiver enters to "squelch level adjust mode".

The squelch level can be adjusted by group "up/down" function key or channel "up/down" function key. Press the key programmed to "squelch level" again, the adjusted level is memorized and returns to the normal user mode.

#### 6-22. Squelch Momentary

While pressing this key, the transceiver unmutes speaker regardless of an existence of a carrier and "MON" appears and busy "LED" lights on.

Release this key, the transceiver mutes the speaker and "MON" disappears and busy "LED" lights off.

#### 6-23. Squelch Off

Press this key, the transceiver unmutes speaker regardless of the existence of a carrier and "MON" appears and busy "LED" lights.

Press this key again, the transceiver mutes the speaker and "MON" disappears and busy "LED" lights off.

#### 6-24. Talk Around

Press this key, the transceiver uses the receive frequency and the tone for transmission.

The operator can call the other party directory (without repeater). Press this key again, the talk around function goes off.

#### 6-25. Emergency Call

Press this key, the transceiver enters to an emergency mode. In this mode, the channel is switched to the programmed emergency channel automatically and starts transmission with an emergency ID code which is programmed to ANI board.

The display depends on "Emergency Channel Display" setting.

#### 6-26. SP Attenuation

Press this key once, the volume level of speaker-microphone is attenuated. Press this key again, the volume level of speaker-microphone returns to the previous level.

#### 7.2-Tone

Built-in 2-Tone decoder (decoder program 1, decoder program 2, decoder program 3) is available for option signalling. It is possible to use individual call or group call.

#### 8. DTMF

Built-in DTMF encoder is available for dialing (Manual dial, Auto-dial (9 memory), Re-dial (1 memory)) (Keypad model only). Built-in DTMF decoder is available for option signalling

It is possible to use individual call, group call, DBD (Dead Beat Disable).

### OPERATING FEATURES / REALIGNMENT

#### 9. Data Programming (PC Mode)

#### 9-1. Preparation and Connection

TK-390 transceiver is programmed by using a personal computer, programming interface cable KPG-36, and programming software KPG-38D.

The programming software can be used with an IBM-PC or compatible machine. Figure 3 shows the setup for programming.

#### 9-2. Programming Interface Cable KPG-36 Description

The KPG-36 is required to interface TK-390 to the computer. It has a circuit in its D-sub 25pin connector case that converts RS-232C logic level to TTL level.

KPG-36 is used to connect between TK-390 universal connector and RS-232C serial port of computer.

#### 9-3. Programming Software KPG-38D Description

KPG-38D is the programming software for TK-390 supplied on a 3.5" floppy disk. This software runs under MS-DOS version 3.1 or later on an IBM-PC/XT, AT, or PS2 or compatible machine.

The data can be input to or read from TK-390 and edited on the screen. The programmed or edited data can be printed out. It is also possible to tune the transceiver.

We recommend that install KPG-38D for example to harddisk first then use it.

KPG-38D instruction manual part No.: B62-0814-XX.

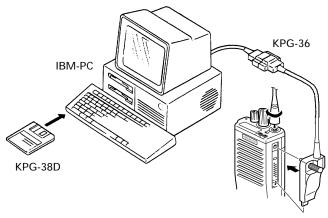
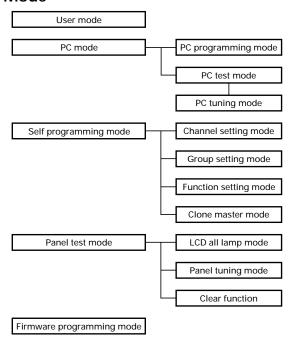


Fig. 3

#### REALIGNMENT

#### 1. Mode



Mode	Function
User mode	Customer use this mode
PC mode	Communication between the radio
	and PC (IBM compatible).
	It requires the KPG-38D
PC programming mode	Frequency, signalling and features
	write to the radio and read from
	the radio.
PC test mode	Check the radio using the PC.
	This feature is included in the FPU.
Self programming mode	Frequency, signalling and features
	write to the radio.
Panel test mode	Dealer use to check the fundamen-
(Refer to Adjustment)	tal characteristics.
Firmware programming mode	Re-write the firmware of the flash
	ROM.

#### 2. How to Enter Each Mode

Mode	Operation
User mode	Power on
PC mode	Power on begins the USER MODE.
Self programming mode	Hold down the [Side 1] key and the
	[Side 2] key, turn the radio power
	on.
Panel test mode	Hold down the [Side 2] key and
	[PTT], turn the radio power on, and
	release [PTT] first.
Firmware programming mode	Held down the [Side 2] key and
	[PTT], turn the radio power on, and
	release [Side 2] key first.

#### 3. Self Programming

Write mode for frequency data and signalling etc. Mainly used by the person maintaining the user equipment.

#### 3-1. Enter to the self programming mode

Turn the power switch on, with the lead wire with plug PF (8 pin) shorted to the E (10 pin) lead (Figure 4), or delete R466 (SELF, Figure 5) in the TX-RX unit and turn the power switch on while pressing the [Side 1] and [Side 2] keys.

#### Note:

This mode (self programming mode) cannot be set when it has been disabled with the FPU.

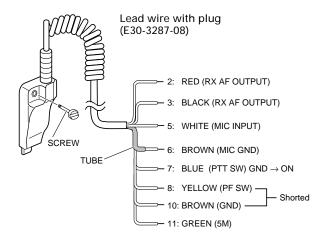


Fig. 4

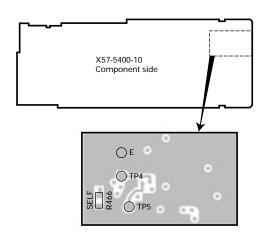


Fig. 5

When enter the self programming mode, "FUNC" appears after "SEL" is displayed for half a second.

Selecting any of Channel setting, Group setting, Function setting, or Clone master with the [Top 1] [Top 2] keys and then pressing [PTT] sets the Setting mode for that time.

Key operations in Self programming mode are as follows.

[Selector switch]: Not used

[PTT] : Functions as a RUN or Execute key

[Top 1] : Use as a Down key [Top 2] : Use as an Up key

[Side 1] : Use for select channel steps in Chan-

nel setting mode, or switching for

QT/DQT.

[Side 2] : Use as a cancel key

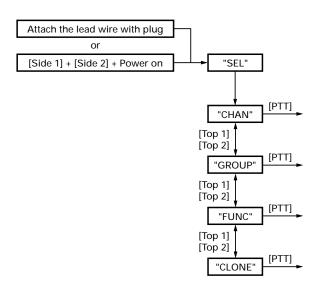
[Orange] : Add or delete frequencies in Channel

setting mode

[Toggle] : Flipping this to the right while in

Channel setting mode, shifts to MHz

steps.



#### 3-2. Channel Setting Mode

Set data for each channel while in this mode. After first entering Self programming mode, select the "CHAN" display with [Top 1] [Top 2] and press [PTT] to set Channel Setting mode. Once in Channel Setting mode, select the group that needs setting with the [Top 1] [Top 2] keys and press [PTT]. Next select the channel for setting with the [Top 1] [Top 2] keys and press [PTT]. The setting items and setting data will then appear so reset the data with the [Top 1] [Top 2] keys and press [PTT]. When finished, the display shifts to the next setting item. After finished setting all items press [PTT] to return to Group selection. Changes in the frequency CH steps and the QT/DQT steps can be made in [Side 1].

No.	Function name	Display	Remarks
	Select	1.–1 during	1-160~160-1
	Group/Channel	group selection	
		1–1. during	
		channel selection	
1	RX frequency	R450.0125	Receive frequency
			(Dot on right edge is
			lit up during 6.25kHz
			steps)
2	RX signalling	RX 023N	Receive QT/DQT
			(Dot on right edge is
			lit up during 1 step
			changes)
3	TX frequency	T450.0125	Transmit frequency
			(Dot on right edge is
			lit up during 6.25kHz
			steps)
4	TX signalling	TX 250.3	Transmit QT/DQT
			(Dot on right edge is
			lit up during 1 step
			changes)
5	Option signalling	2ToneA	OFF, DTMF, 2ToneA,
			2ToneB, 2ToneC
6	DEL/ADD	D/A ADD	Delete, Add
7	Wide/Narrow	WIDE	Wide. Narrow
8	PTT ID	ID OFF	OFF, ON
9	TX power	POW HI	High, Low
10	Busy channel lockout	BCL OFF	OFF, ON
11	Beat shift	SFT OFF	OFF, ON

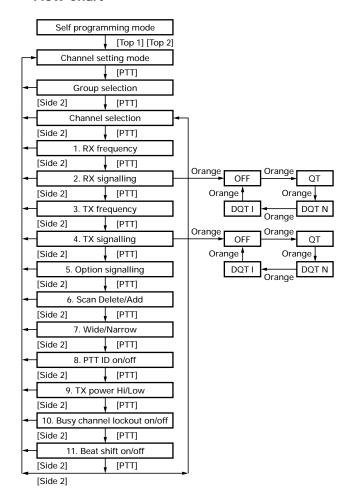
#### Operation

- 1. Select the setting value with the [Top 1] [Top 2] keys.
- 2. Press the [PTT] and the selected value is backed up and operation shifts to the next item for setting.
- 3. Press [Side 2] on the Group selection screen in order to return to Self programming mode.

#### Note

- 1. Different sample displays are shown.
- 2. Setting item No.s are displayed with a 7-segment 2-digit figure on the LCD.
- 3. Self programming mode cannot be set when set to Disaable with the FPU.
- A red LED lights up during TX frequency and TX signalling.
- 5. Press [Orange] on the TX, RX frequencies setting screen in order to clear in the channel frequencies data.
- 6. Press [Orange] on the signalling setting screen in order to change or off the signalling function.
- 7. Flipping [Toggle] to the right during setting of RX, TX frequencies and performing Up/Down operation allows frequencies to be changed in MHz steps.
- 8. The RX and TX frequencies can be entered with the number pad keys.

#### Flow Chart



#### 3-3. Group Setting Mode

Set data for each Group while in this mode. After first entering Self programming mode, select the "GROUP" display with [Top 1] [Top 2] and press [PTT] to set Group Setting mode. Once in Group Setting mode, select the group that needs setting with the [Top 1] [Top 2] keys and press [PTT]. Next select the channel for setting with the [Top 1] [Top 2] keys and press [PTT]. The setting items and setting data will then appear so reset the data with the [Top 1] [Top 2] keys and press [PTT]. When finished, the display shifts to the next setting item. After finished setting all items press [PTT] to return to next Group selection.

No.	Function name	Display	Remarks
	Select Group	GRP 1	1~160
1	Battery save	BATT L	OFF, Short, Mid, Long
2	Time out timer	TOT 60	OFF, 30s~300s (30s step)
3	TOT pre alert	TOT.P 10	OFF, 1s~10s (1s step)
4	TOT rekey time	TOT.K 2	OFF, 1s~60s (1s setp)
5	TOT reset time	TOT.S 2	OFF, 1s~15s (1s step)
6	Group Delete/Add	D/A ADD	Delete, Add
7	Signalling	SIG AND	AND, OR

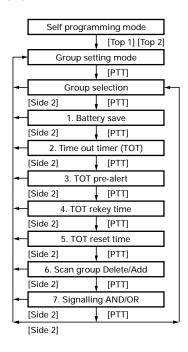
#### Operation

- 1. Select the setting value with the [Top 1] [Top 2] keys.
- 2. Press the [PTT] and the selected value is backed up and operation shifts to the next item for setting.
- 3. Press [Side 2] on the Group selection screen in order to return to the Self programming mode initial display.

#### Note

- 1. Different sample displays are shown.
- Setting item No.s are displayed with a 7-segment 2-digit figure on the LCD.
- Self programming mode cannot be set when set to Disable with the FPU.

#### Flow Chart



#### 3-4. Function Setting Mode

This mode allows making function settings for the transceiver. After first entering Self programming mode, select "FUNC" display with [Top 1] [Top 2] and then press [PTT] to set this mode. Function setting items are listed below.

No.	Function name	Display	Remarks
		,	
1	Power on tone	POW.T 15 (or C)	Continuas, 0~31
2	Control tone	CON.T 15 (or C)	Continuas, 0~31
3	Warning tone	WAR.T 15 (or C)	Continuas, 0~31
4	Alert tone	ALR.T 15 (or C)	Continuas, 0~31
5	Minimum volume	MIN.V 8	0~31
6	Battery warning	BATT ON	Disable, Enable
7	Busy LED	B.LED ON	OFF, ON
8	TX LED	T.LED ON	OFF, ON
9	Invert Display	LCD ON	Disabel, Enable
10	Priority 1	P1 NONE	None, Selected, Fixed
11	Priority 1 group	1.–1	1-1~10-16
12	Priority 1 channel	1–1.	(Priority 1= In fixed)
13	Priority 2	P2 NONE	None, Selected, Fixed
14	Priority 2 group	1.–1	1-1~10-16
15	Priority 2 channel	1–1.	(Priority 2= In fixed)
16	Revert channel	REVT 1	1~8
			1 : Selected
			2 : Last called
			3 : Last used
			4 : Sel+Talk back
			5 : Priority 1
			6 : Priority 1+Talk back
			7 : Priority 2
			8 : Priority 2+Talk back
17	Squelch level	SQ.LV 15	0~15

#### Operation

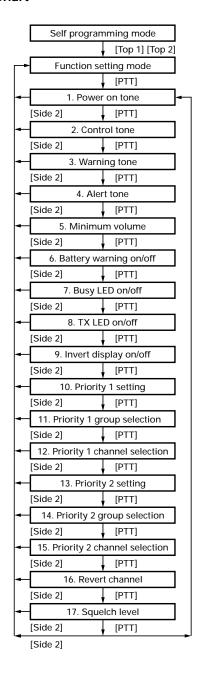
- 1. Select the setting value with the [Top 1] [Top 2] keys.
- 2. Press the [PTT] and the selected value is backed up and operation shifts to the next item for setting.
- 3. Press [Side 2] on the Group selection screen in order to return to the Self programming mode initial display.

#### Note

- 1. Different sample displays are shown.
- 2. Setting item No.s are displayed with a 7-segment 2-digit figure on the LCD.
- Self programming mode cannot be set when set to Disable with the FPU.

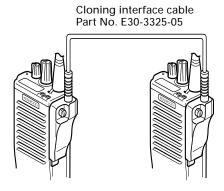
### REALIGNMENT

#### Flow Chart

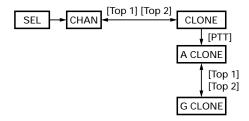


#### 3-5. Clone Mode

 Connect the cloning interface cable between the master side transceiver (source) and slave side transceiver (clone) as shown in the figure.



2. Set the master side transceiver to Self programming mode, and the transceiver display to "CLONE" with the [Top 1] key or [Top 2] key and press [PTT].



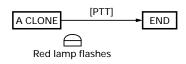
- 3. Set the power switch on the slave side transceiver to ON
- 4. The red LED on the master side transceiver flashes when cloning starts and an "END" message appears when cloning ends.
- 5. The green LED on the slave side transceiver flashes.
- 6. When a problem occurs during cloning, an "ERROR" message appears on the master side transceiver.
- 7. Pressing the [Side 2] key sets clone mode.

#### Note:

The master transceiver copies only to type matched slave.

#### All Clone

 Press [PTT] on the master side transceiver to start cloning of all data except for the transceiver model type and alignment data.



#### Group Clone

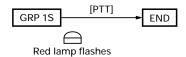
 To clone the transceiver group data and channel data within the group, as well as group alphanumeric data, use the [Top 1] key or [Top 2] key to switch the display on the master side transceiver from "A CLONE" to "G CLONE".



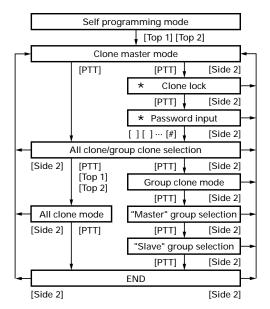
- 2. Press [PTT] to show the group cloning selection.
- 3. Select the group with the [Top 1] or [Top 2] keys.
- Press [PTT] to show the slave side group cloning selection.
- 5. Select the group with the [Top 1] or [Top 2] keys.



6. Press the master side [PTT] to start cloning.



#### Flow Chart



\* Only the keypad model and set to password with the FPU.

#### 4. Firmware Programming Mode

#### 4-1. Preface

Flash memory is mounted on the TK-390. This allows the TK-390 to be upgraded when new features are released in the future. (For details on how to obtain the firmware, contact Customer Service.)

#### 4-2. Connection Procedure

Connect the TK-390 to the personal computer (IBM PC or compatible) with the interface cable (KPG-36). (Connection is the same as in the PC Mode.)

#### 4-3. Programming

- 1. Start up the programming software (KPG-38D), select "firmware program" in the "Program" item, and press the Return key on the personal computer. This starts up the firmware programmer.
- 2. The top screen is displayed. Press any key to advance to the next screen.
- 3. Set the communications speed (normally, 38400 bps) and communications port in the Setup item.
- 4. Set the firmware to be updated by File select (=F1).
- Held down the [Side 2] and [PTT]. Turn the TK-390 power on, and release [Side 2] first. Until the display change to "PROGRAM"
- Check the connection between the TK-390 and the personal computer, and make sure that the TK-390 is in the Program mode.
- 7. Press F10 on the personal computer. A window opens on the display to indicate progress of writing. When the TK-390 starts to receive data, "PG" is appeared on 2 digit sub display.
- If writing ends successfully, the red LED on the TK-390 lights and the checksum is displayed.
- 9. If you want to continue programming other TK-390s, repeat steps 5 to 8.

#### Notes:

- To start the Firmware Programmer from KPG-38D, the Fpro path must be set up by KPG-38D setup.
- This mode cannot be entered if the Main Program mode is set to Disable in the Programming software (KPG-38D).

#### 4-4. Function

- 1. If you press the [Top 2] switch while "PROGRAM" is displayed, the checksum is displayed. If you press the [Top 1] switch while the checksum is displayed, "PROGRAM" is redisplayed.
- If you press the [Top 1] switch while "PROGRAM" is displayed, 1 dot light ("M") to indicate that the write speed is low-speed (19200 bps). If you press the [Top 1] switch again while low-speed (19200 bps), 2 dot lights ("M") to indicate, and the write speed becomes the high-speed mode (38400 bps).

#### Note:

Normally, write in the high-speed mode.

#### 5. Panel Test Mode

#### 1. Overview

The KENWOOD model TK-390 is a UHF/FM hand-held transceiver designed to operate in the frequency range of 450 to 490MHz, the unit consists of a receiver, a transmitter, a phase-locked loop (PLL) frequency synthesizer, power supply circuits, a control unit.

#### 2. Circuit Configuration by Frequency

The receiver is a double-conversion superheterodyne with a first intermediate frequency (IF) of 44.85MHz and a second IF of 455kHz. Incoming a signals from the antenna are mixed with the local signal from the PLL to produce the first IF of 44.85MHz.

This is then mixed with the 44.395MHz second local oscillator output to produce the 455kHz second IF. This is detected to give the demodulated signal.

The transmit signal frequency is generated by the PLL VCO, and modulated by the signal from the microphone. It is then amplified and sent to the antenna.

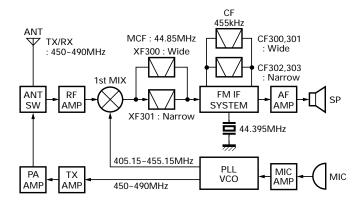


Fig. 1 Frequency configuration

#### 3. Receiver System

#### 3-1. Front-end RF amplifier

The signal coming from the antenna passes through the transmit/receive switching diode circuit, is amplified by the RF amplifier (Q200), and passes through a BPF (L205). The resulting signal is further amplified by the RF amplifier (Q201), passes through a BPF (L210) and goes to the mixer. The band-pass filters (L205 and L210) are two-pole helical resonators that uses varactor diode tuning to reject unwanted signal components.

#### 3-2. First mixer

The 1st mixer uses the GaAs IC (IC200). The 1st mixer mixes the signal with the 1st local oscillator frequency from the VCO, and converts it to the 1st IF (44.85MHz).

The signal then passes through monolithic crystal filter (XF300; Wide, XF301; Narrow) to remove unnecessary nearby frequency components. The signal from the MCF is used as the 1st IF signal.

#### 3-3. IF amplifier

The 1st IF signal is amplified (Q302) and fed into IC300 in the FM IF IC. The IF signal is then mixed with the 2nd local oscillator frequency of 44.395MHz to generate the 2nd IF of 455kHz. The 455kHz signal is then passed through a ceramic filter (CF300, CF301; Wide, CF302, CF303; Narrow) and fed back into IC300 for additional amplification.

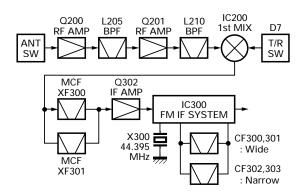


Fig. 2 Receiver section

Item	Rating
Nominal center frequency	44.85MHz
Pass band width	±5~7kHz or more at 3dB
Attenuation band width	±25kHz or less at 3dB
Ripple	1.0dB or less
Insertion loss	4dB or less
Guaranteed attenuation	80dB or more at fo±910kHz
	40dB or more within fo±1MHz
Terminating impedance	350 <b>Ω</b> / 4.5pF

Table 1 Crystal filter XF300 (L71-0523-05): Wide

Item	Rating
Nominal center frequency	44.85MHz
Pass band width	±3.75kHz or more at 3dB
Attenuation band width	±12.5kHz or less at 25dB
Ripple	1.0dB or less
Insertion loss	4dB or less
Guaranteed attenuation	80dB or more at fo±910kHz
	40dB or more within fo±1MHz
Terminating impedance	350Ω / 4.5pF

Table 2 Crystal filter XF301 (L71-0502-05): Narrow

#### 3-4. Wide/Narrow changeover circuit

Narrow and Wide settings can be made for each channel by switching the MCF; XF300 (Wide), XF301 (Narrow) with the ceramic filters CF300, CF301 (Wide), CF302, CF303 (Narrow).

The WIDE (high level) and NARROW (low level) data is output from IC400 (I/O port expansion) pin 4.

When a WIDE (high level) data is received, Q300, Q303 turn off and Q301, Q304 turn on. When a NARROW (low level) data is received, Q300, Q303 turn on and Q301, Q304 turn off. D300, D301 switch to MCF or D302, D303 are switched to ceramic filters when a high/low level data is received.

Q305 turns on/off with the Wide/Narrow data and the IC300 detector output level is changed to maintain a constant output level during wide or narrow signals.

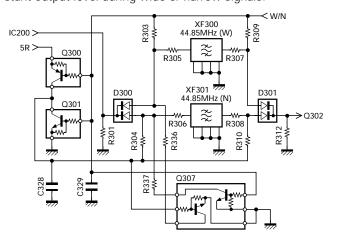


Fig. 3 Wide/Narrow changeover circuit

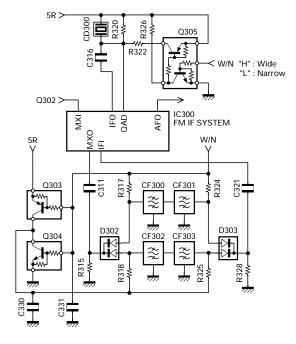


Fig. 4 Wide/Narrow changeover circuit

#### 3-5. Audio amplifier circuit

#### TX-RX unit

The demodulated signal from IC300 goes through IC301, and is amplified by IC601 (2/2), high-pass filtered, low-pass filtered, high-pass filtered, band-eliminate filtered, and deemphasized by IC607.

The signal then goes through an electronic volume control (IC603), an AF amplifier IC604 (2/2), and an AF switch (Q8 is on and Q7 is on of the control unit), and is routed to audio power amplifier (IC1 of the control unit), where it is amplified and output to the internal speaker.

#### Control unit

The audio mute signal (AMP SW) from the microprocessor becomes Low in the standby and Q5, Q6 which are power supply circuit for IC1 turn off. When the audio is output, AMP SW becomes High to turn Q5, Q6 on, and voltage is supplied to power terminal VP of IC1.

Speaker switching is done from IC403 (TX-RX unit) by INT AFC or EXT AFC. First, the logic level at the speakers switching terminal (SSW) on the universal connector is input to the microprocessor (IC406 TX-RX unit). The microprocessor then outputs data to IC403 based on this input.

When there is no SP-MIC installed, this logic level is high. When the INT AFC is high, the EXT AFC goes low, so the AF signal is only input to the amplifier for the internal speaker (INT SP) of IC1. However, when a SP-MIC has been installed, this logic level is low, so the INT AFC goes low and the EXT AFC goes high. So that the AF signal is input only to amplifier for the external speaker (EXT SP) of IC1.

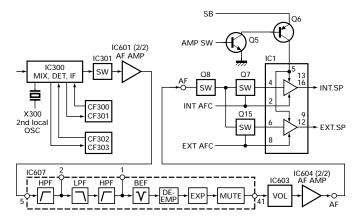


Fig. 5 Audio amplifier circuit

#### 3-6. Squelch circuit

The output signal from the squelch circuit, which consists of IC605 (2/2) and Q600, is applied to the microprocessor. The microprocessor passes information to the shift register (IC403) and it controls the mute control lines (AMP SW, INT AFC, and EXT AFC) according to the input signal (noise pulse) and the microprocessor task condition.

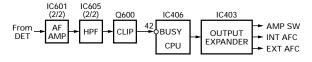


Fig. 6 Squelch circuit

#### 4. Transmitter System

#### 4-1. Microphone amplifier

The signal from IC3 (control unit) goes through the mute switch (Q403).

When the SP-MIC is not attached, the microphone switching terminal (MSW) on the universal connector becomes High, and mute switch (Q403) is turned on. When the SP-MIC is attached, MSW is connected to GND at inside of SP-MIC. For this reason, Q403 is turned off, the internal microphone is muted, and only the input of the external microphone is supplied to the microphone amplifier of the TX-RX unit.

The signal from microphone passes through the limiter circuit in D601, and through the high-pass filter, the ALC circuit, the low-pass filter, the high-pass filter, and pre-emphasis/IDC circuit in IC607. When encoding DTMF, mute switch (Q601) is turned off for muting the microphone input signal.

The signal passes through the D/A converter (IC603) for the maximum deviation adjustment, and enters the summing amplifier consisting of IC605 (1/2), and is mixed with the low speed data from the CPU (IC406).

The output signal from the summing amplifier goes to the VCO modulation input.

The other output signal from the summing amplifier passes through the D/A converter (IC603) again for the BAL adjustment, and the buffer amplifier (IC604 1/2), and goes to the VCXO modulation input.

#### 4-2. Noise cancelling microphone circuit

The two signals from INT MIC (Main & Sub) are input to the positive (+) input (Sub) and to the negative (-) input (Main) of the IC3. If the same signal is input to both Main and Sub, the Main signal is canceled at the output of IC3 (pin 7). In other words, noise from nearby sources not directly connected to the transceiver enters the Main and Sub inputs at the same signal and is therefore canceled out.

When a signal is only input to Main and there is no signal at Sub, the Main signal is output as is, from IC3 (pin 7). In other words, only the voice audio of the operator in extermely close proximity to the Main MIC is input to Main so that the signal is output as is from IC3 (pin 7). Also, when the "N/C" switch is set to "L", transistor Q14 turns off so Sub microphone turns off and operation is the same as above.

#### 4-3. Drive and Final amplifier

The signal from the T/R switch (D7 is on) is amplified by the pre-drive (Q6) and drive amplifier (Q8) to 20mW. The output of the drive amplifier is amplified by the RF power amplifier (IC501) to 4W (1W when the power is low).

The RF power amplifier consists of two stages MOS FET transistor. The output of the RF power amplifier is then passed through the Transmit-Receive (TX-RX) antenna switching (D10 is on) and the harmonic filter (LPF) and the Internal-External (INT-EXT) antennal switching (in the universal connector) and applied to the antenna terminal.

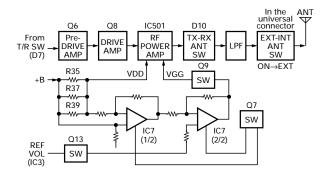


Fig. 8 Drive and final amplifier and APC circuits

#### 4-4. Internal-External (INT-EXT) antenna switching

The INT-EXT antenna switch housed inside the universal connector only switches to the EXT ANT side when an antenna speaker-microphone has been installed.

This INT-EXT antenna switch works mechanically and switches based on the operation shown in Figure 9.

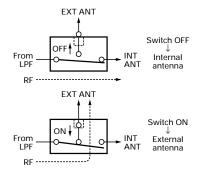


Fig. 9 Internal-External antenna switching

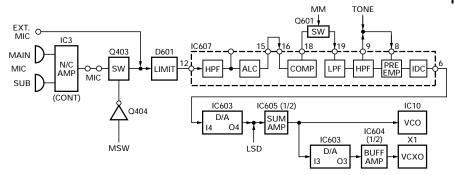


Fig. 7 Microphoen circuit

#### 4-5. APC circuit

The APC circuit always monitors the current flowing through the RF power amplifier (IC501) and keeps a constant current. The voltage drop at R35, R37, and R39 is caused by the current flowing through the RF power amplifier and this voltage is applied to the differential amplifier (IC7 1/2).

IC7 (2/2) compares the output voltage of IC7 (1/2) with the reference voltage from IC3, and the output of IC7 (2/2) controls the VGG of the RF power amplifier to make the both voltages to same voltage.

The change of power high/low is carried out by the change of the reference voltage. Q7, Q9, and Q13 are turned on in transmit and the APC circuit is active. (See Figure 8)

#### 5. PLL Frequency Synthesizer

The frequency synthesizer consists of the VCXO (X1), VCO (IC10), PLL IC (IC5) and buffer amplifiers.

The VCXO generates 16.8MHz. The frequency stability is within 2.0ppm (temperature range of -30 to +60°C). The frequency tuning and modulation of the VCXO are done to apply a voltage to pin 1 of the VCXO. The output of the VCXO is applied to pin 8 of the PLL IC.

The VCO of TK-390 covers the 40MHz spread, setting frequencies in r1, r2 (receive) and t1, t2 (transmit) with a bias voltage applied to the –V terminal of the VCO. A zero (0) volt bias is applied at frequencies lower than r1, t1. Frequencies r1, t1 through r2, t2 are biased with –3 volts. Frequencies higher than r2, t2 are biased with –6 volts.

The relation of VCO frequency versus PLL lock voltage is shown in Figure 11.

The output of the VCO is amplified by the buffer amplifier (Q3) and routed to the pin 5 of the PLL IC. Also the output of the VCO is amplified by the buffer amplifier (Q5) and routed to the next stage according to T/R switch (D7).

The PLL IC consists of a prescaler, fractional divider, reference divider, phase comparator, charge pump. This PLL IC is fractional-N type synthesizer and performs is the 40 or 50kHz reference signal which is eighth of the channel step (5 or 6.25kHz). The input signal from the pins 1 and 5 of the PLL IC is divided down to the 40 or 50kHz and compared at phase comparator. The pulsed output signal of the phase comparator is applied to the charge pump and transformed into DC signal in the loop filter (LPF). The DC signal is applied to the pin 4 of the VCO and locked to keep the VCO frequency constant.

PLL data is output from DT (pin21), CLK (pin 22) and LE (pin 20) of the microprocessor (IC406). The data are input to the PLL IC when the channel is changed or when transmission is changed to reception and vice versa. A PLL lock condition is always monitored by the pin 28 (UL) of the microprocessor. When the PLL is unlocked, the UL goes low.

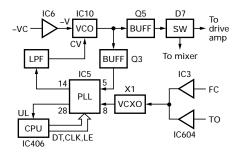


Fig. 10 PLL block diagram

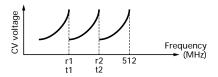


Fig. 11 CV voltage vs frequency

#### 6. Power Supply Circuit

Battery +B is supplied via a 3A fuse from the battery terminal connected to the TX-RX unit. After passing through the power switch power supply (SB) is applied to the two AVR ICs, and AVR circuit.

IC401 supplies 5V (5CM) to the control circuit. IC402 supplies 5V (5M) to the common circuit.

AVR circuit (Q400, Q402, Q405, Q406) supplies voltage to the TX circuit and the RX circuit. 5C is common 5V and output when SAVE is not set at off. 5R is 5V for reception and output during reception. 5T is 5V for transmission and output during transmission.

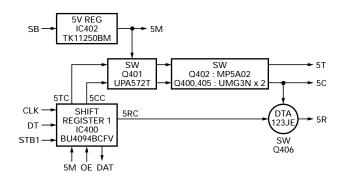


Fig. 12 Power supply circuit

#### 7. Control Circuit

The control unit consists of microprocessor IC406 and its peripheral circuits. It controls the TX-RX unit and transfers data to and from the control unit. The CPU (IC406) mainly performs the following:

- 1) Switching between transmission and reception by PTT signal input.
- Reading channel, frequency, and program data from the memory circuit.
- 3) Sending frequency program data to the PLL.
- 4) Controlling squelch on/off by the pulse signal input from the squelch circuit.
- 5) Controlling the audio mute circuit by decode data input.
- 6) Transmitting encode data (QT, DTQ).
- 7) Sending serial data to output expander (IC400, IC403, IC404 and IC405) to control various function in the unit.

#### 7-1. Memory circuit

IC406 has a flash memory with a capacity of 1M bits that contains the transceiver control program for the CPU and data such as transceiver channels and operating features.

This program can be easily written from an external devices. Data, such as DTMF memories and operating status, are programmed into the EEPROM (IC412).

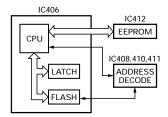


Fig. 13 Memory circuit

#### 7-2. CPU clock shift

When the CPU (IC406) 14MHz clock (X400) high frequency (actually the integral double high frequency of 7MHz because it is halved) is multiplexed with the reception frequency, it becomes an internal beat signal, suppressing the signal sensitivity. To prevent this, by turning Q407 on, the clock frequency is shifted (about 4kHz).

(Shift on/off can be set through programming.)

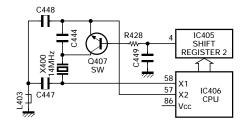


Fig. 14 CPU clock frequency shift

#### 7-3. Shift register

IC400, 403, 404 and 405 is an interface IC for I/O port expansion. It is used to expand the CPU (IC406) output ports.

#### 7-4. D/A converter

IC3 and IC603 is used as a conventional semi-fixed-resistor converter. It sets the following:

- 1) RX sensitivity
- 2) Transmission power
- 3) Modulation level
- 4) Audio power
- 5) Frequency

#### 7-5. Key input

It the clock is supplied to CLK terminal when the RES terminal (CPU pin 70) of the decade counter (IC2) is set to Low, Q0 to Q7 become High sequentially. Normally, KI1 and KI2 are Low (pulled down). When any key is pressed, KI1 or KI2 become High. The CPU detects which key is pressed, according to the voltage of KI1 and KI2 and clock timing.

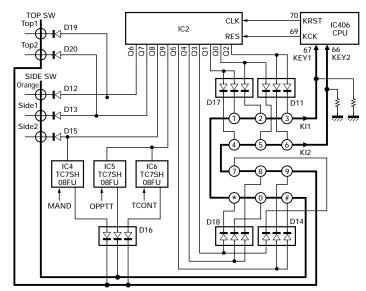


Fig. 15 Key input

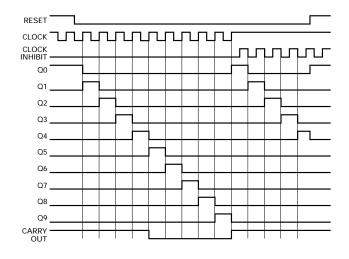


Fig. 16 Decade counter timing chart

#### 7-6. Low battery warning

The battery voltage is monitored by the microprocessor (IC406). When the battery voltage falls below the voltage set by the Low Battery Warning adjustment, the red LED flashes to notify the operator that it is time to replace the battery. If the battery voltage falls even more (approx. 5.8V), a beep sounds and transmission is stopped.

Low battery warning	Battery condition
The red LED flashes during	The battery voltage is low but
transmission	the transceiver is still usable
The red LED flashes and	The battery voltage is low and
continuous beep sounds	the transceiver is not usable to
while PTT pressed.	make calls.

#### 8. Signalling Circuit

#### 8-1. Encode

The CPU (IC406) transmits the encode data selected by the program.

#### Low-speed data (QT, DQT)

Low-speed data is output from pin 35 of the CPU. The signal passes through a low-pass CR filter, and goes to the summing amplifier (IC605 1/2). The signal is mixed with the audio signal and goes to the VCO (IC10) and VCXO (X1) modulation input after passing through the D/A converter (IC603) for BAL adjustment.

#### High-speed data (DTMF)

High-speed data is output from pin 36 of the CPU. The signal passes through a low-pass filter consisting of IC413, and provides a TX DTMF tone and a RX DTMF tone including a beep tone. The TX DTMF tone is passed to the D/A convertor (IC603) for DTMF deviation adjustment, and then applied to the audio processor (IC607).

The signal is mixed with the audio signal and goes to the VCO and VCXO. The RX DTMF tone is passed the D/A convertor (IC603) for audio control, summing amplifier (IC604 2/2), audio power amplifier and then to the speaker.

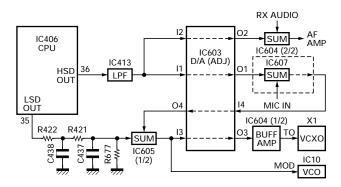


Fig. 17 Encode

#### 8-2. Decode

#### Low-speed data (QT, DQT)

The demodulated signal from the FM IF IC (IC300) is amplified by IC601 (2/2) and passes through a low-pass filter (IC602) to remove audio components. The signal is input to pin 26 of the CPU.

The CPU digitizes this signal, performs processing such as DC restoration, and decodes the signal.

#### High-speed data (DTMF)

The DTMF input signal from the FM IF IC (IC300) is amplified by IC601 (2/2) and goes to IC600, the DTMF decoder. The decoded information is then processed by the CPU. During transmission and standby, the DTMF IC is set to the power down mode when the PD terminal is High. When the line is busy, the PD terminal becomes Low, the power down mode is canceled and decoding si carried out.

#### · High-speed data (2 tone)

The demodulated signal from the FM IF IC (IC300) is amplified by IC601 (2/2) and passes through an audio processor (IC607) and band-pass filter (IC606) to remove a low-speed data.

The CPU digitizes this signal, performs processing such as DC restoration, and decodes the signal.

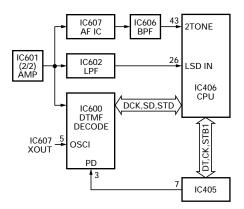


Fig. 18 Decode

### 9. Option Board Terminal

Terminals for mounting the option board are provided at the bottom of the TX-RX unit. The table below shows the correspondence between the board and terminals. Disconnect R414 and R665 in TX-RX unit when the option board is attached.

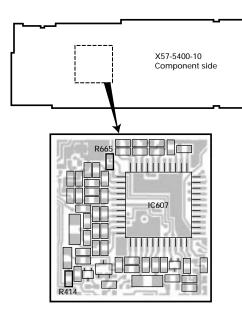


Fig. 19

#### 9-1. Option port 1 (For ANI board etc.)

Name	Function	Note
DATI	Data input	Reference
	Received signal to board	1kHz STD Dev →
		250~350mVrms
DATO	Data output	Reference
	Modulation (ANI) output	1kHz/150mVrms →
	from board	2.5~3.5kHz/wide
		1.25~1.75kHz/narrow
TCONT	SP amp control from board	
STONE	Side tone from board	
AUDIH	MIC inhibit from board	
E	Ground	
MUTE	AF mute from board	
SCALL	Sel call LED port from board	No connection
AUX	EM CH request from board	EM : Emergency
PTT	PTT logic to baord	
MAND	Man down logic to board	
EMERG	EM CH logic to board	EM : Emergency
BUSY	Busy logic to baord	
KEY	TX request from board	TX : Transmission
A+	Power supply for board	Switched

#### 9-2. Option port 2 (For voice scrambler etc.)

Name	Function	Note
CODE1	Scramble code select port 1	LSB
CODE2	Scramble code select port 2	
CODE3	Scramble code select port 3	
CODE4	Scramble code select port 4	MSB
TXOUT	Modulation output from	Reference
	board	1kHz/15mVrms →
		2.5~3.5kHz/wide
		1.25~1.75kHz/narrow
RXOUT	Received signal output from	
	board	
NC	No Connection	
E	Ground	
PTTIO	TX (Low) / RX (High) to board	
CLR/C	Clear (High) / Scramble (Low)	
(AUX)	to board (C : means Code)	
RXAEN	Power save request for	
	board Save : Low	
RXIN	Received signal input to	Reference
	board	1kHz STD Dev →
		250~350mVrms
TXIN	Modulation input to board	
+V	Power supply for board	Switched

# **SEMICONDUCTOR DATA**

# 1. Microprocessor : MC-8800-802 (TX-RX Unit IC406) 1-1. Terminal function

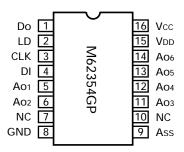
Pin No.	Port name	I/O	Function	
1~4	A7~A4	Ι	NC : Flash memory address bus	
5~7	AD5~AD3	I/O	NC : Flash memory address and data bus	
8~11	A3~A0	Ι	NC : Flash memory address bus	
12	Vcc	-	+5V.	
13	OE	Ι	GND : Address latch output enable	
14	VSS	Ι	GND : Test port	
15~18	CH A~CH D	Ι	Rotary SW 1~4	
19	TGL	Ι	Toggle switch input	
20	LE	0	PLL enable	
21	DATA	0	Common data	
22	CLOCK	0	Common clock	
23	VOL	Ι	Volume level input	
24	PF	Ι	SP key 1/2	
25	TEMP	Ι	TEMP	
26	LSDIN	Ι	Low speed data input (QT/DQT)	
27	BATT	Ι	Battery voltage input	
28	UL	Ι	PLL unlock detect input	
29	GND	-	GND	
30	MDATA	0	Modem data input	
31	EXSP	Ι	EXT SP Connect : L, Non connect : H	
32	AVDD	-	+5V	
33	AVREF1	-	+5V	
34	AVSS	-	GND	
35	LSDOUT	0	Low speed data output (QT/DQT)	
36	HSDOUT	0	High speed data output (DTMF/Beep)	
37	AVREF2	-	+5V	
38	AVREF3	-	GND	
39	SELF	I	Self programming enable/disable	
			H : Enable, L : Disable	
40	INTP0	I	μ-com stop	
41	STD	I	DTMF decoder STD	
42	BUSY	I	Noise pulse input	
43	2TONE	I	2 tone data input	
44	TRD	I	MSK modulation data output timing	
			pulse input	
45	RTM	Ι	MSK demodulation data input timing	
			pluse input	
46	SD	Ι	DTMF decoder SD	
47	RXD	Ι	Serial interface input	
48	TXD	0	Serial interface output	

49	Pin No.	Port name	I/O	Function
51         GND         -         GND           52         SDA         I/O         EEPROM data           53         LD         O         DA converter LD           54         MSTB         O         MSK modulation strobe           55         MSKE         O         MSK modulation enable           56         RESET         I         μ-com reset         Active: L           57,58         X2, X1         -         14.754MHz (System clock)           59         FCLR         O         MSK flame reset output           60         RDT         I         MSK demodulation data input           61,62         STB2, STB1         O         Shift register strobe 2, 1           63         SOE         O         Shift register output enable           64         PTT         I         PTT key input           65         LCDCS         O         LCD driver chip select output enable           64         PTT         I         Key counter return 2, 1           68         LCDDO         I         Radio kill data input for optional board           69         KCK         O         Key counter return 2, 1           68         LCDDO         I         Radio kill	49	DCK	0	DTMF clock
52         SDA         I/O         EEPROM data           53         LD         O         DA converter LD           54         MSTB         O         MSK modulation strobe           55         MSKE         O         MSK modulation enable           56         RESET         I         μ-com reset         Active: L           57,58         X2, X1         -         14.754MHz (System clock)           59         FCLR         O         MSK flame reset output           60         RDT         I         MSK demodulation data input           61,62         STB2, STB1         O         Shift register strobe 2, 1           63         SOE         O         Shift register output enable           64         PTT         I         PTT key input           65         LCDCS         O         LCD driver chip select output           66,67         KEY2, KEY1         I         Key counter return 2, 1           68         LCDDO         I         Radio kill data input for optional board           69         KCK         O         Key counter reset output           70         KRST         O         Key counter reset output           71         A10         I/O <td>50</td> <td>SCL</td> <td>0</td> <td>EEPROM clock</td>	50	SCL	0	EEPROM clock
53         LD         O         DA converter LD           54         MSTB         O         MSK modulation strobe           55         MSKE         O         MSK modulation enable           56         RESET         I         μ-com reset         Active: L           57,58         X2, X1         -         14.754MHz (System clock)           59         FCLR         O         MSK flame reset output           60         RDT         I         MSK demodulation data input           61,62         STB2, STB1         O         Shift register strobe 2, 1           63         SOE         O         Shift register output enable           64         PTT         I         PTT key input           65         LCDCS         O         LCD driver chip select output           66,67         KEY2, KEY1         I         Key counter return 2, 1           68         LCDDO         I         Radio kill data input for optional board           69         KCK         O         Key counter return 2, 1           68         LCDDO         I         Radio kill data input for optional board           69         KCK         O         Key counter reset output           71	51	GND	-	GND
54         MSTB         O         MSK modulation strobe           55         MSKE         O         MSK modulation enable           56         RESET         I         μ-com reset         Active: L           57,58         X2, X1         -         14.754MHz (System clock)           59         FCLR         O         MSK flame reset output           60         RDT         I         MSK demodulation data input           61,62         STB2, STB1         O         Shift register strobe 2, 1           63         SOE         O         Shift register output enable           64         PTT         I         PTT key input           65         LCDCS         O         LCD driver chip select output           66,67         KEY2, KEY1         I         Key counter return 2, 1           68         LCDDO         I         Radio kill data input for optional board           69         KCK         O         Key counter return 2, 1           68         LCDDO         I         Radio kill data input for optional board           70         KRST         O         Key counter return 2, 1           68         LCDDO         I         Radio kill data input for optional board	52	SDA	I/O	EEPROM data
55         MSKE         O         MSK modulation enable           56         RESET         I         μ-com reset         Active: L           57,58         X2, X1         -         14.754MHz (System clock)           59         FCLR         O         MSK flame reset output           60         RDT         I         MSK demodulation data input           61,62         STB2, STB1         O         Shift register strobe 2, 1           63         SOE         O         Shift register output enable           64         PTT         I         PTT key input           65         LCDCS         O         LCD driver chip select output           64         PTT         I         PTT key input           65         LCDCS         O         LCD driver chip select output           66         KEY2, KEY1         I         Key counter return 2, 1           68         LCDDO         I         Radio kill data input for optional board           69         KCK         O         Key counter return 2, 1           68         LCDDO         I         Radio kill data input for optional board           70         KRST         O         Key counter reset output           71	53	LD	0	DA converter LD
56         RESET         I         μ-com reset         Active: L           57,58         X2, X1         -         14.754MHz (System clock)           59         FCLR         O         MSK flame reset output           60         RDT         I         MSK demodulation data input           61,62         STB2, STB1         O         Shift register strobe 2, 1           63         SOE         O         Shift register output enable           64         PTT         I         PTT key input           65         LCDCS         O         LCD driver chip select output           65         LCDCS         O         LCD driver chip select output           66,67         KEY2, KEY1         I         Key counter return 2, 1           68         LCDDO         I         Radio kill data input for optional board           69         KCK         O         Key counter return 2, 1           68         LCDDO         I         Radio kill data input for optional board           69         KCK         O         Key counter return 2, 1           68         LCDDO         I         Radio kill data input for optional board           70         KRST         O         Key counter return 2, 1	54	MSTB	0	MSK modulation strobe
57,58         X2, X1         -         14.754MHz (System clock)           59         FCLR         O         MSK flame reset output           60         RDT         I         MSK demodulation data input           61,62         STB2, STB1         O         Shift register strobe 2, 1           63         SOE         O         Shift register output enable           64         PTT         I         PTT key input           65         LCDCS         O         LCD driver chip select output           66,67         KEY2, KEY1         I         Key counter return 2, 1           68         LCDDO         I         Radio kill data input for optional board           69         KCK         O         Key counter return 2, 1           68         LCDDO         I         Radio kill data input for optional board           69         KCK         O         Key counter return 2, 1           68         LCDDO         I         Radio kill data input for optional board           69         KCK         O         Key counter reset output           70         KRST         O         Key counter reset output           71         A10         I/O         NC : Not used           72	55	MSKE	0	MSK modulation enable
59         FCLR         O         MSK flame reset output           60         RDT         I         MSK demodulation data input           61,62         STB2, STB1         O         Shift register strobe 2, 1           63         SOE         O         Shift register output enable           64         PTT         I         PTT key input           65         LCDCS         O         LCD driver chip select output           66,67         KEY2, KEY1         I         Key counter return 2, 1           68         LCDDO         I         Radio kill data input for optional board           69         KCK         O         Key counter recock output           70         KRST         O         Key counter reset output           71         A10         I/O         NC : Not used           72         RD         O         Flash ROM output enable           74         A17         I/O Flash memory address and data bus           75         CE         I         Flash memory chip enable           76         A11         I/O NC           78,79         -         -         NC : Not used           80         GND         -         GND           81,82 <td></td> <td>RESET</td> <td>Ι</td> <td>μ-com reset Active : L</td>		RESET	Ι	μ-com reset Active : L
60         RDT         I         MSK demodulation data input           61,62         STB2, STB1         O         Shift register strobe 2, 1           63         SOE         O         Shift register output enable           64         PTT         I         PTT key input           65         LCDCS         O         LCD driver chip select output           66,67         KEY2, KEY1         I         Key counter return 2, 1           68         LCDDO         I         Radio kill data input for optional board           69         KCK         O         Key counter recock output           70         KRST         O         Key counter reset output           71         A10         I/O         NC : Not used           72         RD         O         Flash ROM output enable           74         A17         I/O Flash memory address and data bus           75         CE         I         Flash memory chip enable           76         A11         I/O NC           77         A14         I/O NC           80         GND         -           81,82         A9, A8         I/O NC           85         WR         O         Flash ROM write bus	57,58	X2, X1	-	14.754MHz (System clock)
61,62         STB2, STB1         O         Shift register strobe 2, 1           63         SOE         O         Shift register output enable           64         PTT         I         PTT key input           65         LCDCS         O         LCD driver chip select output           66,67         KEY2, KEY1         I         Key counter return 2, 1           68         LCDDO         I         Radio kill data input for optional board           69         KCK         O         Key counter clock output           70         KRST         O         Key counter reset output           71         A10         I/O         NC : Not used           72         RD         O         Flash ROM read bus           73         OE         I         Flash ROM output enable           74         A17         I/O         Flash memory address and data bus           75         CE         I         Flash memory chip enable           76         A11         I/O         NC           78,79         -         -         NC : Not used           80         GND         -         GND           81,82         A9, A8         I/O         NC <td< td=""><td>59</td><td>FCLR</td><td>0</td><td>MSK flame reset output</td></td<>	59	FCLR	0	MSK flame reset output
63         SOE         O         Shift register output enable           64         PTT         I         PTT key input           65         LCDCS         O         LCD driver chip select output           66,67         KEY2, KEY1         I         Key counter return 2, 1           68         LCDDO         I         Radio kill data input for optional board           69         KCK         O         Key counter clock output           70         KRST         O         Key counter reset output           71         A10         I/O         NC : Not used           72         RD         O         Flash ROM read bus           73         OE         I         Flash ROM output enable           74         A17         I/O         Flash memory address and data bus           75         CE         I         Flash memory chip enable           76         A11         I/O         NC           77         A14         I/O         NC           80         GND         -         NC : Not used           81,82         A9, A8         I/O         NC           83,84         A13, A14         I/O         NC           86         Vcc	60	RDT	-1	MSK demodulation data input
64         PTT         I         PTT key input           65         LCDCS         O         LCD driver chip select output           66,67         KEY2, KEY1         I         Key counter return 2, 1           68         LCDDO         I         Radio kill data input for optional board           69         KCK         O         Key counter clock output           70         KRST         O         Key counter reset output           71         A10         I/O         NC : Not used           72         RD         O         Flash ROM read bus           73         OE         I         Flash ROM output enable           74         A17         I/O         Flash memory address and data bus           75         CE         I         Flash memory chip enable           76         A11         I/O         NC           77         A14         I/O         NC           78,79         -         -         NC : Not used           80         GND         -         GND           81,82         A9, A8         I/O         NC           83,84         A13, A14         I/O         NC           85         WR         O	61,62	STB2, STB1	0	9
65         LCDCS         O         LCD driver chip select output           66,67         KEY2, KEY1         I         Key counter return 2, 1           68         LCDDO         I         Radio kill data input for optional board           69         KCK         O         Key counter clock output           70         KRST         O         Key counter reset output           71         A10         I/O         NC : Not used           72         RD         O         Flash ROM output enable           73         OE         I         Flash memory address and data bus           75         CE         I         Flash memory chip enable           76         A11         I/O         NC           77         A14         I/O         NC           78,79         -         -         NC : Not used           80         GND         -         GND           81,82         A9, A8         I/O         NC           83,84         A13, A14         I/O         NC           85         WR         O         Flash ROM write bus           86         Vcc         -         +5V           87,88         A16, A15         I/O	63	SOE	0	Shift register output enable
66,67         KEY2, KEY1         I         Key counter return 2, 1           68         LCDDO         I         Radio kill data input for optional board           69         KCK         O         Key counter clock output           70         KRST         O         Key counter reset output           71         A10         I/O         NC : Not used           72         RD         O         Flash ROM read bus           73         OE         I         Flash ROM output enable           74         A17         I/O         Flash memory address and data bus           75         CE         I         Flash memory chip enable           76         A11         I/O         NC           77         A14         I/O         NC           78,79         -         -         NC : Not used           80         GND         -         GND           81,82         A9, A8         I/O         NC           83,84         A13, A14         I/O         NC           85         WR         O         Flash ROM write bus           86         Vcc         - +5V           87,88         A16, A15         I/O         A16, A15 : Flash	64	PTT	Ι	
68         LCDDO         I         Radio kill data input for optional board           69         KCK         O         Key counter clock output           70         KRST         O         Key counter reset output           71         A10         I/O         NC : Not used           72         RD         O         Flash ROM read bus           73         OE         I         Flash ROM output enable           74         A17         I/O         Flash memory address and data bus           75         CE         I         Flash memory chip enable           76         A11         I/O         NC           77         A14         I/O         NC           78,79         -         -         NC : Not used           80         GND         -         GND           81,82         A9, A8         I/O         NC           83,84         A13, A14         I/O         NC           85         WR         O         Flash ROM write bus           86         Vcc         -         +5V           87,88         A16, A15         I/O         A16, A15 : Flash memory address bus           91         A12         I/O	65	LCDCS	0	LCD driver chip select output
69         KCK         O         Key counter clock output           70         KRST         O         Key counter reset output           71         A10         I/O         NC : Not used           72         RD         O         Flash ROM read bus           73         OE         I         Flash ROM output enable           74         A17         I/O         Flash memory address and data bus           75         CE         I         Flash memory chip enable           76         A11         I/O         NC           77         A14         I/O         NC           78,79         -         NC : Not used           80         GND         -         GND           81,82         A9, A8         I/O         NC           83,84         A13, A14         I/O         NC           85         WR         O         Flash ROM write bus           86         Vcc         -         +5V           87,88         A16, A15         I/O         A16, A15         Flash memory address bus           91         A12         I/O         NC         NC : Not used           93-95         AD2-AD0         I/O	66,67	KEY2, KEY1	I	3
70         KRST         O         Key counter reset output           71         A10         I/O         NC : Not used           72         RD         O         Flash ROM read bus           73         OE         I         Flash ROM output enable           74         A17         I/O         Flash memory address and data bus           75         CE         I         Flash memory chip enable           76         A11         I/O         NC           77         A14         I/O         NC           78,79         -         -         NC : Not used           80         GND         -         GND           81,82         A9, A8         I/O         NC           83,84         A13, A14         I/O         NC           85         WR         O         Flash ROM write bus           86         Vcc         -         +5V           87,88         A16, A15         I/O         A16, A15 : Flash memory address bus           91         A12         I/O         NC           92         NC         -         NC : Not used           93-95         AD2-AD0         I/O         NC	68	LCDDO	-1	Radio kill data input for optional board
71         A10         I/O         NC : Not used           72         RD         O Flash ROM read bus           73         OE         I Flash ROM output enable           74         A17         I/O Flash memory address and data bus           75         CE         I Flash memory chip enable           76         A11         I/O NC           77         A14         I/O NC           78,79         -         - NC : Not used           80         GND         - GND           81,82         A9, A8         I/O NC           83,84         A13, A14         I/O NC           85         WR         O Flash ROM write bus           86         Vcc         - +5V           87,88         A16, A15         I/O A16, A15           89,90         A16, A15         I A16, A15 : Flash memory address bus           91         A12         I/O NC           92         NC         - NC : Not used           93~95         AD2~AD0         I/O NC           96,97         AD7, AD6         I/O NC           98,99         GND         - GND	69	KCK	0	
72         RD         O         Flash ROM read bus           73         OE         I         Flash ROM output enable           74         A17         I/O         Flash memory address and data bus           75         CE         I         Flash memory chip enable           76         A11         I/O         NC           77         A14         I/O         NC           78,79         -         -         NC : Not used           80         GND         -         GND           81,82         A9, A8         I/O         NC           83,84         A13, A14         I/O         NC           85         WR         O         Flash ROM write bus           86         Vcc         -         +5V           87,88         A16, A15         I/O         A16, A15           89,90         A16, A15         I         A16, A15 : Flash memory address bus           91         A12         I/O         NC           92         NC         -         NC : Not used           93-95         AD2-AD0         I/O         NC           96,97         AD7, AD6         I/O         NC           98,99	70	KRST	0	
73         OE         I         Flash ROM output enable           74         A17         I/O         Flash memory address and data bus           75         CE         I         Flash memory chip enable           76         A11         I/O         NC           77         A14         I/O         NC           78,79         -         -         NC : Not used           80         GND         -         GND           81,82         A9, A8         I/O         NC           83,84         A13, A14         I/O         NC           85         WR         O         Flash ROM write bus           86         Vcc         -         +5V           87,88         A16, A15         I/O         A16, A15           89,90         A16, A15         I         A16, A15 : Flash memory address bus           91         A12         I/O         NC           92         NC         -         NC : Not used           93-95         AD2-AD0         I/O         NC           96,97         AD7, AD6         I/O         NC           98,99         GND         -         GND	71	A10	I/O	NC : Not used
74         A17         I/O         Flash memory address and data bus           75         CE         I         Flash memory chip enable           76         A11         I/O         NC           77         A14         I/O         NC           78,79         -         -         NC : Not used           80         GND         -         GND           81,82         A9, A8         I/O         NC           83,84         A13, A14         I/O         NC           85         WR         O         Flash ROM write bus           86         Vcc         -         +5V           87,88         A16, A15         I/O         A16, A15           89,90         A16, A15         I         A16, A15 : Flash memory address bus           91         A12         I/O         NC           92         NC         -         NC : Not used           93-95         AD2-AD0         I/O         NC           96,97         AD7, AD6         I/O         NC           98,99         GND         -         GND	72	RD	0	Flash ROM read bus
75         CE         I         Flash memory chip enable           76         A11         I/O         NC           77         A14         I/O         NC           78,79         -         -         NC : Not used           80         GND         -         GND           81,82         A9, A8         I/O         NC           83,84         A13, A14         I/O         NC           85         WR         O         Flash ROM write bus           86         Vcc         -         +5V           87,88         A16, A15         I/O         A16, A15           89,90         A16, A15         I         A16, A15 : Flash memory address bus           91         A12         I/O         NC           92         NC         -         NC : Not used           93-95         AD2-AD0         I/O         NC           96,97         AD7, AD6         I/O         NC           98,99         GND         -         GND	73	OE	Ι	
76         A11         I/O         NC           77         A14         I/O         NC           78,79         -         -         NC : Not used           80         GND         -         GND           81,82         A9, A8         I/O         NC           83,84         A13, A14         I/O         NC           85         WR         O         Flash ROM write bus           86         Vcc         -         +5V           87,88         A16, A15         I/O         A16, A15         :Flash memory address bus           91         A12         I/O         NC           92         NC         -         NC : Not used           93-95         AD2-AD0         I/O         NC           96,97         AD7, AD6         I/O         NC           98,99         GND         -         GND	74	A17	I/O	Flash memory address and data bus
77 A14 I/O NC 78,79 NC: Not used 80 GND - GND 81,82 A9, A8 I/O NC 83,84 A13, A14 I/O NC 85 WR O Flash ROM write bus 86 Vcc - +5V 87,88 A16, A15 I/O A16, A15 89,90 A16, A15 I A16, A15: Flash memory address bus 91 A12 I/O NC 92 NC - NC: Not used 93-95 AD2-AD0 I/O NC 96,97 AD7, AD6 I/O NC 98,99 GND - GND	75	CE	Ι	Flash memory chip enable
78,79         -         -         NC : Not used           80         GND         -         GND           81,82         A9, A8         I/O         NC           83,84         A13, A14         I/O         NC           85         WR         O         Flash ROM write bus           86         Vcc         -         +5V           87,88         A16, A15         I/O         A16, A15           89,90         A16, A15         I         A16, A15 : Flash memory address bus           91         A12         I/O         NC           92         NC         -         NC : Not used           93~95         AD2~AD0         I/O         NC           96,97         AD7, AD6         I/O         NC           98,99         GND         -         GND	76	A11	I/O	NC
80         GND         -         GND           81,82         A9, A8         I/O         NC           83,84         A13, A14         I/O         NC           85         WR         O         Flash ROM write bus           86         Vcc         -         +5V           87,88         A16, A15         I/O         A16, A15           89,90         A16, A15         I         A16, A15 : Flash memory address bus           91         A12         I/O         NC           92         NC         -         NC : Not used           93~95         AD2~AD0         I/O         NC           96,97         AD7, AD6         I/O         NC           98,99         GND         -         GND	77	A14	I/O	
81,82	78,79	_	-	NC : Not used
83,84         A13, A14         I/O         NC           85         WR         O         Flash ROM write bus           86         Vcc         -         +5V           87,88         A16, A15         I/O         A16, A15           89,90         A16, A15         I         A16, A15 : Flash memory address bus           91         A12         I/O         NC           92         NC         -         NC : Not used           93-95         AD2-AD0         I/O         NC           96,97         AD7, AD6         I/O         NC           98,99         GND         -         GND	80		-	GND
85         WR         O         Flash ROM write bus           86         Vcc         -         +5V           87,88         A16, A15         I/O         A16, A15           89,90         A16, A15         I         A16, A15 : Flash memory address bus           91         A12         I/O         NC           92         NC         -         NC : Not used           93~95         AD2~AD0         I/O         NC           96,97         AD7, AD6         I/O         NC           98,99         GND         -         GND	81,82		I/O	NC
86         Vcc         -         +5V           87,88         A16, A15         I/O         A16, A15           89,90         A16, A15         I         A16, A15 : Flash memory address bus           91         A12         I/O         NC           92         NC         -         NC : Not used           93~95         AD2~AD0         I/O         NC           96,97         AD7, AD6         I/O         NC           98,99         GND         -         GND	83,84	A13, A14	I/O	NC
87,88         A16, A15         I/O         A16, A15           89,90         A16, A15         I         A16, A15 : Flash memory address bus           91         A12         I/O         NC           92         NC         -         NC : Not used           93~95         AD2~AD0         I/O         NC           96,97         AD7, AD6         I/O         NC           98,99         GND         -         GND	85	WR	0	Flash ROM write bus
89,90       A16, A15       I       A16, A15 : Flash memory address bus         91       A12       I/O       NC         92       NC       -       NC : Not used         93~95       AD2~AD0       I/O       NC         96,97       AD7, AD6       I/O       NC         98,99       GND       -       GND	86	Vcc	-	+5V
91 A12 I/O NC 92 NC - NC : Not used 93-95 AD2-AD0 I/O NC 96,97 AD7, AD6 I/O NC 98,99 GND - GND	87,88	A16, A15	I/O	·
92 NC - NC : Not used 93-95 AD2-AD0 I/O NC 96,97 AD7, AD6 I/O NC 98,99 GND - GND	89,90	A16, A15	1	A16, A15 : Flash memory address bus
93~95 AD2~AD0 I/O NC 96,97 AD7, AD6 I/O NC 98,99 GND – GND	91	A12	I/O	NC
96,97 AD7, AD6 I/O NC 98,99 GND – GND	92	NC	-	NC : Not used
98,99 GND – GND	93~95	AD2~AD0	I/O	NC
·	96,97	AD7, AD6	I/O	NC
100 ASTR I/O NC : Flash memory address stroke	98,99	GND	-	
100   A310   110   110 . Hash memory address strobe	100	ASTB	I/O	NC : Flash memory address strobe

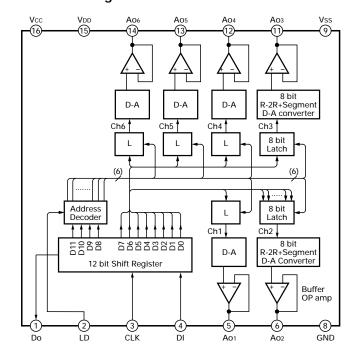
### **SEMICONDUCTOR DATA**

#### 2. D/A Converter: M62354GP (TX-RX Unit IC3)

#### 2-1. Terminal connection



#### 2-2. Block diagram

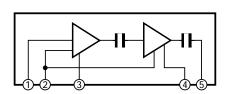


#### 2-3. Terminal function

Pin No.	Symbol	I/O	Function
1	Do	0	12-bit shift register MSB bit data is
			output.
2	LD	I	When the LD is at the high level,
			the 12-bit shift register value is
			loaded to the D/A output register.
3	CLK	I	Shift clock input. With the rise of
			the shift clock, the input signal from
			the DI is input to the 12-bit shift
			register.
4	DI	I	Serial data input. Input serial data
			12 bits long.
5,6	AO1, AO2	0	8-bit resolution D/A.
7	NC	-	Not connected.
8	GND	-	GND.
9	Vss	-	Terminal for determining the D/A
			conversion reference low side point
			level.
10	NC	-	Not connected.
11~14	AO3~AO6	0	8-bit resolution D/A.
15	VDD	_	Terminal for determining the D/A
			conversion reference up side point
			level.
16	Vcc	_	Power supply.

### 3. RF Power Amplifier: XHW2727-2 (Control Unit IC501)

#### 3-1. Block diagram



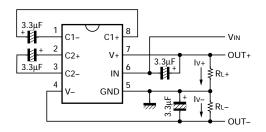
#### 3-2. Maximum ratings (Flange temperature = 25°C)

Rating	Symbol	Value	Unit
DC supply voltage (Pins 3,4)	VDD1,2	10	Vdc
DC control voltage (Pin 2)	Vcont	10	Vdc
RF input power	Pin	40	dBm
RF output power (VDD1,2=9V)	Pout	9	W
Operating case temperature range	Тс	-30 to +100	°C
Storage temperature range	Tstg	-30 to +100	°C

### **SEMICONDUCTOR DATA**

### 4. DC-DC Converter : MAX865 (TX-RX Unit IC4)

#### 4-1. Terminal connection



#### 4-2. Terminal description

Pin No.	Name	Function
1	C1-	Negative terminal of the flying boost capacitor.
2	C2+	Positive terminal of the flying inverting capacitor.
3	C2-	Negative terminal of the flying inverting capacitor.
4	V-	Output of the inverting charge pump.
5	GND	Ground
6	IN	Positive power supply input
7	V+	Output of the boost charge pump.
8	C1+	Positive terminal of the flying boost capacitor.

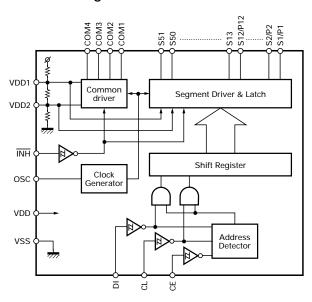
#### 4-3. Electrical characteristics

Parameter	Conditions	MIN	TYP	MAX	Units
Minimum supply voltage	Rload=10kΩ	2.0	1.5		V
Maximum supply voltage	Rload=10kΩ			6.0	V
Supply current	TA=+25°C		0.6	1.05	mA
	Ta=-40°C to +85°C (Note 1)			1.15	
Oscillator frequency	TA=+25°C	19.5	24	32.5	kHz
	Ta=-40°C to +85°C (Note 1)	18		34	

Note 1: These specifications are guaranteed by design and are not production tested.

### 5. LCD Driver: LC75824W (Control unit IC101)

#### 5-1. Block diagram



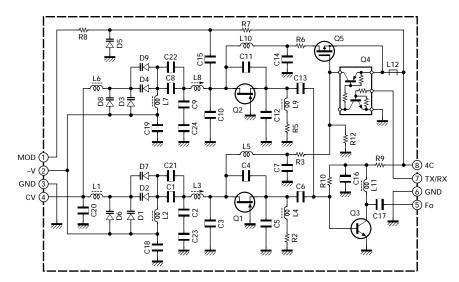
#### 5-2. Terminal function

Die Na Name 100 Autor							
Pin No.	Name	I/O	Active	Function			
1~12	S1/P1~S12/P12	0	_	Segment output for displaying			
13~51	S13~S51			data transferred from serial data.			
52~55	COM1~COM4	0	-	Common driver output.			
				Frame frequency fo=(fosc/512)Hz			
56	VDD	-	-				
57	VDD1	I	-	Apply 2/3 the LCD drive bias			
				voltage from outside. If 1/2 the			
				bias is applied, connect to VDD2.			
58	VDD2	I	-	Apply 1/3 the LCD drive bias			
				voltage from outside. If 1/2 the			
				bias is applied, connect to VDD1.			
59	VSS	-	-				
60	OSC	I/O	-	Oscillation terminal.			
61	ĪNH	I	L	Force the display to turn off			
				regardless of internal data. Serial			
				data can be input regardless of			
				whether it is "H" or "L".			
62	CE	I	Н	Chip enable. Serial data transfer			
				terminal. Connected to the			
				microprocessor.			
63	CL	Ι		Synchronizing clock. Serial data			
				transfer terminal. Connected to			
				the microprocessor.			
64	DI	Ι	-	Transfer data. Serial data transfer			
				terminal. Connected to the			
				microprocessor.			

### **SEMICONDUCTOR DATA / DESCRIPTION OF COMPONENTS**

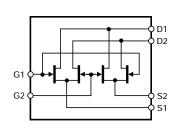
### 6. VCO System: KCH41 (TX-RX Unit IC10)

### 6-1. Circuit diagram



# 7. Active DBM : GN2011 (TX-RX Unit IC200)

### 7-1. Circuit diagram



### Control Unit (X53-3780-XX) -12 : K -13 : K4

Ref. No.	Use / Function	Operation / Condition
IC1	Audio power amplifier	0.5W/16Ω, BTL method
IC2	Decimal counter	
IC3	MIC noise canceling circuit	
IC4	Option board (MAND)	AND circuit
	control switch	MAND : "H", OUT : "H"
		when Q8 port is "H"
IC5	Option board (OPPTT)	AND circuit
	control switch	OPPTT : "H", OUT : "H"
		when Q9 port is "H"
IC6	Option board (TCONT)	AND circuit
	control switch	MAND : "H", OUT : "H"
		when Q9 port is "H"
IC101	LCD driver	
IC501	RF power amplifier	
Q5	AF AMP switch	AF AMP SW "H" : on
Q6	AVR	AF AMP switch power
		supply
Q7	Int. audio mute switch	INT. AFC "H" : INT. SP
		audio signal on
Q8	Audio mute switch	MUTE "L" : RX audio
	signal off	

Ref. No.	Use / Function	Operation / Condition
Q14	N/C switch	N/C switch "H" : N/C on
Q15	Ext. audio mute switch	EXT AFC "H" : EXT. SP
		audio signal on
D2	AVR	AF AMP power supply
D6~9	Key pad backlight	
D11	Reverse current prevention	
	for number key pad	
D12,13	Reverse current prevention	
	for PF key.	
D14	Reverse current prevention	
	for number key pad	
D15,16	Reverse current prevention	
	for PF key.	
D17,18	Reverse current prevention	
	for number key pad	
D19,20	Reverse current prevention	
	for PF key.	
D101	Speed up	
D102,103	LCD backlight	
D104	LED	Red, green

# **DESCRIPTION OF COMPONENTS**

### TX-RX Unit (X57-5400-10)

Ref. No.	Use / Function	Operation / Condition	Ref. No.	Use / Function	Operation / Condition
IC2	Level shift		Q200	RF amplifier	RX front 1st amplifier
IC3	D/A converter (adjustment)		Q201	RF amplifier	RX front 2nd amplifier
IC4	DC-DC converter	Outputs ± twice the	Q300	DC switch	1st IF W/N switch sets
		input voltage 5M			to on when Narrow
IC5	Phase locked loop system		Q301	DC switch	1st IF W/N switch sets
IC6	Level shift				to on when Wide
IC7	APC comparator		Q302	IF amplifier	Post amplifier
IC10	VCO system		Q303	DC switch	2nd IF W/N switch sets
IC200	Active DBM				to on when Narrow
IC300	FM IF system	2nd mixer, Quadrature	Q304	DC switch	2nd IF W/N switch sets
		detector, AF output,			to on when Wide
		Noise amplifier output,	Q305	DC switch	
		S-meter output	Q307	DC switch	
IC301	Audio mute switch		Q400	DC switch	5TC "H" : on
IC400	Shift register	Output expander	Q401	DC switch	5CC "H" : on,
IC401	Voltage regulator	5CM			5TC "H" : on
IC402	Voltage regulator	5M	Q402	DC switch	Regulator 5T, 5C
IC403~405	Shift register	Output expander	Q403	DC switch	MIC mute
IC406	Microprocessor	16 bit +1M flash	Q404	DC switch	MIC switch
IC407	Voltage detector	INTPO	Q405	DC switch	5CC "H" : on
IC408	Address decoder	AND gate	Q406	DC switch	5R
IC409	Voltage detector	Reset	Q407	Clock frequency shift	
IC410	Address decoder	OR gate	Q408	DC switch	TX LED (Red) driver
IC411	Address decoder	NOR gate	Q409	DC switch	Busy LED (Green) driver
IC412	EEPROM		Q410	DC switch	Lamp
IC413	Active filter	For HSD output	Q411	Current driver	Lamp : Backlight
IC600	DTMF decoder		Q600	DC switch	Clip
IC601	Buffer amplifier		Q601	Mute switch	MIC line mute
IC602	Active filter	LPF for LSD input	Q602	DC switch	
IC603	D/A converter (Adjustment)		Q603	DC switch	SQL : W/N adjustment
IC604	Summing amplifier/	AF/TO	D4	Noise rejection	
	Buffer amplifier		D6	Current steering	
IC605	Active filter/	SQL : HPF, MOD	D7	TX/RX switch	
	Summing amplifier	: Summing amplifier	D8	Temperature compensation	
IC606	Active filter	LPF for 2 tone	D9	Voltage protection	
IC607	Audio processor		D10,11	ANT switch	
Q2	DC convert switch		D200	Overload protection	
Q3	RF amplifier	Buffer for RF input	D201,202	Varactor tuning	For L205
Q4	Ripple filter	4C	D203,204	Varactor tuning	For L210
Q5	Buffer amplifier		D300,301	RF switch	1st IF wide/narrow
Q6	RF amplifier	TX drive	D302,303	RF switch	2nd IF wide/narrow
Q7	DC switch	APC circuit power	D400	Reverse protection	
		switch	D401	Overload protection	
Q8	RF amplifier	TX drive	D402,403	Surge absorption	
Q9	DC switch	APC voltage control	D409	Voltage reference	
Q10	DC switch	APC off : Speed up	D601	Voltage clamp	
Q13	DC switch	D/A port protection			<u> </u>

Parts without Parts No. are not supplied.

Les articles non mentionnes dans le Parts No. ne sont pas fournis.

Teile ohne Parts No. werden nicht geliefert.

#### TK-390

CONTROL UNIT (X53-3780-XX) Desti-New Ref. No. Address Parts No. Description nation TK-390 1A A02-2139-43 CABINET ASSY 1A A02-2140-43 CABINET ASSY (10KEY) K4 3A \* A10-1374-31 CHASSIS 2B \* A22-2501-15 SUB PANEL ASSY 3B \* A62-0537-33 PANEL ASSY B03-0594-04 DRESSING PLATE 1C \* ACSY 10 \* B09-0363-03 CAP (UNIVERSAL) **ACSY** 1B B11-1183-14 REFLECTOR LCD B38-0786-05 1B \* LCD 1A B42-3394-14 STANDARD LABEL 1A \* B43-1111-04 BADGE KENWOOD 1D B46-0470-00 WARRANTY CARD 12 B62-0816-00 13 1D \* INSTRUCTION MANUAL B72-1344-04 MODEL NAME PLATE 3A 16 1D \* D32-0421-24 STOPPER ACSY E23-1048-05 TERMINAL (BATT-) 3A E23-1062-05 TERMINAL (BATT +) 18 3A TERMINAL (TX/RX-GND) 2B E23-1102-04 19 \* 20 1B \* E29-1165-05 INTER CONNECTOR (LCD) E37-0682-05 FLAT CABLE (TX/RX-CONT) 2A \* 21 E37-0684-05 LEAD WIRE WITH CONNECTOR (SP) 22 1B PROCESSED LEAD WIRE (TOGGLE SW) \* E37-0692-05 23 2B 24 2B \* E37-0722-05 LEAD WIRE WITH CONNECTOR (ANTCOAXIAL) PROCESSED LEAD WIRE (UNIVERSAL-GND) F37-0777-05 \* 26 3В \* E58-0443-15 UNIVERSAL CONNECTOR COIL SPRING (BATT RELEASE) \* G01-0891-04 27 2A 28 2B G11-2537-04 SCHLEGEL SHEET SHEET (UNIVERSAL-CHASSIS) \* G11-2538-04 30 2A,3A \* G13-1678-04 **CUSHION** G13-1688-04 CUSHION (CAP) 31 3A \* G53-0814-04 PACKING (BATT +) PACKING (CHASSIS) 3B \* G53-0822-04 32 33 1B G53-0823-04 PACKING (SP) \* G53-0824-13 PACKING (CABINET) 2A 34 36 20 \* H12-3018-02 PACKING FIXTURE 1C H25-0029-04 PROTECTION BAG 37 ACSY 38 3D \* H52-1085-02 ITEM CARTON CASE 40 2A \* J19-5330-14 HOLDER(BATT RELEASE) 41 1A \* J21-8326-14 HARDWARE FIXTURE (SIDE KEY) 1B J21-8328-14 HARDWARE FIXTURE (LCD) 42 \* 2A J21-8329-14 HARDWARE FIXTURE (CONT UNIT) 43 J21-8330-04 HARDWARE FIXTURE (POWER MUDULE) 3A \* 44 45 2B J21-8343-04 HARDWARE FIXTURE J29-0652-05 \* 46 10 CLIP **ACSY** 1A \* J82-0047-05 FPC (SIDE KEY) 48 3R \* J82-0049-05 FPC (VOL-ENC) 49 3В \* J82-0052-15 FPC (UNIVERSAL) 50 2B \* J99-0346-14 ADHESIVE TAPE (TOGGLE SW) KEY TOP (TOP) 2B \* K29-5172-12 52 53 1A \* K29-5173-13 KEY TOP (SIDE KEY) 54 2B \* K29-5282-04 KNOB ASSY (SEL) 55 2B \* K29-5283-04 KNOB ASSY (VOL)

L : Scandinavia K: USA P: Canada Y: PX (Far East, Hawaii) T: England E: Europe Y: AAFES (Europe) X: Australia M: Other Areas

Ref. No.	Address	New parts	Parts No.		Descripti	ion	Dest natio
<del>5</del> 6	1A	*	K29-5193-13	KEY TOP (10	OKEY)		K4
57	1A	*	K29-5216-24	KNOB ASS	Y (SIDE KEY)		
58	2A	*	K29-5220-13		B (BATT REL		
			132 0/00 0F	CHOKE COL	. (DOWED IV	IIIDIII E)	
			L33-0680-05	CHOKE COI	L (POWER IV	IUDULE)	
Д	1B	*	N14-0577-04	CIRCULAR I	NUT (COAXIA	AL CON)	
3	2B	*	N14-0578-04	CIRCULAR I	NUT (VOL/EN	VC)	
C	3A		N30-2608-45	PAN HEAD	MACHINE S	CREW	
D	1A,2B	*	N78-2030-46	PAN HEAD	TAPTITE SCI	REW	
E	2A,2B		N83-2004-46	PAN HEAD	TAPTITE SCI	REW	
F	3A		N89-2605-46	BAINDING	HEAD TAPTI	TE SCREW	
62	1C		N99-2004-05	SCREW SE	Т	ACSY	
64	3B		R31-0626-05	VARIABLE F	RESISTOR		
66	3B	*	S60-0408-15	ROTARY SV	WITCH		
67	2B	*	S72-0402-05	TOGGLE SV			
69	1B	*	T07-0349-05	SPEAKER			
70	2A,2B		T91-0575-05	MIC ELEME	NT		
CO	NTRO	)L (	JNIT (X53-3	780-XX	) -12	K -13	: K4
D6-9		*	B30-2171-05		BACK LIGHT	Τ)	K4
D102,103		*	B30-2190-05	LED (LCD B)			
D104			B30-2019-05	LED (TX/BU	121)		
C1,2			CK73GB1H471K	CHIP C	470PF	K	
C5			CK73GB1H471K	CHIP C	470PF	K	
C8			CK73FB1C474K	CHIP C	0.47UF	K	
C12,13			CK73FB1C474K	CHIP C	0.47UF	K	
C14			CK73FB1C104KTD	CHIP C	0.10UF	K	
C15			CK73GB1H471K	CHIP C	470PF	K	
C16		*	C92-0734-05	ELRCTRO	100UF	10WV	
C19			CK73FB1C474K	CHIP C	0.47UF	K	
C22			CK73GB1H471K	CHIP C	470PF	K	
C27-29			CK73GB1C104K	CHIP C	0.10UF	K	
C30			CK73FB1C474K	CHIP C	0.47UF	K	
C31			CK73GB1E223K	CHIP C	0.022UF	K	
C32			CK73FB1C474K	CHIP C	0.47UF	K	
C33,34			CK73GB1H471K	CHIP C	470PF	K	
C35			CK73GB1H102K	CHIP C	1000PF	K	
C37			CC73GCH1H101J	CHIP C	100PF	J	
C38			CK73GB1H471K	CHIP C	470PF	K	
C43			CK73GB1H471K	CHIP C	470PF	K	
C104			CC73GCH1H101J	CHIP C	100PF	j	
C501			CK73GB1H183K	CHIP C	0.018UF	K	
CN1		*	E40-5947-05	FLAT CABLE	E CONNECTO	OR	
CN2			E40-5662-05	PIN ASSY S			
		*	E40-5948-05	1	E CONNECTO	OR	
CN3	i	Ι΄.	E40-5662-05	PIN ASSY S			
							1
CN4		*	E40-5920-05	1	E CONNECTO	OR	
CN4 CN101		*		1		DR	
CN3 CN4 CN101 CN501,502 L1-4		*	E40-5920-05	FLAT CABLE	Т	OR	

FERRITE CHIP

FERRITE CHIP

L92-0149-05

L92-0138-05

L6

L101

#### CONTROL UNIT (X53-3780-XX) TX-RX UNIT (X57-5400-10)

Mary	Def Ne	A d d = = = =	New	Darta Na	D	-1-41	Desti-	Def No	A 44	New	Dordo No			NIT (X57-5400	Desti-
MICHIGANIZARIA   CHIPR   AV	Ref. No.	Address		Parts No.		•		Ref. No.	Address	parts	Faits No.				nation
RECIDENTIAL   GIFF   10K   1 /169V   GS											TX-RX UNIT	(X57-54	100-10	)	
March   Marc					1			C1			C02 0540 05	CUID TAN	10115	6.2\\\\\	
Mode															
Beg															
March   Marc	K4,3			KK/3GDIJ4/3J	CHIPK 4/K	J 1/1000									
SECTION   SECTION   CHEER	D4 0			DO2 1252 05	CHID D U UNIV										
ST   ST   ST   ST   ST   ST   ST   ST								CII			C92-0300-03	CHIP-TAIN	1.50F	101	
STATE								C12			CV72CD1C104V	CHID C	0.10115	ν	
STATE															
Ref															
STATE   STATE   STATE   COLOR   COLO	ICIO			IKK730D134733	CIIII K 47K	J 1/1000									
REF   REF   REF   REF   COUNTY   COUN	D1/I			DV72CD1  104	CUID D 100V	I 1/16\A/									
R17-18   R67-3681-1702  GHPR								0.10			C72-0300-03	CIIII-IAN	1.501	10 V	
R77.56					1			C19 20			CK73GB1C104K	CHIPC	0 10HF	K	
REPAIR   Company   Compa															
REZ-23   REZ-268-1002							K/I								
R223   R32611082	1(17,20			KK73GD131013	CIIII K 100	J 1/1000	IN4								
REF-93   REF-93   REF-93   CHEPR   DOK   J   J   J   J   J   J   J   J   J	D22 22			DV72CD1  102	CUID D 1 OV	I 1/16\A/									
R25-92   R75-GB-132-31   CHR R 2								024			072-0002-00	OTHE-TAIN	0.2201	33 V V	
R23								C27			CK73CB1C104V	CHIPC	0.10115	K	
R236   R236B1192J															
Red															
RS	IVOO			וועטטוועטטן וועטטוואן	OTHER ZZN	J 1/1000									
R56	D3/I			DK 73 CR1   102	CHIDD 10V	I 1/14\M									
R86-84   RG75GB1182]								U32			072-0002-00	CHIF-TAIN	U.ZZUF	20 V V	
R25-42   R373GB1102J							V	C22			CC72CCU1U0A0D	CHID C	4 ODE	D	
R834   R873681473J															
R85-94   R873GB1J02J							N4								
R85-59   R873GB11022	N43,44			KK73GD134733	CHIFK 4/K	J 1/1000									
R85-55   R8736B1J1021	D/15_/10			DK73CB1 I102 I	CHIP D 1 OK	I 1/16\M	K								
R66   R673GB1J1S3J								030			66736611110106	CI III C	1.011	C	
R52   R52   R52-265   R62-1252-05   CHIP R   1.0K   J   1/16W   C41   C41   C42   C72-656-10-5   CHIP R   1.0K   J   1/16W   C41   C42   C72-656-10-5   CHIP R   1.0K   J   1/16W   C45   C72-656-10-5   CHIP R   1.0K   J   1/16W   C45   C72-656-10-5   CHIP R   1.0K   J   1/16W   C46   C47-656-11-0-5   C48   C72-656-11-0-5   CHIP C   C47-05   C48   C72-656-11-0-5   C48   C48   C48-64   C72-656-11-0-5   C48   C48-64							11.4	C20			CV72CD1U102V	CHID C	1000DE	V	
REF															
RK73GB1103J												1			
R73GB11733	11.37			IKK730D131023	CIIII K 1.0K	J 1/1000									
R105   RK73GB1J32J   CHIP R 3.3K	R101-104			RK73GB1 I103 I	CHIPR 10K	I 1/16\M/									
R106   RK3736B1J27J								043			672-0307-03	CIIII-IAN	4.701	0.3 V V	
RK73GB1J102J   CHIP R   1.0K   J   1/16W   C46   C47   CK73GB1H102K   CHIP C   1000PF   K   C48   CK73GB1H102K   CHIP C   1000PF   K   C49.50   CK73GB1H102K   CHIP C   470PF   K   C49.50   CK73GB1H101K   CHIP C   470PF   K   C49.50   CK73GB1H102K   CHIP C   470PF   K   C49.50   CK73GB1H10								C45			CC73GCH1H060D	CHIP C	6 OPF	D	
D2															
DT23 9(B)	NOOZ			INN/30D131023	1.00	3 1/10W									
D11	D2			DT73 9/B)	ZENER DIODE										
D12_13				· · ·			K4								
D14				· ·				017,00			0.0000000000000000000000000000000000000	0			
D15							K4	C52			CK73GB1H102K	CHIP C	1000PF	K	
D16															
D16															
D16-18	D16			IMN10	DIODE		K	C59				CHIP C	10PF	D	
D19,20   D101   15S373   D10DE   D10DE   C66,67   C69   CC73GCH1H050C   CHIP C   470PF   K   C69   C70   CC73GCH1H050C   CHIP C   5.0PF   C   C70   CC73GCH1H101J   CHIP C   100PF   J   CHIP C   100PF   J   CHIP C   100PF   J   CHIP C   100PF   J   CHIP C   CP3PF															
D101   C1															
C1								C66,67			CK73GB1H471K	CHIP C	470PF	K	
C70															
C3								C70							
C3	IC2			MC74HC4017F	IC (GATE CMOS)										
TC7SH08FU															
CS01	IC4-6			TC7SH08FU	IC (2ch AND GATE)										
CC73GCH1H020C   CHIP C   COPF   CC73GCH1H020C   CHIP C   CHIP C   CHIP C   COPF   CC73GCH1H010C   CHIP C   CHIP C   COPF   CC73GCH1H010C   CHIP C   CC73GC			*					C76			CC73GCH1H101J	CHIP C	100PF	J	
C78			*		, ,						CC73GCH1H020C				
06 07,8 014 015       2SK1824 UMC4 2SK1824       TRANSISTOR FET       C81,82 C83 C84,85 C84,85       CC73GCH1H010C CK73GB1H102K CK73GB1H102K CK73GB1H102K CK73GB1H102C CK73GCH1H010C CK73GB1H102C CK73GB1H102C CK73GB1H102C CK73GB1H102C CK73GB1H102C CK73GB1H102C CK73GB1H102C CK73GB1H102C CK73GB1H102C CHIP C CK73GB1H102C CK73GB1H102C CHIP C CK73GB1H102C CK73GB1H102C CHIP C CK73GB1H102C CHIP C CK73GB1H102C CK73GB1H102C CHIP C CK73GB1H101C CHIP C CK73GB1H102C CK73GB1H102C CHIP C CK73GB1H102C CK73GB1H102C CHIP C CK73GB1H102C CK73GB1H102C CHIP C CK73GB1H102C CHIP C CK73GB1H102C CK73GB1H102C CHIP C CK73GB1H102C CHIP C CK73GB1H102C CHIP C CK73GB1H102C CHIP C CK73GB1H102C CHIP C CK73GB1H102C CHIP C CK73GB1H102C CHIP C CK73GB1H102C CHIP C CK73GCH1H010C CHIP C CK73GCH1H010C CK73GCH1H010C CHIP C CK73GCH1H010C CHIP C CHIP C CHIP C CHIP C CHIP C CHIP C CHIP C CHIP C CHIP								C78			CK73GB1H103K	CHIP C	0.010UF	K	
06     2SB798(DL,DK)     TRANSISTOR     C81,82     CK73GB1H102K     CHIP C     1000PF K       07,8     UMC4     TRANSISTOR     C83     CC73GCH1H010C     CHIP C     1.0PF C       015     2SK1824     FET     C84,85     CK73GB1H102K     CHIP C     1000PF K       027     C87     CK73GB1H102K     CHIP C     1.0PF C     CK73GB1H102K       028     CC73GCH1H010C     CHIP C     1.0PF C     CK73GB1H102K       028     CC73GCH1H010C     CHIP C     1.0PF C       029     CC73GCH1H010C     CHIP C     1.0PF C       029     CC73GCH1H010C     CHIP C     1.0PF C       020     CC73GCH1H010C     CHIP C     1.0PF C </td <td>Q5</td> <td></td> <td></td> <td>2SC4617(S)</td> <td>TRANSISTOR</td> <td></td> <td></td> <td>C79</td> <td></td> <td></td> <td>CK73FB1C474K</td> <td>CHIP C</td> <td></td> <td>K</td> <td></td>	Q5			2SC4617(S)	TRANSISTOR			C79			CK73FB1C474K	CHIP C		K	
07,8         2SK1824         FET           014         UMC4         TRANSISTOR           015         C83         CC73GCH1H010C         CHIP C         1.0PF         C           015         C84,85         CK73GB1H102K         CHIP C         1000PF         K           016         C87         CK73GB1H102K         CHIP C         1.0PF         C           017         C88         CC73GCH1H010C         CHIP C         1.0PF         C           018         CC73GCH1H010C         CHIP C         1.0PF         C           019         CC73GCH1H010C         CHIP C         1.0PF         C	Q6				TRANSISTOR			C81,82			CK73GB1H102K	CHIP C			
C84,85   C84,85   CK73GB1H102K   CHIP C   1000PF   K   C86   CK73GB1H102K   CHIP C   1.0PF   C   CK73GB1H102K   CHIP C   1000PF   K   C87   C88   CC73GCH1H010C   CHIP C   1000PF   K   CC73GCH1H090D   CHIP C   9.0PF   D   C89   CC73GCH1H010C   CHIP C   1.0PF   C   CC73GCH1H180J   CHIP C   1.0PF   C   CC73GCH1H180J   CHIP C   1.0PF   J   CHIP	Q7,8			' '											
C84,85   C84,85   CK73GB1H102K   CHIP C   1000PF   K   C86   CK73GB1H102K   CHIP C   1.0PF   C   CK73GB1H102K   CHIP C   1000PF   K   C87   CK73GB1H102K   CHIP C   1000PF   K   CC73GCH1H090D   CHIP C   9.0PF   D   C89   CC73GCH1H010C   CHIP C   1.0PF   C   CC73GCH1H180J   CHIP C   1.0PF   C   CC73GCH1H180J   CHIP C   1.0PF   J   CF73GCH1H180J   CHIP C   1.0PF   CF73GCH1H180J   CHIP C   1.0PF   J   CF73GCH1H180J   CHIP C   1.0PF   CF73GCH1	Q14			UMC4	TRANSISTOR			C83			CC73GCH1H010C	CHIP C	1.0PF	С	
C86 C87 C88 C88 CC73GCH1H010C CHIP C 1.0PF C CK73GB1H102K CHIP C 1000PF K CC73GCH1H090D CHIP C 9.0PF D  C89 CC73GCH1H010C CHIP C 1.0PF C CC73GCH1H010C CHIP C 1.0PF C CC73GCH1H010C CHIP C 1.0PF C CC73GCH1H180J CHIP C 1.0PF C	Q15			2SK1824				C84,85				CHIP C			
C87 C88 CK73GB1H102K CHIP C 1000PF K CC73GCH1H090D CHIP C 9.0PF D  C89 CC73GCH1H010C CHIP C 1.0PF C CC73GCH1H180J CHIP C 18PF J								C86			CC73GCH1H010C	CHIP C	1.0PF		
C88 CC73GCH1H090D CHIP C 9.0PF D  C89 CC73GCH1H010C CHIP C 1.0PF C CC73GCH1H180J CHIP C 18PF J															
C89 C73GCH1H010C CHIP C 1.0PF C C73GCH1H180J CHIP C 18PF J								C88							
C90 CC73GCH1H180J CHIP C 18PF J								[							
								C89			CC73GCH1H010C	CHIP C	1.0PF	С	
								C90			CC73GCH1H180J	CHIP C	18PF	J	
1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1								C91			CC73GCH1H010C	CHIP C	1.0PF	С	

Ref. No.	New parts	Parts No.		Description	on	Desti- nation	Ref. No.	Address	New parts			Descripti	on	Desti- nation
C92		CC73GCH1H080D	CHIP C	8.0PF	D		C419			C92-0589-05	CHIP-TAN	47UF	6.3WV	
C93,94		CC73GCH1H100D	CHIP C	10PF	D		C420		*	C92-0702-05	ELECTRO	47UF	6.3WV	
C95		CK73GB1H102K	CHIP C	1000PF	K		C421			CK73GB1H471K	CHIP C	470PF	K	
C96		CC73GCH1H101J	CHIP C	100PF	 J		C422			CK73FF1C105Z	CHIP C	1.0UF	Z	
C97		CK73GB1H102K	CHIP C	100FF	K		C422 C423,424			CK73GB1H471K	CHIP C	470PF	K	
C99		CK73GB1H102K	CHIP C	1000PF	K		C425			CK73GB1H103K	CHIP C	0.010UF	K	
C100		C92-0560-05	CHIP-TAN	10UF	6.3WV		C426			CK73GB1H471K	CHIP C	470PF	K	
C150		CC73GCH1H010C	CHIP C	1.0PF	С		C427			CK73GB1H103K	CHIP C	0.010UF	K	
C200		CC73GCH1H2R5B	CHIP C	2.5PF	В		C428			C92-0004-05	CHIP-TAN	1.0UF	16WV	
C202		CC73GCH1H040C	CHIP C	4.0PF	С		C429			CK73GB1H102K	CHIP C	1000PF	K	
C203,204		CK73GB1H471K	CHIP C	470PF	K		C430			CK73FB0J105K	CHIP C	1.0UF	K	
C205,204		CC73GCH1H040C	CHIP C	4.0PF	C		C430			CK73GB1H471K	CHIP C	470PF	K	
					J		C431						K	
C207		CC73GCH1H101J	CHIP C	100PF					١.,.	CK73FB0J105K	CHIP C	1.0UF		
C209,210		CC73GCH1H050C	CHIP C	5.0PF	С		C433		*	C92-0698-05	ELECTRO	47UF	16WV	
C211		CK73GB1H102K	CHIP C	1000PF	K		C434,435			CK73GB1H471K	CHIP C	470PF	K	
C212,213		CK73GB1H471K	CHIP C	470PF	K		C436			CK73FF1C105Z	CHIP C	1.0UF	Z	
C215		CC73GCH1H050C	CHIP C	5.0PF	С		C437			CK73GB1H472K	CHIP C	4700PF	K	
C216		CC73GCH1H080D	CHIP C	8.0PF	D		C438			CK73GB1H103K	CHIP C	0.010UF	K	
C218		CC73GCH1H050C	CHIP C	5.0PF	C		C439		1	CK73GB1E103K	CHIP C	0.010UF	K	
C219,220		CK73GB1H471K	CHIP C	470PF	K		C440			CK73GB1C103K	CHIP C	0.01001 0.027UF	K	
Cana		007300114110500	CLUB C	E ODE	C		C441			CV73UP10100V	CHIP C	0.010115	V	
C222		CC73GCH1H050C	CHIP C	5.0PF	С		C441			CK73HB1C103K	CHIP C	0.010UF	K	
C223		CC73GCH1H101J	CHIP C	100PF	J		C442			CK73FB1C474K	CHIP C	0.47UF	K	
C225,226		CC73GCH1H050C	CHIP C	5.0PF	С		C443			CK73GB1H222K	CHIP C	2200PF	K	
C227,228		CK73GB1H471K	CHIP C	470PF	K		C444			CC73GCH1H680J	CHIP C	68PF	J	
C229		CC73GCH1H050C	CHIP C	5.0PF	C		C445,446			CK73GB1H222K	CHIP C	2200PF	K	
C230		CC73GCH1H030C	CHIP C	3.0PF	С		C447,448			CC73GCH1H270J	CHIP C	27PF	J	
C231		CC73GCH1H120J	CHIP C	12PF	Ĵ		C449			CK73GB1H103K	CHIP C	0.010UF	K	
C233		CC73GCH1H1205 CC73GCH1H050C	CHIP C	5.0PF	C		C447			C92-0560-05	CHIP-TAN	10UF	6.3WV	
C234 C236		CC73GCH1H151J CC73GCH1H151J	CHIP C CHIP C	150PF 150PF	J		C451-454 C456-477			CC73GCH1H101J CC73GCH1H101J	CHIP C CHIP C	100PF 100PF	J J	
C237-240		CK73GB1H471K	CHIP C	470PF	K		C478			CK73GB1C104K	CHIP C	0.10UF	K	
C241,242		CK73GB1C104K	CHIP C	0.10UF	K		C479			CC73GCH1H271J	CHIP C	270PF	J	
C244		CC73GCH1H151J	CHIP C	150PF	J		C480			CC73GCH1H221J	CHIP C	220PF	J	
C245		CK73GB1H102K	CHIP C	1000PF	K		C481			CK73GB1H102K	CHIP C	1000PF	K	
C247,248		CK73GB1H471K	CHIP C	470PF	K		C485			CC73GCH1H101J	CHIP C	100PF	J	
C251		CC73GCH1H030C	CHIP C	3.0PF	С		C600			CK73GB1C473K	CHIP C	0.047UF	K	
C302,303		CC73GCH1H040C	CHIP C	4.0PF	C		C601			C92-0003-05	CHIP-TAN	0.47UF	25WV	
C304		CK73GB1H102K	CHIP C	1000PF	K		C602			CK73GB1C104K	CHIP C	0.10UF	K	
C305		CK73GB1H103K	CHIP C	0.010UF	K		C603			CK73GB1C683K	CHIP C	0.068UF	K	
C306-308		CK73GB1H102K	CHIP C	1000PF	K		C604			CK73GB1H103K	CHIP C	0.010UF	K	
C309		C92-0560-05	CHIP-TAN	10UF	6.3WV		C606			CK73GB1H222K	CHIP C	2200PF	K	
C310,311		CK73GB1C104K	CHIP C	0.10UF	K		C607			CC73GCH1H101J	CHIP C	100PF	J	
C312		CK73GB1H103K	CHIP C	0.010UF	K		C608			CK73GB1H103K	CHIP C	0.010UF	K	
C313,314		CC73GCH1H220J	CHIP C	22PF	J		C609		1	CC73GCH1H121J	CHIP C	120PF	J	
C315		CK73GB1H102K	CHIP C	1000PF	K		C610			CK73GB1H103K	CHIP C	0.010UF		
C316		CC73GCH1H820J	CHIP C	82PF	J		C611			CK73GB1E123K	CHIP C	0.012UF	K	
C317		CK73GB1H102K	CHIP C	1000PF	K		C612		1	C92-0507-05	CHIP-TAN	4.7UF	6.3WV	
									1		1			
C318		CK73GB1C104K	CHIP C	0.10UF	K		C613		1	CK73GB1H103K	CHIP C	0.010UF	K	
C319		CK73GB1H103K	CHIP C	0.010UF	K		C615		1	CC73GCH1H200J	CHIP C	20PF	J	
C320		C92-0560-05	CHIP-TAN	10UF	6.3WV		C619			CC73GCH1H471J	CHIP C	470PF	J	
C321		CK73GB1C104K	CHIP C	0.10UF	K		C621			CK73GB1C104K	CHIP C	0.10UF	K	
C322		CK73GB1H103K	CHIP C	0.010UF	K		C623		1	CK73GB1E103K	CHIP C	0.010UF	K	
C323		C92-0560-05	CHIP-TAN	10UF	6.3WV		C625,626			CK73GB1C104K	CHIP C	0.10UF	K	
C327-331		CK73GB1H103K	CHIP C	0.010UF	K		C627			CK73GB1H122K	CHIP C	1200PF	K	
C400,401		CC73GCH1H101J	CHIP C	100PF	J		C629,630			CK73GB1H103K	CHIP C	0.010UF	K	
0400 444		0070001111101	CLUD C	10005	1		0/21/22			0730010101	CLUD C	0.10115	V	
C403-411		CC73GCH1H101J	CHIP C	100PF	J		C631,632		1	CK73GB1C104K	CHIP C	0.10UF	K	
C413		CK73GB1H471K	CHIP C	470PF	K		C633			CK73GB1H103K	CHIP C	0.010UF	K	
C414		CK73GB1H103K	CHIP C	0.010UF	K		C634,635		1	CK73GB1H562K	CHIP C	5600PF	K	
	l	CK73FB0J105K	CHIP C	1.0UF	K		C636		1	CK73GB1C333K	CHIP C	0.033UF	K	
C415					K		C637				CHIP C		K	

Ref. No.	Address	New	Parts No.	Desci	iption	Desti-	Ref. No.	Address	New	Parts No.	TX-RX UNIT (X57-S	Desti-
C638		parts		CHIP C 2700	•	nation	L202		parts *	L40-8275-44	SMALL FIXED INDUCTOR (82.0NH)	nation
C639			CK73GB1H272K CC73GCH1H090D	CHIP C 27001			L202 L203		*	L40-6875-44	1	
C640,641			CK73GB1H272K	CHIP C 9.0PF			L203 L204		*	L40-3375-44	SMALL FIXED INDUCTOR (68.0NH) SMALL FIXED INDUCTOR (33.0NH)	
C640,641			CC73GCH1H151J	CHIP C 27001			L204 L205		*	L79-1470-05	HELICAL BLOCK	
C643			CK73GB1H122K	CHIP C 1300			L205		*	L40-2275-44	SMALL FIXED INDUCTOR (22.0NH)	
0/44			01/720004114001/	0.00	DF 1/		1.007			140 0075 44	CAMALL FIVED INIDIJATOD (00 0AU)	
C644			CK73GB1H102K	CHIP C 1000			L207		*	L40-8275-44	SMALL FIXED INDUCTOR (82.0NH)	
C645 C646			CC73GCH1H090D C92-0560-05	CHIP C 9.0PF CHIP-TAN 10UF	6.3WV		L208 L209		*	L40-6875-44 L40-3375-44	SMALL FIXED INDUCTOR (68.0NH)	
C647			CK73GB1C104K	CHIP-TAIN TOUF			L209 L210		*	L79-1470-05	SMALL FIXED INDUCTOR (33.0NH) HELICAL BLOCK	
C648			CC73GCH1H101J	CHIP C 100P			L210		*	L40-8275-44	SMALL FIXED INDUCTOR (82.0NH)	
											, ,	
C649-651			CK73GB1C104K	CHIP C 0.10L			L214		*	L40-8275-44	SMALL FIXED INDUCTOR (82.0NH)	
C652			C92-0507-05	CHIP-TAN 4.7UF			L215,216			L39-1272-05	TOROIDAL COIL	
C653,654 C655			CK73GB1H472K CC73GCH1H101J	CHIP C 47001 CHIP C 100P1			L217 L218			L92-0140-05 L39-1272-05	FERRITE CHIP	
C656			CK73FB1H563K	CHIP C 0.056			L218 L219		*	L40-8275-44	TOROIDAL COIL SMALL FIXED INDUCTOR (82.0NH)	
											, ,	
C657			C92-0507-05	CHIP-TAN 4.7UF			L220			L40-1575-44	SMALL FIXED INDUCTOR (15.0NH)	
C659			CK73GB1C104K	CHIP C 0.10L CHIP-TAN 10UF			L300			L40-1095-34	SMALL FIXED INDUCTOR (1UH/8)	
C660 C661			C92-0560-05 CK73GB1C104K	CHIP-TAN 10UF CHIP C 0.10L	6.3WV IF K		L301 L400			L40-3391-37 L92-0136-05	SMALL FIXED INDUCTOR (3.300UH) FERRITE CHIP	
C663			CC73GCH1H101J	CHIP C 0.100			L400 L401-403			L92-0138-05	FERRITE CHIP	
5555			33703311111313							272 0100 00	TERRITE STILL	
C664			CK73GB1C104K	CHIP C 0.10L			X1		*	L77-1704-05	TCXO (16.8MHz)	
C665			CK73GB1C333K	CHIP C 0.033			X300		*	L77-1741-05	CRYSTAL RESONATOR (44.395MHz)	
C666			CK73GB1E153K	CHIP C 0.015			X400			L78-0431-05	RESONATOR (14.7456MHz)	
C667			CC73GCH1H120J	CHIP C 12PF	J		X600		١.	L77-1708-05	CRYSTAL RESONATOR (3.579545MHz)	
C668			CK73GB1H102K	CHIP C 1000	PF K		XF300		*	L71-0523-05	MCF (44.85MHz)	
C669			CC73GCH1H101J	CHIP C 100P	: J		XF301			L71-0502-05	MCF (44.85MHz)	
C681			CK73GB1E103K	CHIP C 0.010	UF K							
C700-710			CC73HCH1H101J	CHIP C 100PI	: J		CP1			R90-0714-05	MULTI-COMP 10K X 4	
C714-720			CC73HCH1H101J	CHIP C 100P	J		CP700-707			R90-0741-05	MULTIPLE RESISTOR	
C722-728			CC73HCH1H101J	CHIP C 100P	: ၂		CP711-716			R90-0741-05	MULTIPLE RESISTOR	
							CP718-720			R90-0741-05	MULTIPLE RESISTOR	
CN400		*	E40-5563-05	FLAT CABLE CONNE			R4			R92-1252-05	CHIP R 0 OHM	
CN401		*	E40-5947-05	FLAT CABLE CONNE	CTOR					DIVERSE LANCE	01110 0 4001/ 1 4/4/14	
CN402			E40-5662-05	PIN ASSY SOCKET	0.7.0.0		R5			RK73GB1J104J	CHIP R 100K J 1/16W	
CN403		*	E40-5856-05	FLAT CABLE CONNE	CTOR		R6			RK73GB1J473J	CHIP R 47K J 1/16W	
CN404,405			E23-0342-05	TEST TERMINAL			R7 R8			RK73GB1J104J RK73GB1J683J	CHIP R 100K J 1/16W CHIP R 68K J 1/16W	
F400		*	F53-0143-05	CHIP FUSE 3A			R9			RK73GB1J063J	CHIP R 18K J 1/16W	
CD300			L79-1072-05	CERAMIC DISCRI			R11			RK73GB1J273J	CHIP R 27K J 1/16W	
CF300,301			L72-0916-05	CERAMIC FILTER			R13			RK73GB1J104J	CHIP R 100K J 1/16W	
CF302,303			L72-0939-05	CERAMIC FILTER			R14			RK73GB1J184J	CHIP R 180K J 1/16W	
L1 L3			L92-0138-05 L92-0140-05	FERRITE CHIP FERRITE CHIP			R15 R16			RK73GB1J682J RK73GB1J151J	CHIP R 6.8K J 1/16W CHIP R 150 J 1/16W	
L3			192-0140-05	FERRITE CHIP			KIO			KK/3GDIJIJIJ	CHIPK 130 J 1/10W	
L5			L40-3981-37	SMALL FIXED INDU	CTOR (0.390UH)		R17			RK73GB1J394J	CHIP R 390K J 1/16W	
L7			L40-2275-44	SMALL FIXED INDU	CTOR (22.0NH)		R18		1	RK73GB1J102J	CHIP R 1.0K J 1/16W	
L8		*	L40-1275-44	SMALL FIXED INDU			R19			RK73GB1J100J	CHIP R 10 J 1/16W	
L9			L40-1575-44	SMALL FIXED INDU	CTOR (15.0NH)		R24			RK73GB1J102J	CHIP R 1.0K J 1/16W	
L10,11			L92-0149-05	FERRITE CHIP			R25			RK73GB1J224J	CHIP R 220K J 1/16W	
L12			L33-0760-05	SMALL FIXED INDU	CTOR		R27			RK73GB1J472J	CHIP R 4.7K J 1/16W	
L13			L40-1095-34	SMALL FIXED INDU			R28		1	RK73GB1J223J	CHIP R 22K J 1/16W	
L14		*	L33-1224-05	SMALL FIXED INDU	, ,		R29			RK73GB1J184J	CHIP R 180K J 1/16W	
L15,16			L33-0761-05	SMALL FIXED INDU	CTOR		R31			RK73GB1J101J	CHIP R 100 J 1/16W	
L17			L40-1092-81	SMALL FIXED INDU	CTOR		R32			RK73GB1J122J	CHIP R 1.2K J 1/16W	
L18			L40-4791-37	SMALL FIXED INDU	CTOR (4.70011H)		R33			RK73GB1J103J	CHIP R 10K J 1/16W	
L10		*	L40-2775-44	SMALL FIXED INDU			R34			RK73GB1J103J	CHIP R 22K J 1/16W	
L20,21			L92-0140-05	FERRITE CHIP	(= / )		R35		1	RK73EB2ER39K	CHIP R 0.39 K 1/4W	
L150			L33-0760-05	SMALL FIXED INDU	CTOR		R37		1	RK73EB2ER39K	CHIP R 0.39 K 1/4W	
L152			L40-1075-44	SMALL FIXED INDU			R38			RK73GB1J272J	CHIP R 2.7K J 1/16W	
1150			140 2075 44	CMALL EIVED INDU	CTOD (20 0NUN		D20			DV72FD2FD2OV	CHIRD 0.20 1/ 1/4/4/	
L153 L200		*	L40-3975-44 L40-4775-34	SMALL FIXED INDU SMALL FIXED INDU			R39 R40			RK73EB2ER39K RK73GB1J682J	CHIP R 0.39 K 1/4W CHIP R 6.8K J 1/16W	
L200 L201			L40-4775-34 L40-2275-44	SMALL FIXED INDU			R41,42			RN73GH1J154D	CHIP R 150K D 1/16W	
LLUI	1		L 10 ZZ / J-77	SININGE LIVED HADO	01 ON (22.01VII)	1 1	1371,772		1	1417 301113 1340	O. W. K. 190K D. 1/10W	1

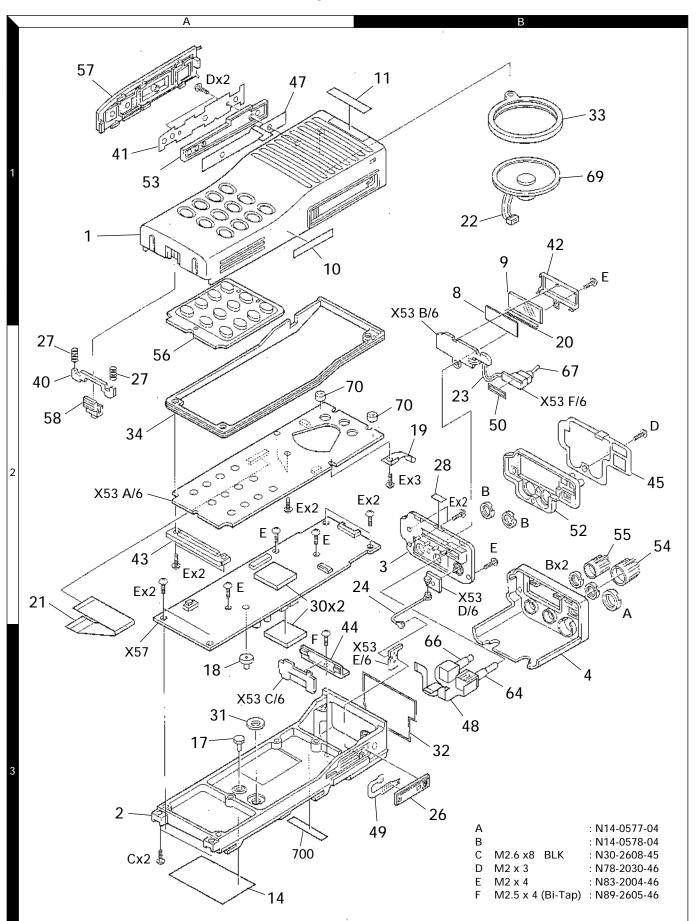
Ref. No.	Address	New parts	Parts No.		Descri	iptio	n	Desti- nation	Ref. No.	Address	New parts			Descripti	on	Desti- nation
R43			RK73GB1J220J	CHIP R	22	J	1/16W		R321			RK73GB1J102J	CHIP R	1.0K J	1/16W	
R44,45			RN73GH1J154D	CHIP R	150K	D	1/16W		R322			RK73GB1J272J	CHIP R	2.7K J	1/16W	
R46			RK73GB1J152J	CHIP R		J	1/16W		R324,325			RK73GB1J223J	CHIP R	22K J	1/16W	
R47,48			RN73GH1J154D	CHIP R	150K	D	1/16W		R326			RK73GB1J473J	CHIP R	47K J	1/16W	
R49			RK73GB1J101J	CHIP R	100	J	1/16W		R328			RK73GB1J473J	CHIP R	4.7K J	1/16W	
1(1)			1117 305 13 10 13	Or iii IX	100	5	17 10 17		11020			1417.000131723	01111111	1.710 3	17 10 11	
R50			RK73GB1J471J	CHIP R	470	J	1/16W		R329			RK73GB1J104J	CHIP R	100K J	1/16W	
R51			RK73GB1J221J	CHIP R	220	J	1/16W		R330			R92-1252-05	CHIP R	0 OHM		
R52			RK73GB1J103J	CHIP R	10K	J	1/16W		R331			RK73GB1J104J	CHIP R	100K J	1/16W	
R53			RK73GB1J820J	CHIP R	82	J	1/16W		R332			RK73GB1J224J	CHIP R	220K J	1/16W	
R54			RK73GB1J681J	CHIP R	680	J	1/16W		R333			R92-1252-05	CHIP R	0 OHM		
DEE			DI/700D4 1400 I	OLUB D	10		4/4/14/		D22.4			DV700D1 1100 I	OLUD D	10	4 /4 / ) ) /	
R55			RK73GB1J100J	CHIP R	10	J	1/16W		R334			RK73GB1J100J	CHIP R	10 J	1/16W	
R56			RK73GB1J473J	CHIP R	47K	J	1/16W		R335			RK73GB1J473J	CHIP R	47K J	1/16W	
R57			RK73GB1J104J	CHIP R		J	1/16W		R336,337			R92-1252-05	CHIP R	0 OHM		
R58			RK73GB1J105J	CHIP R	1.0M	J	1/16W		R338			RK73GB1J220J	CHIP R	22 J	1/16W	
R59			RK73GB1J104J	CHIP R	100K	J	1/16W		R400			RK73GB1J102J	CHIP R	1.0K J	1/16W	
R60			RK73GB1J222J	CHIP R	2.2K	J	1/16W		R401			RK73GB1J272J	CHIP R	2.7K J	1/16W	
R61	1		R92-1252-05	CHIP R	0 OHM	-			R402			RK73GB1J2723	CHIP R	3.3K J	1/16W	
R63	1		R92-1252-05	CHIP R	0 OHM				R402 R403			RK73GB1J332J	CHIP R		1/16W	
	1			1		,	1/1/\4/						1			
R65			RK73GB1J222J	CHIP R		J	1/16W		R404			RK73GB1J182J	CHIP R	1.8K J	1/16W	
R69-71			RK73GB1J821J	CHIP R	820	J	1/16W		R405			RK73GB1J332J	CHIP R	3.3K J	1/16W	
R73			RK73GB1J223J	CHIP R	22K	J	1/16W		R407			RK73GB1J102J	CHIP R	1.0K J	1/16W	
R74			RK73GB1J101J	CHIP R	100	J	1/16W		R408			RK73GB1J104J	CHIP R	100K J	1/16W	
R75			RK73GB1J102J	CHIP R		J	1/16W		R409			RK73GB1J102J	CHIP R	1.0K J	1/16W	
R76			RK73GB1J101J	CHIP R	100	J	1/16W		R410			RK73GB1J1025	CHIP R	2.7K J	1/16W	
R79			RK73GB1J101J	CHIP R		J	1/16W		R410			RK73GB1J272J	CHIP R	820 J	1/16W	
R80			RK73GB1J560J	CHIP R	56	J	1/16W		R412			RK73GB1J182J	CHIP R	1.8K J	1/16W	
R82			R92-1252-05	CHIP R	0 OHM				R413			RK73GB1J473J	CHIP R	47K J	1/16W	
R200			RK73GB1J124J	CHIP R	120K	J	1/16W		R414			R92-1252-05	CHIP R	0 OHM		
R201			RK73GB1J103J	CHIP R	10K	J	1/16W		R415			RK73GB1J471J	CHIP R	470 J	1/16W	
R202			RK73GB1J561J	CHIP R		J	1/16W		R416,417			RK73GB1J104J	CHIP R	100K J	1/16W	
D000			DUZOODA IAOO I	alub b	4.01/				D.100			DUZGODA IAGO I	0.000	401/	4.44.14.1	
R203			RK73GB1J102J	CHIP R		J	1/16W		R420			RK73GB1J103J	CHIP R	10K J	1/16W	
R204			RK73GB1J121J	CHIP R	120	J	1/16W		R421			RK73GB1J223J	CHIP R	22K J	1/16W	
R205,206			RK73GB1J104J	CHIP R	100K	J	1/16W		R422			RK73GB1J103J	CHIP R	10K J	1/16W	
R207			RK73GB1J124J	CHIP R	120K	J	1/16W		R423			RK73GB1J473J	CHIP R	47K J	1/16W	
R208			RK73GB1J103J	CHIP R	10K	J	1/16W		R424			RK73GB1J103J	CHIP R	10K J	1/16W	
R209			RK73GB1J561J	CHIP R	560	J	1/16W		R425			R92-1252-05	CHIP R	0 OHM		
				1											1/1/\\/	
R210			RK73GB1J102J	CHIP R		J	1/16W		R426			RK73GB1J103J	CHIP R	10K J	1/16W	
R211			RK73GB1J100J	CHIP R	10	J	1/16W		R428			RK73GB1J472J	CHIP R	4.7K J	1/16W	
R212,213			RK73GB1J104J	CHIP R	100K	J	1/16W		R429			RK73GB1J103J	CHIP R	10K J	1/16W	
R214			R92-1252-05	CHIP R	0 OHM				R430			RK73GB1J473J	CHIP R	47K J	1/16W	
R215			RK73GB1J222J	CHIP R	2.2K	J	1/16W		R432,433			RK73GB1J473J	CHIP R	47K J	1/16W	
R216			RK73GB1J271J	CHIP R		J	1/16W		R434			RK73GB1J103J	CHIP R	10K J	1/16W	
R217			RK73GB1J271J	CHIP R		J	1/16W		R435,436			RK73GB1J103J	CHIP R	1.0K J	1/16W	
	1			1		J	1/ TO VV		R435,436 R438-440				1			
R218,219 R220			R92-1252-05 RK73GB1J274J	CHIP R CHIP R	0 OHM 270K	J	1/16W		R438-440 R442			RK73GB1J102J RK73GB1J152J	CHIP R CHIP R	1.0K J 1.5K J	1/16W 1/16W	
11220			1117 300 132743	Or III IX	LIUN	J	1/1000		11774			1300131323	0111111	1.010 J	17 10 4 4	
R221			RK73GB1J224J	CHIP R		J	1/16W		R443			RK73GB1J821J	CHIP R	820 J	1/16W	
R222			RK73GB1J274J	CHIP R	270K	J	1/16W		R444			R92-0670-05	CHIP R	0 OHM		
R223			RK73GB1J184J	CHIP R	180K	J	1/16W		R445			R92-1252-05	CHIP R	0 OHM		
R300	1		RK73GB1J220J	CHIP R	22	J	1/16W		R446			RK73GB1J472J	CHIP R	4.7K J	1/16W	
R301			RK73GB1J122J	CHIP R		J	1/16W		R447			RK73GB1J473J	CHIP R	47K J	1/16W	
D2U2 2U4			RK73GB1J472J	CHIP R	1 7V	1	1/14\\\		R448			RK73GB1J102J	CHID D	1.0K J	1/16W	
R303,304				1		J	1/16W						CHIP R			
R305,306			R92-1252-05	CHIP R	0 OHM		4/4/14/		R449,450			RK73GB1J473J	CHIP R	47K J	1/16W	
R307,308	1		RK73GB1J221J	CHIP R		J	1/16W		R452-455			RK73GB1J104J	CHIP R	100K J	1/16W	
R309,310	1		RK73GB1J472J	CHIP R	4.7K	J	1/16W		R456-460			RK73GB1J473J	CHIP R	47K J	1/16W	
R312			RK73GB1J122J	CHIP R	1.2K	J	1/16W		R463,464			RK73GB1J473J	CHIP R	47K J	1/16W	
R313			RK73GB1J331J	CHIP R	330	J	1/16W		R465			RK73GB1J104J	CHIP R	100K J	1/16W	
R315			RK73GB1J472J	CHIP R		J	1/16W		R466			R92-1252-05	CHIP R	0 OHM	.,	
R317,318			RK73GB1J472J	CHIP R		J	1/16W		R467,468			RK73GB1J473J	CHIP R		1/16\//	
													1	47K J	1/16W	
D210	1	l	RK73GB1J123J	CHIP R	12K	J	1/16W		R470-472 R474			R92-1252-05	CHIP R CHIP R	0 OHM 10K J		
R319 R320			RK73GB1J392J	CHIP R	3.9K	J	1/16W					RK73GB1J103J			1/16W	

TY.	.PY	TIMIT	(X57	-5400	.10)

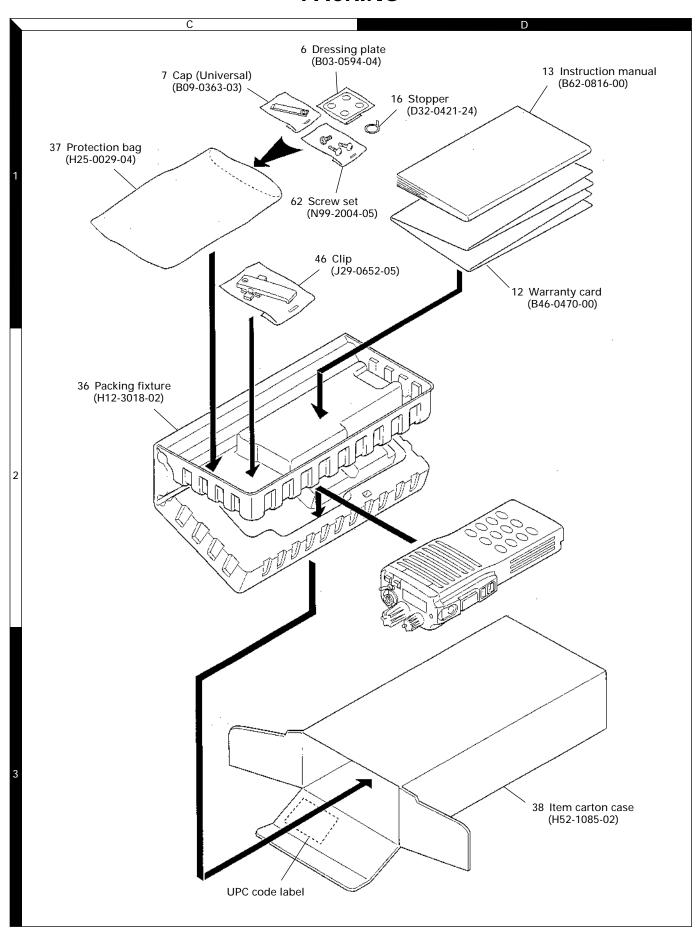
		New					Desti-			New		TX-RX UNIT (X57-54	Desti-
Ref. No.	Address	parts	Parts No.		Description	on	nation	Ref. No.	Address	parts	Parts No.	Description	nation
R475-477			RK73GB1J101J	CHIP R	100 J	1/16W		R674			RK73GB1J104J	CHIP R 100K J 1/16W	
R481			R92-1252-05	CHIP R	0 OHM			R675			RK73GB1J473J	CHIP R 47K J 1/16W	
R601			RK73GB1J153J	CHIP R	15K J	1/16W		R676			RK73GB1J684J	CHIP R 680K J 1/16W	
R602			RK73GB1J223J	CHIP R	22K J	1/16W		R677			RK73GB1J333J	CHIP R 33K J 1/16W	
R603			R92-1252-05	CHIP R	0 OHM			R678			RK73GB1J184J	CHIP R 180K J 1/16W	
R605,606			R92-1252-05	CHIP R	0 OHM			R700			RK73HB1J100J	CHIP R 10 J 1/16W	
R607			RK73GB1J153J	CHIP R	15K J	1/16W		R701,702			RK73HB1J102J	CHIP R 1.0K J 1/16W	
R608			RK73GB1J103J	CHIP R	10K J	1/16W		R703-705			RK73HB1J100J	CHIP R 10 J 1/16W	
R609			RK73GB1J154J	CHIP R	150K J	1/16W		R706			RK73HB1J102J	CHIP R 1.0K J 1/16W	
R610			RK73GB1J563J	CHIP R	56K J	1/16W		R707			R92-1368-05	CHIP R 0 OHM	
R611			RK73GB1J184J	CHIP R	180K J	1/16W		R708,709			RK73HB1J102J	CHIP R 1.0K J 1/16W	
R612			RK73GB1J334J	CHIP R	330K J	1/16W		R710			RK73HB1J100J	CHIP R 10 J 1/16W	
R614			RK73GB1J184J	CHIP R	180K J	1/16W		R711-716			RK73HB1J102J	CHIP R 1.0K J 1/16W	
R615,616			RK73GB1J103J	CHIP R	10K J	1/16W							
R617			R92-1252-05	CHIP R	0 OHM			D4			MA2S111	DIODE	
D/10			DI/700D4 I/00 I	OLUD D	(0)(	4 /4 / 14 /		D6			MA2S111	DIODE	
R618			RK73GB1J683J	CHIP R	68K J	1/16W		D7			DAN235E	DIODE	
R620			RK73GB1J334J	CHIP R	330K J	1/16W		D8			DA221	DIODE	
R621			RK73GB1J683J	CHIP R	68K J	1/16W		D9			MA8062	ZENER DIODE	
R622			RK73GB1J223J	CHIP R	22K J	1/16W		D10			N 41000	DIODE	
R623			RK73GB1J154J	CHIP R	150K J	1/16W		D10			MI809	DIODE	
								D11			1SS312	DIODE	
R624			RK73GB1J104J	CHIP R	100K J	1/16W		D200			HSM88AS	DIODE	
R625			RK73GB1J333J	CHIP R	33K J	1/16W		D201-204			1SV273	VARIABLE CAPACITANCE DIODE	
R626			RK73GB1J104J	CHIP R	100K J	1/16W		D300-303			DAN235E	DIODE	
R627			RK73GB1J684J	CHIP R	680K J	1/16W							
R628			RK73GB1J334J	CHIP R	330K J	1/16W		D400 D401			1SR154-400 MA2S111	DIODE	
R629			RK73GB1J223J	CHIP R	22K J	1/16W		D402,403			NNCD6.8G	ZENER DIODE	
R632			RK73GB1J103J	CHIP R	10K J	1/16W		D409			MA2S111	DIODE	
R635			RK73GB1J393J	CHIP R	39K J	1/16W		D601			MA742	DIODE	
R637			R92-1252-05	CHIP R	0 OHM	.,		1500.				5.052	
R638			RK73GB1J472J	CHIP R	4.7K J	1/16W		IC2			LMC7101BIM5	IC (OP AMP)	
11000			10000131723	01111111	1.710 3	17 10 11		IC3		*	M62354GP	IC (D/A CONVERTER)	
R639			RK73GB1J104J	CHIP R	100K J	1/16W		IC4		*	MAX865	IC (DC/DC CONVERTER)	
R640			RN73GH1J683D	CHIP R	68K D	1/16W		IC5			SA7025DK	IC (PLL SYSTEM)	
R641			RK73GB1J104J	CHIP R	100K J	1/16W		IC6			LMC7101BIM5	IC (OP AMP)	
R642			RN73GH1J333D	CHIP R	33K D	1/16W		100			EIVIO7 IO IBIIVIO	10 (01 71111)	
R643			RK73GB1J564J	CHIP R	560K J	1/16W		IC7			NJM2904V	IC (APC)	
			1447 000 1000 10	0	00011	.,		IC10		*	KCH41	HIC (VCO)	
R644			RK73GB1J474J	CHIP R	470K J	1/16W		IC200			GN2011(Q)	IC	
R645			RN73GH1J274D	CHIP R	270K D	1/16W		IC300			TA31136FN	IC (FM IF DETECTOR)	
R646			RK73GB1J472J	CHIP R	4.7K J	1/16W		IC301			TC7S66FU	IC (ANALOG SWITCH)	
R647			R92-1252-05	CHIP R	0 OHM	17 10 11		10001			10700010	To ( www.coo own.on)	
R648			RK73GB1J104J	CHIP R	100K J	1/16W		IC400			BU4094BCFV	IC (8bit SHIFT/STORE REGISTER)	
				Or III IX	1001	17 10 4 4		IC400 IC401,402			TK11250BM	IC (VOLTAGE REGULATOR)	
R649			RN73GH1J682D	CHIP R	6.8K D	1/16W		IC403-405			BU4094BCFV	IC (8bit SHIFT/STORE REGISTER)	
R650			RK73GB1J103J	CHIP R	10K J	1/16W		IC406		*	MC-8800-802	IC (MICRO PROCESSOR)	
R651			RK73GB1J155J	CHIP R	1.5M J	1/16W		IC407			RN5VL45C	IC (REGULATOR)	
R652			RK73GB1J473J	CHIP R	47K J	1/16W		1				, ,	
R653			RK73GB1J4733	CHIP R	180K J	1/16W		IC408			TC7S08FU	IC (2INPUT AND GATE)	
				J IX		.,		IC409			PST9140NR	IC (RESET SW)	
R654			RN73GH1J683D	CHIP R	68K D	1/16W		IC410			TC7S32FU	IC (2INPUT OR GATE)	
R655			RK73GB1J474J	CHIP R	470K J	1/16W		IC410			TC7S02FU	IC (2ΔINPUT NOR GATE)	
R656			RN73GH1J682D	CHIP R	6.8K D	1/16W		IC411			AT2408N10SI2.5	IC (8kbit SERIAL EEPROM)	
R657			RK73GB1J101J	CHIP R	100 J	1/16W		10112				1.5 (SHART SERVICE EET ROTVI)	
R658			RK73GB1J101J	CHIP R	220K J	1/16W		IC413			TA75S01F	IC (OP AMP)	
R659			RK73GB1J103J	CHIP R	10K J	1/16W		IC600 IC601			LC73872M TC75W51FU	IC (DTMF RECEIVER) IC (OP AMP X2)	
R660			RK73GB1J1223J	CHIP R	22K J	1/16W		IC602			TA75W01FU	IC (OP AMP X2)	
R661			RK73GB1J2233	CHIP R	390K J	1/16W		IC603			M62364FP	IC (D/A CONVERTER)	
R662			RK73GB1J374J	CHIP R	47 J	1/16W		10000			1410230711	IS (S/A CONVENIEN)	
R663			RK73GB1J470J	CHIP R	47 J 22 J	1/16W		IC604-606			TC75W51FU	IC (OP AMP X2)	
				J IX		.,		IC607			TC35453F	IC (AUDIO PROCESSOR)	
R664			RK73GB1J333J	CHIP R	33K J	1/16W		Q2			2SJ144(GR)	FET	
R665,666			R92-1252-05	CHIP R	0 OHM	17 1 V V V		Q2 Q3			2SC5066(0)	TRANSISTOR	
R668			RK73GB1J334J	CHIP R	330K J	1/16W		Q4			2SC4617(S)	TRANSISTOR	
R669			R92-1252-05	CHIP R	0 OHM	17 1 UVV		Q.7			2307017(3)	TIV IN OLO TOTA	1
R670-673			RK73GB1J472J	CHIP R	4.7K J	1/16W		Q5			2SC5108(Y)	TRANSISTOR	1
013				Orm IX	N J	17 1 UVV		4.0			2000100(1)		<u> Ш</u>

Ref. No.	Address	New parts	Parts No.	Description	Desti- nation	Ref. No.	Address	New parts	Parts No.	Description	Desti- nation
Q6 Q7 Q8			2SC5110(0) DTC114EE 2SC4988	TRANSISTOR DIGITAL TRANSISTOR TRANSISTOR							
Q9 Q10			DTA144EE DTC144EE	DIGITAL TRANSISTOR DIGITAL TRANSISTOR							
Q13 Q200,201			2SK1824 3SK274	FET FET							
Q300 Q301 Q302			DTA144EE DTC144EE 2SK1215(E)	DIGITAL TRANSISTOR DIGITAL TRANSISTOR FET							
Q303 Q304 Q305 Q307			DTA144EE DTC144EE UMC4 UMH6	DIGITAL TRANSISTOR DIGITAL TRANSISTOR TRANSISTOR TRANSISTOR							
Q400			UMG3N	TRANSISTOR							
Q401 Q402 Q403 Q404 Q405			UPA572T MP5A02 2SJ243 DTC144EE UMG3N	FET TRANSISTOR FET DIGITAL TRANSISTOR TRANSISTOR							
Q406 Q407 Q408-410 Q411 Q600			DTA123JE 2SC4215(Y) 2SC4617(S) 2SB1132(Q,R) DTC144EE	DIGITAL TRANSISTOR TRANSISTOR TRANSISTOR TRANSISTOR DIGITAL TRANSISTOR							
Q601-603			2SK1824	FET							

### **EXPLODED VIEW**



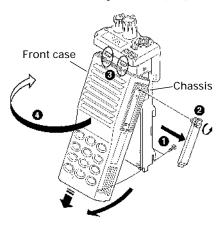
# **PACKING**



# **DISASSEMBLY FOR REPAIR**

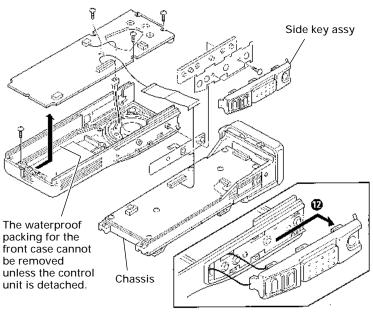
## **Disassembly of Front Case and Chassis**

- 1. Remove the 2 screws (1) and a cap fixed screw (2).
- Press the chassis bottom upwards and remove the clips
   (3) at the top. The front case is still connected to the
   chassis by the FPC at this time so be gentle when lifting
   upwards, otherwise unwanted stress is applied on the
   FPC.
- 3. The front case and chassis can only be opened to the side when connected by the FPC (4).



## Remove the Side Key Assy

1. The side key assy is clips form a slide-hook structure. Lift up gently and take from the side (12).



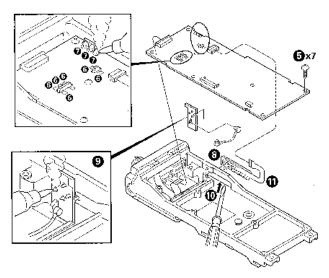
### Remove the TX-RX Unit from the Chassis

- 1. The TX-RX unit cannot be removed simply by removing the seven screws (5).
- 2. A total of 9 solder connections, 6 on the RF power module (6) and 3 on the antenna daughter board (7) must be disconnected.

### **Remove the Universal Connector**

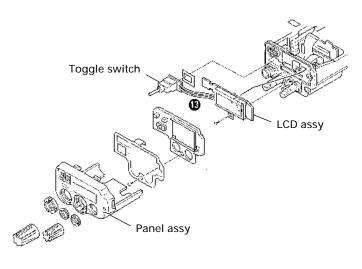
- 1. The universal connector ( **8**) is fastened to the chassis with bouble-side tape.
- 2. First unsolder the connection on the antenna daughter board (9).
- 3. Press firmly with a tool such as a screwdriver and so that it can peel (10).

Note: You must replace both parts together when replacing the universal connector or the FPC ( 1).



# Disassembly of the Panel Assy

1. The LCD assy and toggle switch are joined by wire (3). Use caution not to break this wire during handling.



# **ADJUSTMENT**

## **Test Equipment Required for Alignment**

No.	Test Equipment		Major Specifications
1	Standard Signal Generator	Frequency Range	Maximum 600MHz or more.
	(SSG)	Modulation	Frequency modulation and external modulation.
		Output	–133dBm/0.05μV to 7dBm/501mV
2	Power Meter	Input Impedance	50Ω.
		Operation Frequency	Up to 600MHz.
		Measurement Range	Full scale of 10W or so.
3	Deviation Meter	Frequency Range	400 to 560MHz.
4	Digital Volt Meter	Measuring Range	FS=18V or so.
	(DVM)	Accuracy	High input impedance for minimum circuit loading.
5	Oscilloscope		DC through 30MHz.
6	High Sensitivity	Frequency Range	Up to 1GHz or so.
	Frequency Counter	Frequency Stability	0.2ppm or less.
7	Ammeter		5A.
8	AF Volt Meter	Frequency Range	50Hz to 1MHz.
	(AF VTVM)	Voltage Range	1mV to 10V.
9	Audio Generator (AG)	Frequency Range	100Hz to 100kHz or more.
		Output	0 to 1V.
10	Distortion Meter	Capability	3% or less at 1kHz.
		Input Level	50mV to 10Vrms.
11	16 <b>Ω</b> Dummy Load		Approx. $16\Omega$ , 5W.
12	Regulated Power Supply		5V to 10V, approx. 5A
			Useful if ammeter equipped.
13	Spectrum Analyzer	Measuring Range	DC to 1GHz or more.
14	Tracking Generator	Center Frequency	50kHz to 600MHz.
		Frequency Deviation	±35MHz.
		Output Voltage	100mV or more.

### ■ The following parts are required for adjustment

### 1. Antenna connector adapter

The antenna connector of this radio uses an SMA terminal.

Use an antenna connector adapter [SMA(f) – BNC(f) or SMA(f) - N(f)] for adjustment. (The adapter is not provided as an option, so buy a commercially-available one.)

#### Note

When the antenna connector adapter touches the knob, draw out the knob to mount the connector.

#### 2. Universal connector

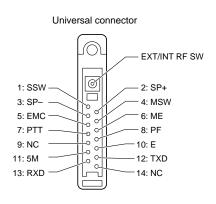
Use the interface cable (KPG-36) for PC tuning or the lead wire with plug (E30-3287-08) and screw (N08-0535-08) for panel tuning. Connct the plug to the universal connector of the radio and tighten the screw.

The lead wire with plug (E30-3287-08) and screw (N08-0535-08) terminals are as follows. Numbers are universal connector terminal numbers.

### Caution

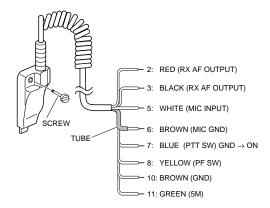
- When connecting the plug to the universal connector of the radio, a short circuit may occur. To provent this, be sure to turn the radio POWER switch off.
- Since the RX AF output is a BTL output, there is a DC component. Isolate this with a capacitor or transformer as shown in the figure.
- 3. Do not connct an instrument between red or black and GND.

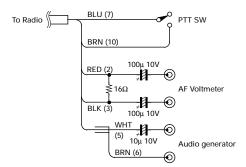
### · Universal connector



# **ADJUSTMENT**

### · Panel tuning

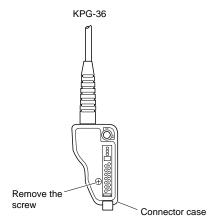


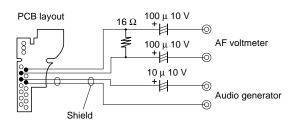


### · PC tuning

Connect the wires to the PCB in the connector case of interface cable.

For output the wires out of the connector case, need to process the connector case.

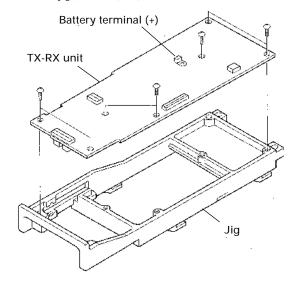




## Repair Jig (Chassis)

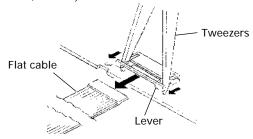
Use jig (part No.: A10-1399-03) for repairing the TK-390 Place the TX-RX unit on the jig and fit it with 7 screws.

**Note:** Supply power from an external power supply (Battery terminal: +, jig (chassis): -)



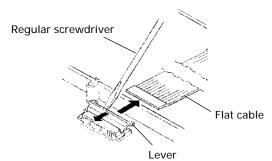
### How to Remove the Flat Cable

 Gently draw out both sides of the connector lever uniformly in the direction of the arrow with tweezers. (CN101, CN400)



2. Gently rise up the connector lever in the direction of the arrow with a fine regular screwdriver or tweezers. (CN1, CN3, CN401, CN403)

**Note:** Gently push both sides of the connector lever, when put in the flat cable.



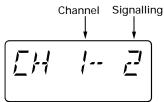
# **ADJUSTMENT**

### **Panel Test Mode**

This mode is used for making transceiver connection tests and clearing the memory.

To set Panel test mode, turn on the power with [Side 2] and [PTT] are still held down and then first release [PTT]. This mode cannot be set when disabled with the FPU.

In Panel test mode, when channel or signalling is selected, the signalling that was most recently used is displayed.



Key operations in Panel test mode are as follows.

[Selector switch]: Use to select the test channel.

[PTT] : For transmit.
[Top 1] : For down signalling.
[Top 2] : For up signalling.

[Side 1] : For setting Panel tuning mode.

[Side 2] : For switching power between Hi/Low.

Enter the LCD all lamp mode if held

down for 2 seconds.

[Orange] : For monitoring.

[Toggle] : For switching between Wide/Narrow.

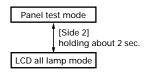
### Frequency (MHz)

	<u> </u>	
СН	RX	TX
1	470.1000 (Center)	470.0000 (Center)
2	450.1000 (Low)	450.0000 (Low)
2	489.9000 (Hi)	489.9750 (Hi)
4	470.0000	470.0000
5	470.2000	470.2000
6	470.4000	470.4000

### Signalling

No.	Encode tone	Decode tone
1	None	None
2	QT 67.0Hz	QT 67.0Hz
3	QT 250.3Hz	QT 25.03Hz
4	DQT 023N	DQT 023N
5	None	2 tone 321.7/928.1Hz
6	DTMF (9)	DTMF (159)
7	MSK PN pattern	None
8	100Hz square wave	None
9	Single tone 1633Hz	None

### · LCD all lamp mode





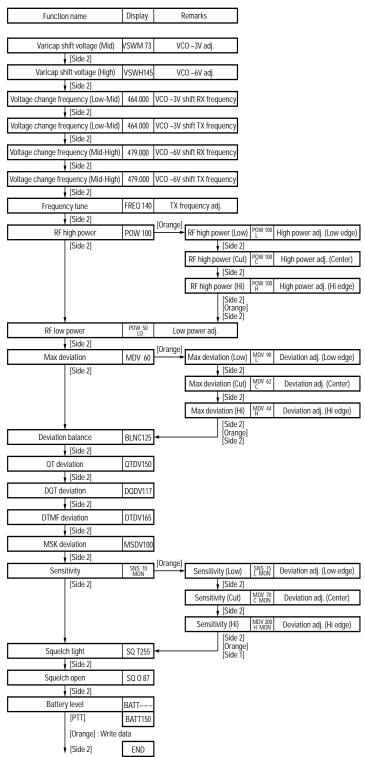
#### Clear function

Pressing [PTT] while holding down [Orange] in Panel test mode, triggers the clear function which clears all transceiver data settings.

### Panel tuning mode

Press [Side 1] in Panel test mode to set Panel tuning mode.

Note: Different sample displays are shown.



### **Common Section**

	Measurement			Adj	ustment			
Item	Condition	Test- equipment	Unit	Terminal	Unit	Parts	Method	Specifications/Remarks
Varicap shift voltage     Mid	1) Set panel tuning mode Push Side2 to select VSWM **	DC VTVM	TX-RX	TP2	Panel	Top1/ Top2	-3V	±0.1V
High	2) Push Side2 to select VSWH * * *						-6V	±0.1V
2. Voltage change frequency Low-Mid RX	1) Set panel tuning mode Push Side2 to select ***. *** r1			TP1			4.15V	±0.05V
TX	2) Push Side2 to select *** *** t1						4.15V	±0.05V
Mid-Hi RX	3) Push Side2 to select ***.** r2						4.15V	±0.05V
TX	4) Push Side2 to select *** *** t2						4.15V	±0.05V

## **Transmitter Section**

**Caution:** When adjusting transmit power and sensitivity, connect the cable to the SMA antenna connector on the top panel. At this time, use the antenna-less type jig connector (E30-3287-08) in the universal connector.

	Condition	Mea	sureme	ent		Adj	ustment	
Item		Test- equipment	Unit	Terminal	Unit	Parts	Method	Specifications/Remarks
1. Frequency adjustment	1) Set panel test mode CH No.: 3 Signalling No.: 1 Select FRQ *** in panel tuning mode PTT: ON	Power meter f. counter	Panel	ANT	Panel	Top1/ Top2	470.100MHz	±50Hz
2. Maximum power check	1) Set panel test mode CH No. : 3 Signalling No. : 1 BATT terminal voltage : 7.5V Select POW 255 in panel tuning mode PTT : ON	Power meter Ammeter	Panel	ANT			Check	4.0W or more
3. TX high power adjustment	1) Set panel test mode CH No.: 3 Signalling No.: 1 Select POW *** in tuning mode Push Orange to 3 point adjustment mode Select POW *** L PTT: ON  2) Push Side2 to select POW *** M PTT: ON  3) Push Side2 to select POW *** H PTT: ON				Panel	Top1/ Top2	4.20W	±0.1W 2.3A or less

		Mea	sureme	ent		Adj	ustment	
Item	Condition	Test- equipment	Unit	Terminal	Unit	Parts	Method	Specifications/Remarks
4. TX high power check	1) Set panel test mode CH No. : 1 Signalling No. : 1 PTT : ON	Power meter Ammeter	Panel	ANT			Check	3.7W~5.2W 2.3A or less
	2) CH No. : 3 Signalling No. : 1 PTT : ON							
	3) CH No. : 5 Signalling No. : 1 PTT : ON							
5. TX low power adjustment	1) Set panel test mode CH No.: 3 Signalling No.: 1 Select POW * * * LO in panel tuning mode Push Orange to 3 point adjustment mode Select POW * * * L D PTT: ON				Panel	Top1/ Top2	1.0W	±0.1W 1.2A or less
	2) Push Side2 to select POW *** M LO PTT: ON							
	3) Push Side2 to select POW *** H LO PTT: ON							
6. TX low power check	1) Set panel test mode CH No. : 1 Signalling No. : 1 Set low power (Push Top2) PTT : ON						Check	0.5W~1.5W 1.2A or less
	2) CH No. : 3 Signalling No. : 1 Set low power (Push Top2) PTT : ON							
	3) CH No. : 5 Signalling No. : 1 Set low power (Push Top2) PTT : ON							
7. DQT balance adjustment	1) Set panel test mode CH No.: 3 Signalling No.: 1 Select BLNC * * * in panel tuning mode Deviation meter filter setting LPF: 3kHz HPF: OFF PTT: ON	Power meter Deviation meter Oscilloscope AG AF VTVM	Panel Side	ANT	Panel	Top1/ Top2	Make the demodulation waves into square waves.	

		Mea	sureme	ent	L	Adj	ustment	
Item	Condition	Test- equipment	Unit	Terminal	Unit	Parts	Method	Specifications/Remarks
8. Maximum deviation adjustment (Wide)	1) Set panel test mode CH No.: 3 Signalling No.: 1 Select MDV * * * in panel tuning mode Push Orange to 3 point adjustment mode Select MDV * * *  L AG: 1kHz/150mV Deviation meter filter setting LPF: 15kHz HPF: OFF PTT: ON  2) Push Side2 to select POW * * * M PTT: ON  3) Push Side2 to select POW * * * H PTT: ON	Power meter Deviation meter Oscilloscope AG AF VTVM	Panel	ANT	Panel	Top1/ Top2	3.95kHz (According to the larger +, -)	±50Hz
9. Maximum deviation adjustment (narrow)	1) Set panel test mode CH No.: 3 Signalling No.: 1 Select MDV *** in panel tuning mode Turn the toggle SW to the right (narrow). Push Orange to 3 point adjustment mode Select MDV *** N L AG: 1kHz/150mV Deviation meter filter setting LPF: 15kHz HPF: OFF PTT: ON  2) Push Side2 to select POW *** N M PTT: ON  3) Push Side2 to select POW *** N H PTT: ON						1.90kHz (According to the larger +, -)	±50Hz
10. MIC sensitivity check	1) Set panel test mode CH No.: 3 Signalling No.: 1 AG: 1kHz/15mV Deviation meter filter setting LPF: 15kHz HPF: OFF PTT: ON 2) Turn the toggle SW to the right (narrow) PTT: ON						Check	1.8kHz~3.6kHz 0.9kHz~1.8kHz

		Mea	sureme	ent		Adj	ustment	
Item	Condition	Test- equipment	Unit	Terminal	Unit	Parts	Method	Specifications/Remarks
11. QT deviation adjustment (wide)	1) Set panel tuning mode CH No.: 3 Signalling No.: 1 MIC input: OFF Select QTDV * * * in panel tuning mode Deviation meter filter setting LPF: 3kHz HPF: 50Hz De-emphasis: 750µs PTT: ON	Power meter Deviation meter Oscilloscope AG AF VTVM	Panel Side	ANT	Panel	Top1/ Top2	0.75kHz	±0.05kHz
12. QT deviation adjustment (narrow)	1) Set panel tuning mode CH No.: 3 Signalling No.: 1 MIC input: OFF Select QTDV * * * in panel tuning mode Turn the toggle SW to the right (narrow) Deviation meter filter setting LPF: 3kHz HPF: 50Hz De-emphasis: 750µs PTT: ON						0.375kHz	±0.05kHz
13. DQT deviation adjustment (wide)	1) Set panel test mode CH No.: 3 Signalling No.: 1 Select DQDV * * * in panel tuning mode Deviation meter filter setting LPF: 3kHz HPF: OFF PTT: ON						0.75kHz	±0.05kHz
14. DQT deviation adjustment (narrow)	1) Set panel tuning mode CH No.: 3 Signalling No.: 1 Select DQDV * * * in panel tuning mode Turn the toggle SW to the right (narrow) Deviation meter filter setting LPF: 3kHz HPF: OFF PTT: ON						0.375kHz	±0.05kHz
15. DTMF deviation adjustment (wide)	1) Set panel test mode CH No.: 3 Signalling No.: 1 Select DTDV * * * in tuning mode Deviation meter filter setting LPF: 15kHz HPF: OFF PTT: ON						3.0kHz	±50Hz

		Mea	sureme	nt		Adj	ustment	
Item	Condition	Test- equipment	Unit	Terminal	Unit	Parts	Method	Specifications/Remarks
16. DTMF deviation adjustment (narrow)	1) Set panel tuning mode CH No.: 3 Signalling No.: 1 Select DTDV * * * in panel tuning mode Turn the toggle SW to the right (narrow) Deviation meter filter setting LPF: 15kHz HPF: OFF PTT: ON	Power meter Deviation meter Oscilloscope AG AF VTVM	Panel Side	ANT	Panel	Top1/ Top2	1.5kHz	±50Hz
17. MSK deviation adjustment (wide)	1) Set panel test mode CH No.: 3 Signalling No.: 1 Select MSDV * * * in panel tuning mode Deviation meter filter setting LPF: 15kHz HPF: OFF PTT: ON						3.00kHz	±50Hz
18. MSK deviation adjustment (narrow)	1) Set panel tuning mode CH No.: 3 Signalling No.: 1 Select MSDV * * * in panel tuning mode Turn the toggle SW to the right (narrow) Deviation meter filter setting LPF: 15kHz HPF: OFF PTT: ON						1.50kHz	±50Hz
19. Transmission S/N check (wide)	1) Set panel test mode CH No.: 3 Signalling No.: 1 Deviation meter filter setting LPF: 300Hz HPF: 3kHz De-emphasis: 750µs PTT: ON						Check	42dB or more
20. Transmission S/N check (narrow)	1) Set panel test mode CH No.: 3 Signalling No.: 1 Turn the toggle SW to the right (narrow) Deviation meter filter setting LPF: 300Hz HPF: 3kHz De-emphasis: 750µs PTT: ON							37dB or more
21. BATT detection writing	1) Set panel test mode CH No.: 3 Signalling No.: 1 Select BATT * * * in panel tuning mode PTT: ON	Power meter DC VTVM		ANT BATT terminal	Side	Orange	Write the voltage level	6.2V

			Measurement			Adj	ustment	
Item	Condition	Test- equipment	Unit	Terminal	Unit	Parts	Method	Specifications/Remarks
22. BATT detection check	1) Set panel test mode CH No. : 3 Signalling No. : 1 BATT terminal voltage : 5.7V PTT : ON	Power meter DC VTVM	Panel Bottom	ANT BATT terminal			Check	Cannot transmit LED (TX) blinks
	2) BATT terminal voltage : 6.5V PTT : ON							Transmit

# **Receiver Section**

		Mea	sureme	nt		Adj	ustment	
Item	Condition	Test- equipment	Unit	Terminal	Unit	Parts	Method	Specifications/Remarks
1. AF level check	1) Set panel test mode CH No.: 3 Signalling No.: 1 SSG freq.: Each freq. output: -53dBm/501µV MOD.: 1kHz DEV.: 3kHz 2) Turn the toggle SW to the right (narrow) SSG freq.: Each freq. output: -53dBm/501µV	AF VTVM Oscilloscope Distortion meter Audio analyzer	Panel Side	ANT Universal			Check	0.63V±0.3V 3% or less 0.63V±0.3V 5% or less
	MOD. : 1kHz DEV. : 1.5kHz							
2. Hum and noise ratio check	1) Set panel test mode CH No. : 3 Signalling No. : 1 SSG output : -53dBm/501µV							42dB or more
	2) Turn the toggle SW to the right (narrow) SSG output: –53dBm/501μV							37dBm
3. BPF coil  Use this adjustment procedure after replaceing any of the BPF coils	1) Set panel test mode CH No.: 2 Connect the spectrum analyzer to TP3. Signalling No.: 1 Push Orange to 3 point adjustment mode Select SNS 25 L	Tracking generator Spectrum analyzer	Panel TX-RX	TP3	TX-RX	L205 L210	The frequency with thave to be the center After finished peaking a point adjustment at RX BPF 0-10	g the waveform, perform a nd set for maximum sensitivity.  ATT 10dB A write B blank Center AMKR 443.6MHz -31.07dBm Hi

# TK-390 TK-390

# **ADJUSTMENT**

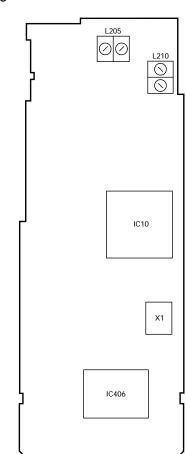
		Mea	asureme	ent		Adj	ustment	
Item	Condition	Test- equipment	Unit	Terminal	Unit	Parts	Method	Specifications/Remarks
4. Sensitivity adjustment and check (wide)	1) Set panel test mode CH No.: 3 Signalling No.: 1 Select SNS * * * in panel tuning mode Push Orange to 3 point adjustment mode Select SNS * * * L SSG freq.: Low output: -116dBm/0.35µV  2) Push Side2 to select SNS * * * M SSG freq.: Center  3) Push Side2 to select SNS * * * H SSG freq.: Hi	SSG  AF VTVM Oscilloscope Distortion meter Audio analyzer	Panel Side	ANT Universal	Panel	Top1/ Top2	Adjust for maximum SINAD	12dB SINAD or more
5. Sensitivity check (narrow)	1) Set panel test mode CH No.: 1 Signalling No.: 1 SSG output: -116dBm/0.35µV MOD.: 1kHz DEV.: 1.5kHz Turn the toggle SW to the right (narrow)  2) Set panel test mode CH No.: 3 Signalling No.: 1 Turn the toggle SW to the right (narrow)  3) Set panel test mode CH No.: 5 Signalling No.: 1 Turn the toggle SW to the right (narrow)						Check	12dB SINAD or more
6. Tight squelch adjustment (wide)	1) Set panel test mode CH No.: 3 Signalling No.: 1 Select SQ T * * * in panel tuning mode				Panel	Top1/ Top2	Normally set to 110	
7. Tight squelch adjustment (narrow)	1) Set panel test mode CH No.: 3 Signalling No.: 1 Select SQ T * * * in panel tuning mode Turn the toggle SW to the right (narrow)							
8. Squelch adjustment (wide)	1) Set panel test mode CH No.: 3 Signalling No.: 1 Select SQ O * * * in panel tuning mode SSG output: 3dB below to 12dB SINAD level						Adjust to point of opening squelch	

# **ADJUSTMENT**

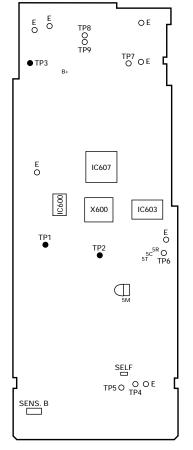
		Measurement				Adj	ustment	
Item	Condition	Test- equipment	Unit	Terminal	Unit	Parts	Method	Specifications/Remarks
9. Squelch adjustment (narrow)	1) Set panel test mode CH No.: 3 Signalling No.: 1 Select SQ O * * * in panel tuning mode Turn the toggle SW to the right (narrow) SSG output: 3dB below to 12dB SINAD level	AF VTVM Oscilloscope Distortion meter Audio analyzer	Panel Side	ANT Universal	Panel	Top1/ Top2	Adjust to point of opening squelch	
10. Squelch check (wide)	1) Set panel test mode CH No. : 3 Signalling No. : 1 SSG output : -118dBm/0.28μV 2) SSG output : -127dBm/0.1μV						Check	Squelch must be opened  Squelch must be closed
11. Squelch check (narrow)	1) Set panel test mode CH No.: 3 Signalling No.: 1 Turn the toggle SW to the right (narrow) SSG output: -118dBm  2) SSG output: -127dBm						Check	Squelch must be opened  Squelch must be closed

# Adjustment Point ■ Foil side

47

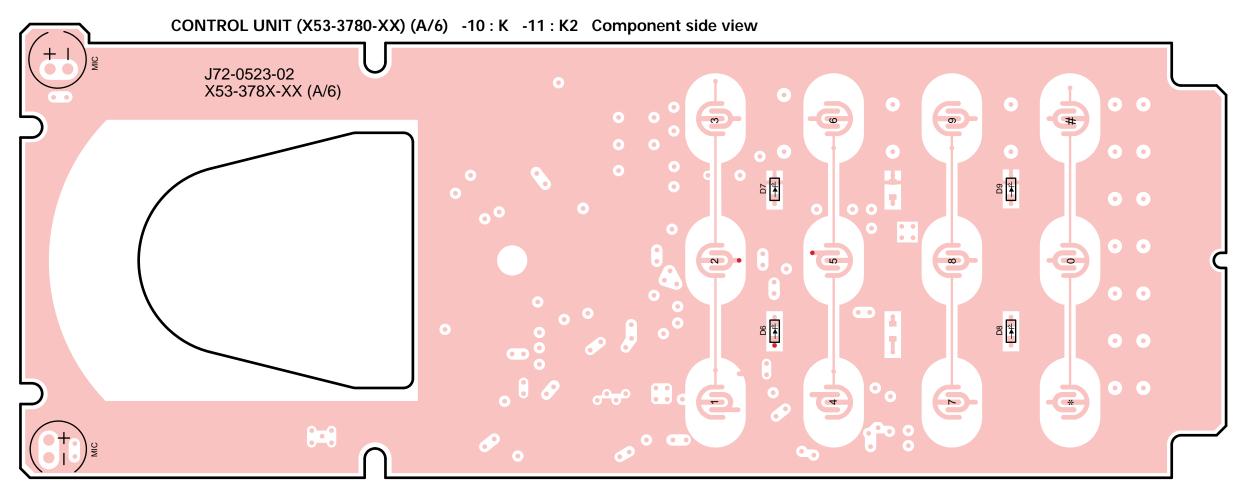


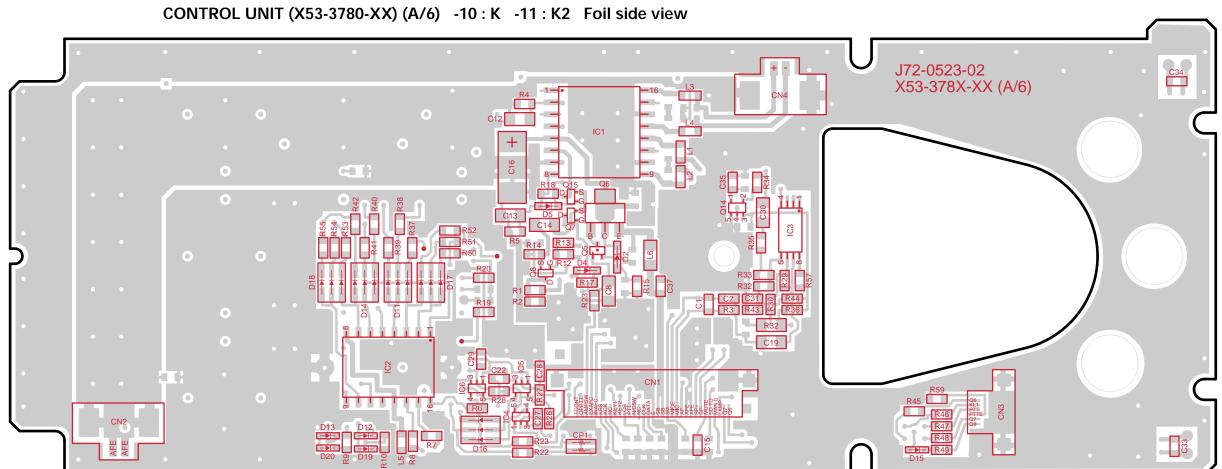
# **■** Component side



# TK-390 PC BOARD VIEWS

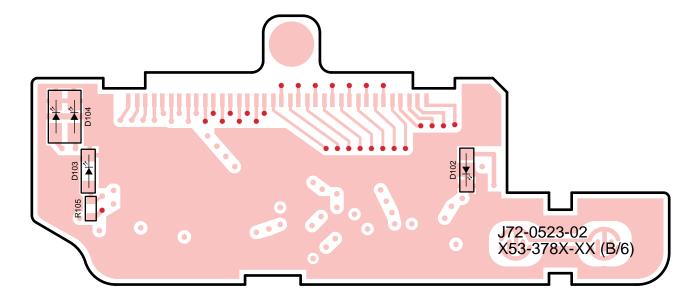
49

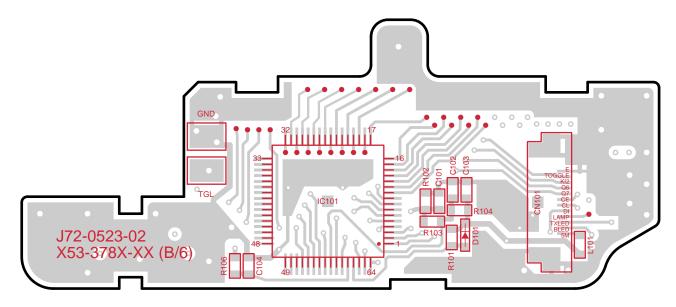


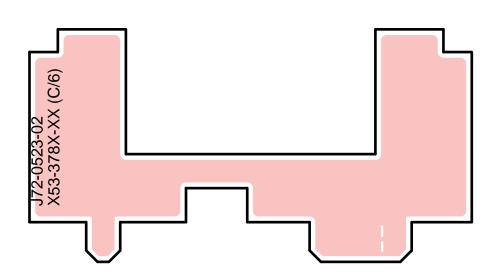


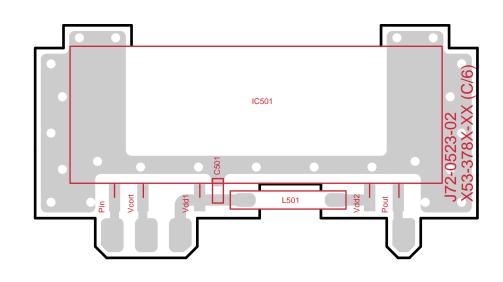
CONTROL UNIT (X53-3780-XX) (B~F/6) -10 : K -11 : K2 Foil side view



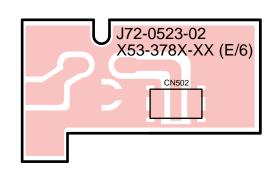


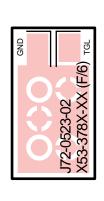


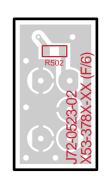


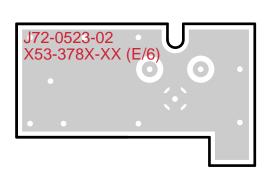


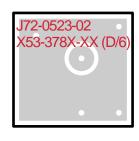






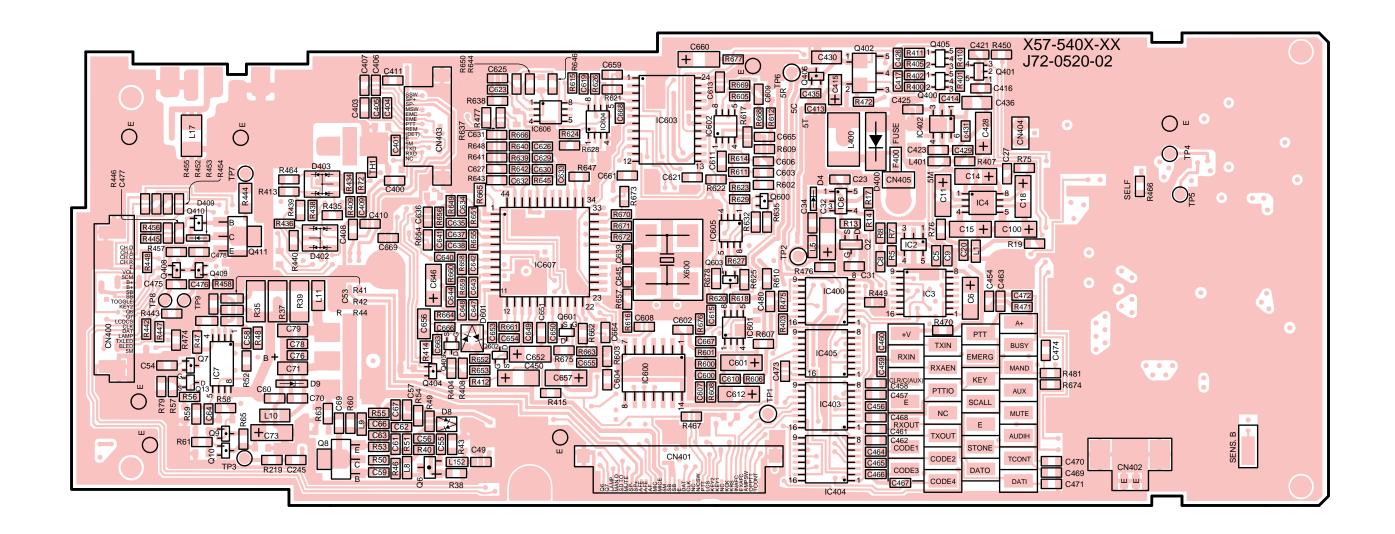




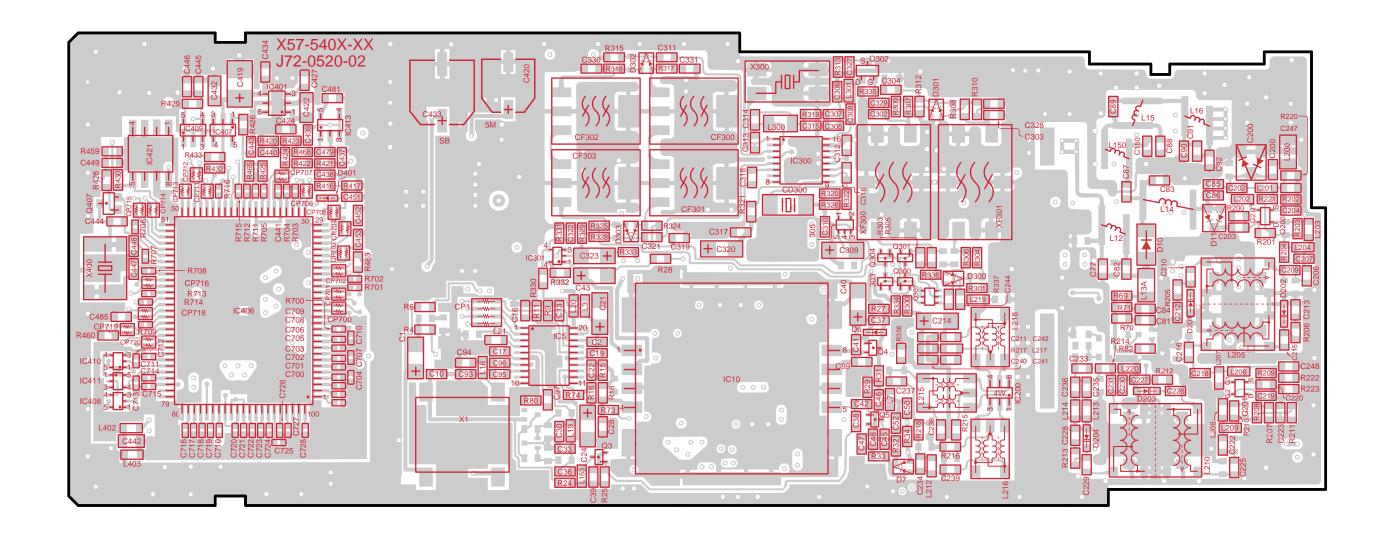


# TK-390 PC BOARD VIEWS

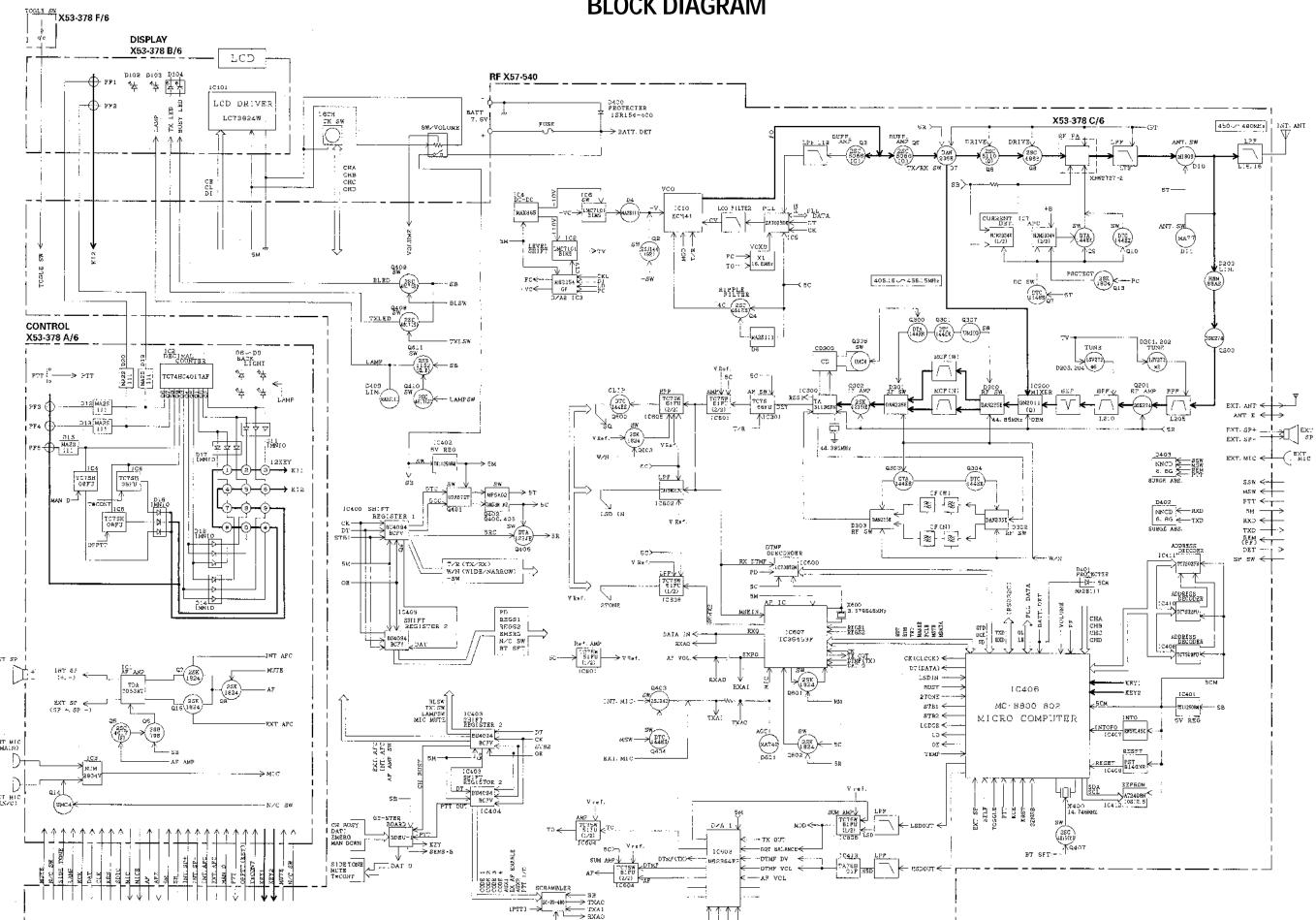
TX-RX UNIT (X57-5400-10) Component side view



53



# TK-390 TK-390 BLOCK DIAGRAM

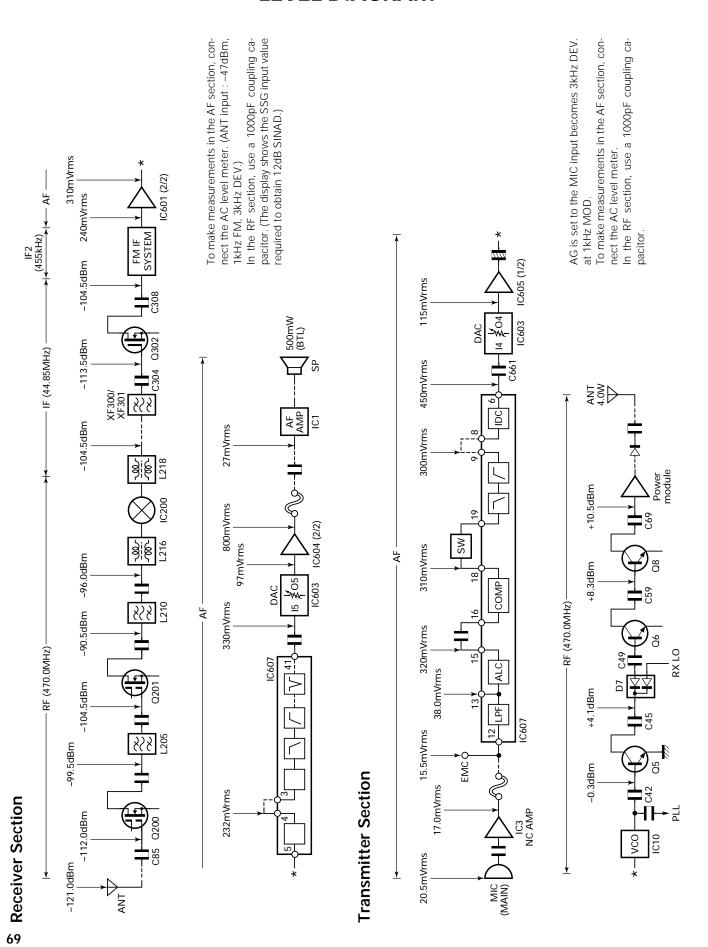


# TK-390 TK-390

70

# **LEVEL DIAGRAM**

# **TERMINAL FUNCTION**



CN No.	Pin No.	Name	1/0	Function	CN No.	Pin No.	Name	I/O	Function
		TX-R	ΧL	JNIT (X57-5400-10)		26	SP+	ı	BTL input + for external speaker.
CN400	1	CH D		CH switch data.		27	SP-	ı	BTL input – for external speaker.
CIN4UU						28	MUTE	0	Audio mute signal output.
for	2	CH A CH C		CH switch data.		29	SD TO	0	For optional board.
for X53	3 4	CH B		CH switch data. CH switch data.		30	MAN D	0	For optional board.
(B/6)	5	Е		GND.		31	LAMP	0	Backlight LED control.
(D/O)	6	VOL	_	Voltage level input for audio control.					Normally: 0V, Lighting: 7.5V
	7	5CM	_	5V.		32	Q7	0	Key scan IC Q7 signal input.
	8	B+	_	Power input after passing through the fuse.		33	Q6	0	Key scan IC Q6 signal input.
	9	B+	_	Power input after passing through the fuse.	CN402	1	AFE	-	Audio GND.
	10	SB	_	Power output after power switch.		2	AFE	_	AUdio GND.
	11	SB	_	Power output after power switch.	CN403	1	SSW	ı	EXT/INT speaker switch input.
	12	TOGGLE		Normally: 5V, switched toggle when		2	SP+	0	BTL output + for external speaker.
	12	TOGGEE	'	connected GND.		3	SP-	0	BTL output – for external speaker.
	13	KEY2	l	Key input.		4	MSW	1	EXT/INT MIC switch input.
	14	Q6	0	Key scan IC Q6 signal output.		5	EMC	ı	External microphone input.
	15	Q7	0	Key scan IC Q7 signal output.		6	ME	_	External microphone GND.
	16	LCDCS	0	Chip select output for LCD driver.		7	PTT	ı	External PTT input.
	17	CLK	0	Clock data output for LCD driver.		8	PF	ı	Programmable function key input.
	18	DAT	0	Data output for LCD driver.		9	NC	_	Not use.
	19	LAMP	_	Backlight LED control.		10	E	_	GND
				Normally: 0V, Lighting: 7.5V		11	5M	_	5V.
	20	TXLED	_	TX LED control.		12	TXD	0	Serial data output.
				Normally: 0V, Lighting: 7.5V		13	RXD	ı	Serial data input.
	21	BLED	_	Busy LED control.		14	NC	_	Not use.
				Normally: 0V, Lighting: 7.5V		CC	NTROL	_ UI	NIT (X53-3780-XX) (A/6)
	22	5M	-	5V.	CN1	1	Q6	0	Key scan IC Q6 output.
CN401	1	TCONT	0	For optional board. (See page 22.)		2	Q7	0	Key scan IC Q7 output.
	2	OPPTT	0	For optional board. (See page 22.)	for	3	LAMP	1	Backlight LED control.
for	3	AMPSW	0	Audio AMP control switch output.	X57				Normally: 0V, Lighting: 7.5V
X53	4	EXAFC	0	External speaker switch control.		4	MAN D	1	For optional board. (See page 22.)
(A/6)	5	INAFC	0	Internal speaker switch control.		5	SD TO	1	For optional board. (See page 22.)
	6	KRS	0	Key scan IC reset output.		6	MUTE	ı	Audio mute signal input.
	7	KCK	0	Key scan IC clock data output.		7	SP-	0	BTL output – for external speaker.
	8	SOD	-	Not use.		8	SP+	0	BTL output + for external speaker.
	9	KEY1	I	Key input.		9	AFE	-	Audio GND.
	10	KEY2	ı	Key input.		10	AFE	_	Audio GND.
	11	LCS	_	Not use.		11	AF	ı	Audio output.
	12	PTT	1	PTT signal input.		12	MIC	0	MIC signal input.
	13	N/CSW	0	Noise canseler microphone switch output.		13	MICE	-	MIC GND.
	14	NC	-	Not use.		14	5M	-	5V.
	15	CLK	0	Clock data output.		15	SB	-	Power output after power switch.
	16	DAT	0	Data output.		16	SB	-	Power output after power switch.
	17	E	-	GND.		17	E	-	GND.
	18	SB	-	Power output after power switch.		18	DATA	ı	Data input.
	19	SB	-	Power output after power switch.		19	CLK		Clock data input.
	20	5M	-	5V.		20	NC	-	Not use.
	21	MICE	-	MIC GND.		21	N/CSW	I	Noise canseler microphone switch input
	22	MIC	I	MIC signal input.		22	PTT	0	PTT signal output.
		AF		Audio output.	1	23	LCS	-	Not use.
	23		0	-				_	
	23 24 25	AFE AFE	-	Audio GND. Audio GND.		24 25	KEY2 KEY1	0	Key output. Key output.

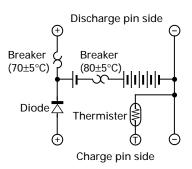
# **TERMINAL FUNCTION / KNB-17A (Ni-Cd BATTERY)**

CN No.	Pin No.	Name	I/O	Function
	26	NC	-	Not use.
	27	кск		Key scan IC clock data input.
	28	KRS	1	Key scan IC reset input.
	29	INAFC	1	Internal speaker switch input.
	30	EXAFC		External speaker switch input.
	31	AMPSW	l ,	Audio AMP control switch input.
	32	OPPTT	l ,	For optional board. (See page 22.)
	33	TCONT		For optional board. (See page 22.)
CN2	1	AFE	_	Audio GND.
for X57	2	AFE	_	Audio GND.
CN3	1	Q8	0	Key scan IC Q8 signal output.
	2	Q7	0	Key scan IC Q7 signal output.
	3	PTTE	-	PTT GND.
	4	PTT		Normally: 5V, transmit when connected GNI
	5	KI1		Key input.
	6	Q6	0	Key scan IC Q6 signal output.
CN4	1	+	-	BTL + output for internal speaker.
	2	_	_	BTL – output for internal speaker.
	CC	NTROL	. UN	NIT (X53-3780-XX) (B/6)
CN101	1	E	l _	GND.
0.1.0.	2	TOGGLE	0	Normally: 5V, switched toggle when
for	_	TOUGLE		connected GND.
X57	3	KI2	0	Key output.
AJ1	4	Q6		Key scan IC Q6 signal input.
	5			
		Q7	'	Key scan IC Q7 signal input.
	6	CE		Chip select input for LCD driver.
	7	CL		Clock data input for LCD driver.
	8	DI		Data input for LCD driver.
	9	LAMP	-	Backlight LED control.
				Normally: 0V, Lighting: 7.5V
	10	TX LED	-	TX LED control.
				Normally: 0V, Lighting: 7.5V
	11	BLED	-	Busy LED control.
				Normally: 0V, Lighting: 7.5V
	12	5M	-	5V.
	I	1	I	1

### **KNB-17A** External Vlew



# KNB-17A Circuit Diagram



# **KNB-17A** Specifications

Voltage	7.2V (1.2V x 6)
Charging current	1500mAh
Dimensions (mm)	58.0 W x 110.8 H x 20.0 D
(Projections included)	
Charger and charging time	
KSC-19 (Normal Charger)	Approx. 8 hours
KSC-20 (Rapid charger)	
Weight	220a

# **KMC-25/26 (SPEAKER MICROPHONE)**

### KMC-25 External View



## KMC-26 External View

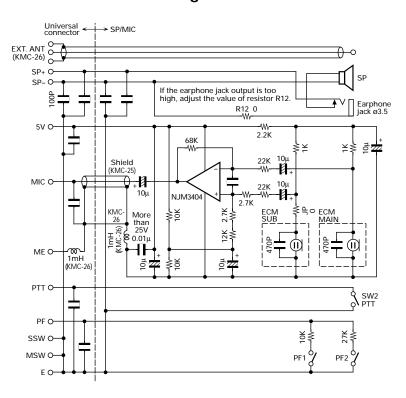
Note: Stubby antenna are options



# KMC-25/26 Specifications

Microphone	
Impedance	. 2k <b>Ω</b>
Sensitivity	65dB±4.0dB at 1kHz
Speaker	
Impedance	. 16Ω
Input	. 0.5W
Maximum input	. 1.5W
Dimensions	. 62 W x 81 H x 29 D (mm)
Weight (With plug cord)	. Approx. 0.17kg

## KMC-25/26 Circuit Diagram



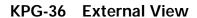
### KMC-25/26 Parts List

\* : New parts

Ref. No.	New parts	Parts No.	Description	Model
	*	A02-2092-08 A02-2253-08	Case (Front)	25 26
		A02-2253-08 A02-2093-08	Case (Front) Case (Rear)	25 25
	*	A02-2254-08	Case (Rear)	26
		B09-0382-08	Cap (Phone)	
		D10-0629-08	Lever (PTT)	
	*	E30-3287-08 E30-3322-08	Lead wire with plug assy Lead wire with plug assy	25 26
		J29-0644-08 J42-0495-08	Clip assy Bushing	
		S70-0459-08	Tact switch	
		T07-0359-08 T91-0584-08	Speaker MIC element	
		N08-0535-08	Dressed screw	

# KSC-19 (CHARGER) / KSC-20 (RAPID CHARGER) / KPG-36 (PRO-GRAMMING INTERFACE CABLE) / KRA-15 (WHIP ANTENNA)

## **KSC-19 External View**







## KSC-19 Charging

KNB-17A

Voltage	7.2V
Battery capacity	1500mAh
Charging time	Approx. 8 hours

### KSC-20 External View



# **KSC-20 Specifications**

Ambient temperature ...... 0°C~40°C

Weight ..... 0.18kg

### **KRA-15 External View**



# **SPECIFICATIONS**

#### **GENERAL**

Channel Spacing ...... Wide: 25kHz, Narrow: 12.5kHz

(PLL channel step 5kHz, 25kHz)

Battery Voltage ...... 7.5V DC ± 20%

Temperature Range ...... -30°C to +60°C (-22°F to +140°F)

### **RECEIVER** (Measurements made per EIA standard EIA-316-B)

Sensitivity

EIA 12dB SINAD ...... 0.25uV 20dB Quieting ...... 0.35μV

Spurious and Image Rejection...... -73dB

Frequency Stability ...... ±0.0002% from -30°C to +60°C

Channel Frequency Spread ...... 40MHz

#### TRANSMITTER (Measurements made per EIA standard EIA-316-B)

RF Power Output ...... Hi: 4W, Low: 1W

FM Noise ...... Wide: -45dB, Narrow: -40dB Audio Distortion ...... Less than 3.0% at 1000Hz 

Channel Frequency Spread ...... 40MHz

# KENWOOD CORPORATION

14-6, Dogenzaka 1-chome, Shibuya-ku, Tokyo 150-8501, Japan

KENWOOD SERVICE CORPORATION

P.O. BOX 22745, 2201 East Dominguez Street, Long Beach, CA 90801-5745, U.S.A.

KENWOOD ELECTRONICS LATIN AMERICA S.A.

P.O. BOX 55-2791 Piso 6 Plaza Chase Cl. 47 y Aquilino de la Guardio Panama, Republic of Panama

KENWOOD ELECTRONICS CANADA INC.

6070 Kestrel Road, Mississauga, Ontario, Canada L5T 1S8

KENWOOD ELECTRONICS DEUTSCHLAND GMBH

Rembrücker Str. 15, 63150 Heusenstamm, Germany

KENWOOD ELECTRONICS BELGIUM N.V.

Mechelsesteenweg 418 B-1930 Zaventem, Belgium

KENWOOD ELECTRONICS FRANCE S.A.

13, Boulevard Ney, 75018 Paris, France

KENWOOD ELECTRONICS U.K. LIMITED

KENWOOD House, Dwight Road, Watford, Herts., WD1 8EB United Kingdom

KENWOOD ELECTRONICS EUROPE B.V. Amsterdamseweg 37, 1422 AC Uithoorn, The Netherlands

KENWOOD ELECTRONICS ITALIA S.p.A.

Via G. Sirtori, 7/9 20129 Milano, Italy

KENWOOD IBERICA S.A.

Bolivia, 239-08020 Barcelona, Spain

KENWOOD ELECTRONICS AUSTRALIA PTY. LTD.

(A.C.N. 001 499 074)

P.O. Box 504, 8 Figtree Drive, Australia Centre, Homebush, N.S.W. 2140, Australia

KENWOOD & LEE ELECTRONICS, LTD.

Unit 3712-3724, Level 37, Tower one Metroplaza, 223 Hing Fong Road, Kwai Fong, N.T., Hong Kong