

COMMUNICATIONS SYSTEM ANALYZER

R-2001A/R-2002A





Communications Group

R-2001A/R-2002A COMMUNICATIONS SYSTEM ANALYZER



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TABLE OF CONTENTS

Paragra	Paragraph Page	
	FOREWORD	
	SECTION 1	
1-1	Introduction	. 1-1
	SECTION 2 — DESCRIPTION	
2-1	Description	. 2-1
2-3	Microprocessor	. 2-1
2-4	Display	2-1
2-5	System Warnings	2-1
2-6	Functions	2-1
2-7	AM, FM, CW, DSB Signal Generation	2-2
2-8	Simultaneous Modulation	2-2
2-9	Modulation Display	2-2
2-10	Sweep Generation	2-2
2-11	SINAD Metering	2-2
2-12	Multi-Mode Code Synthesizer	2-2
2-13	Off-The-Air Monitor	2-2
2-14	IF Display	2-2
2-15	Spectrum Analyzer	2-3
2-16	RF Burnout Protection	2-3
2-17	Terminated RF Power Measurement	2-3
2-18	In-Line Power Measurement	2-3
2-19	Duplex Generator	2-3
2-20	500-kHz Oscilloscope	2-3
2-21	Frequency Counter	2-4
2-22	AC/DC VOITMETER	2-4
2-23	Power Supply	2-4
2-24	Accessories	2-4

SECTION 3 — INSTALLATION

3-1	Packing Information	3-1
3-4	Initial Setup	3-1
3-5	Analyzer	3-1
3-6	Battery Pack	3-2
3-7	Blower Assembly	3-2

SECTION 4 — OPERATION

4-1	General	4-1
4-3	Controls, Indicators, and Connectors	4-1
4-5	Operation	4-12
4-7	Calibrate	4-12

Paragraph

SECTION 4 — OPERATION (CONT)

4-8	Generator Operation	4-13
4-9	Duplex Generation	4-14
4-10	Frequency Counter	4-15
4-11	Spectrum Analyzer	4-15
4-12	Monitor	4-16
4-13	Ext Wattmeter	4-17
4-14	Simultaneous Generate and Measurement Operations	4-17

SECTION 5 — MAINTENANCE

5-1	Service	5-1
5-6	Replacement Parts Ordering	.5-1
5-9	Addresses	5-1
5-10	General Offices	5-1
5-11	U.S.Orders	5-2
5-12	Canadian Motorola Electronics Company	5-2
5-13	All Countries Except U. S. and Canada	5-2
5-14	Major Assemblies	5-4
5-16	Theory of Operation	5-5
5-17	General	5-5
5-20	System Control	5-5
5-25	Generate Mode	5-5
5-34	Power Meter	5-11
5-40	Monitor Mode	5-12
5-52	Duplex Generator	5-14
5-56	Code Synthesizer	5-15
5-65	Frequency Counter	5-17
5-71	Digital Voltmeter (DVM)	5-17
5-84	Oscilloscope	5-19
5-96	Sinad Meter	5-21
5-99	Alignment Procedure	5-22
5-100	Introduction	5-22
5-102	Test Equipment Required	5-22
5-104	Preparation for Alignment	5-23
5-105	Basic Alignment Procedure	5-23
5-118	Extended Alignment Procedure	5-27
5-126	Checkout Procedure	5-32
5-127	Introduction	5-32
5-129	Test Equipment Required	5-32
5-131	Procedure	5-33
5-146	System Troubleshooting	5-37

SECTION 6 — SYSTEM INTERCONNECT AND PARTS LISTS

Paragraph

SECTION 7 - LOW VOLTAGE POWER SUPPLY (A1)

7-1	General	7-1
7-2	Input Power Control	7-1
7-6	DC Output Control	7-1
7-9	Protection Circuitry	7-1
7-13	High Voltage Supply Control	7-2

SECTION 8 — SCOPE AMPLIFIER (A2)

8-1	General	8-1
8-2	Deflection Amplifiers	8-1
8-3	Horizontal Timebase Generator	8-1
8-5	Horizontal Switching	8-1
8-6	Intensity Control	8-1
8-8	Focus Control	8-1
8-9	Astigmatism, Geometry, and Trace Rotation	.8-1

SECTION 9 - SCOPE/DVM CONTROL MODULE (A3)

9-1	General	9-1
9-4	Scope Vertical Control	9-1
9-7	SSB Detection	9-1
9-8	455 kHz PLL	9-2
9-9	Scope Horizontal Control	9-2
9-11	Synthesizer Sweep Control	9-2
9-12	Scope Z-Axis Control	9-2
9-13	Modulation Display Control	9-2
9-17	Peak Detector	9-3
9-18	DVM Control	9-3
9-21	SINAD Detection	9-4
9-22	Module Control	9-4

SECTION 10 — RECEIVER (A4)

10-1	General	10-1
10-2	Down Converter	10-1
10-4	Linear IF Amplifier and Detectors	10-1
10-8	Audio Switching and Filtering	10-1
10-10	Logarithmic Amplifier and Detector	10-2
10-11	Alarm Generator and Audio Amplifier	10-2
10-12	Module Control	10-2

SECTION 11 — RF SYNTHESIZER (A5)

11-1	General	 11-1
11-2	Frequency Synthesis Scheme	 11-1

Paragraph

SECTION 11 - RF SYNTHESIZER (A5) (CONT)

11-5	310-440 MHz Phase Locked Loop	11-1
11-6	60.5 MHz Phase Locked Loop	11-1
11-7	550 MHz Phase Locked Loop	11-1
11-9	500-1000 MHz Phase Locked Loop	11-2
11-10	Modulation Control	11-2
11-11	Module Control	11-2

SECTION 12 - AUDIO SYNTHESIZER (A6)

12-1	General	12-1
12-2	Private Line Generator	12-1
12-7	DPL Generator	.12-1
12-10	1 kHz Tone	12-2
12-11	External Modulation	12-2
12-12	Modulation Control	12-2
12-14	Module Control	12-2

SECTION 13 - PROCESSOR I/O MODULE (A7)

13-1	General	13-1
13-2	10.245 MHz Phase Locked Loop	13-1
13-3	System Control Bus Interface	13-1
13-6	DVM	13-1
13-8	Frequency Counter	13-1

SECTION 14 - IEEE INTERFACE MODULE (A8)

14-1	General	14-1
14.2	IEEE Bus Interface	14-1
14-3	RF Level Control	14-1
14-5	Modulation Control	14-1
14-6	Address Decode and Control Latches	14-1

SECTION 15 - PROCESSOR MODULE (A9)

15-1	General		15-1
15-2	Proces	sor	15-1
15-6	Characte	r Generator	15-1

SECTION 16 — HIGH VOLTAGE POWER SUPPLY (A10)

16-1	Genera	al		 16-1
16-2	High	Voltage	Supply	 16-1

SECTION 17 — RF INPUT MODULE (AH)

17-1	General	17-1
17-2	Input Protection and Power Meter	17-1
17-4	Wideband Amplifier and Frequency Converter	17-1
17-9	Duplex Generator	17-2

SECTION 18 - FRONT PANEL INTERFACE MODULE

18-1	General	18-1
18-2	Input Coupling and Ranging	18-1
18-4	DVM Buffer	18-1
18-5	Frequency Counter Preamp	18-1
18-6	Scope Vertical Preamp	18-1
18-7	Scope Horizontal Preamp	18-1
18-8	Control and Display Interface	18-1

SECTION 19 - 10 MHz FREQUENCY STANDARD MODULE (A13)

19-1	General	19-	1
------	---------	-----	---

SECTION 20 - FRONT PANEL (A14)

SECTION 21 — BLOWER ASSEMBLY

SECTION 22 — IEEE - 488 BUS CONTROL

Introduction	22-1
IEEE-488 Bus Structure	22-3
Bus Signals	. 22-3
Data Transfer	. 22-3
2 Programming	22-3
Command Structure	. 22-4
Command Strings	22-10
Command Types	22-10
Trigger Command	22-10
Return Data	22-10
Programming Commands	22-11
Terminal Mode	22-11
Error Messages	22-11
Service Requests	22-14
Programming Considerations	22-15
General	22-16
R2002A Analyzer Configuration	22-16
	Introduction IEEE-488 Bus Structure Bus Signals Data Transfer Programming Command Structure Command Strings Command Types Trigger Command Return Data Programming Commands Terminal Mode Error Messages Service Requests Programming Considerations General R2002A Analyzer Configuration

SECTION 23 — BATTERY ASSEMBLY

LIST OF ILLUSTRATIONS

Figure Page
1-1 Communications System Analyzer 1-0
2-1 Accessories Supplied with Analyzer
3-1 Typical Communication System Analyzer Packaging
4-1 Controls, Indicators, and Connectors, Front Panel 4-2
4-2 Controls, Indicators, and Connectors, Left Side Panel 4-3
4-3 Controls, Indicators, and Connectors, Rear Panel 4-3
4-4 System Analyzer Time Base Calibrate Test Setup and CRT Display 4-13
4-5 Duplex Generation Test Setup and CRT Display 4-14
4-6 Spectrum Test Setup and CRT Display 4-16
4-7 Wattmeter Test Setup and CRT Display 4-17
4-8 Test Setup for FM Receiver Sensitivity Using Generator and SINAD
Meter with CRT Display 4-18
4-9 Test Setup for Pager and Alert Functions with CRT Display 4-19
4-10 Test Setup for Using DVM and Signal Generate with CRT Display
5-1 Communication System Analyzer, Top View Cover Removed
5-2 Communication System Analyzer, Bottom View Cover Removed
5-4 Generate Mode Block Diagram 5-10
5-5 Power Meter Block Diagram 5-12
5-6 Monitor Mode Block Diagram 5-13
5-7 Duplex Generator Block Diagram 5-15
5-8 Code Synthesizer Block Diagram 5-16
5-9 Frequency Counter Block Diagram 5-17
5-10 Digital Voltmeter (DVM) Block Diagram 5-18
5-11 Oscilloscope Block Diagram
5-12 Sinad Meter Block Diagram
5-13 Scope Amplifier Alignment Points
5-14 Front Panel Interface Alignment Points 5-26
5-15 Scope/DVM Control Alignment Points 5-27
5-16 Horizontal Time Base Alignment Points 5-27
5-17 DVM Input Buffer Alignment Points 5-28
5-18 Scope/DVM Control Test Point Numbering
5-19 Processor I/O A/D Alignment Points

LIST OF TABLES

-			
	2	h	
	α	υ	IC.

·	Page
Physical Characteristics	1-1
Electrical Characteristics	. 1-1
Input/Output Characteristics	. 1-5
Accessories Supplied with the Communication Systems Analyzer	2-5
Optional Equipment for Use with Analyzer	. 2-5
Controls, Indicators, and Connectors	. 4-1
List of Subassemblies	5-4
Control Buses and Functions	5-9
Basic Test Equipment Required	. 5-22
Extended Test Equipment Required	5-23
Test Equipment	. 5-32
System Troubleshooting	. 5-37
Test Point Identification	. 5-42
Internal DVM Inputs	. 9-3
IEEE-488 Interface Controllable Functions	. 22-2
Command Categories	. 22-4
Programming Commands	. 22-5
Terminal Mode ASC II Characters Printable Characters	22-12
Error Messages	22-14
SRQ Data	22-14
Code Synthesizer Programming Considerations	22-15
	Physical Characteristics Electrical Characteristics Input/Output Characteristics Accessories Supplied with the Communication Systems Analyzer Optional Equipment for Use with Analyzer Controls, Indicators, and Connectors List of Subassemblies Control Buses and Functions Basic Test Equipment Required Extended Test Equipment Required Test Equipment System Troubleshooting Test Point Identification Internal DVM Inputs IEEE-488 Interface Controllable Functions Command Categories Programming Commands Terminal Mode ASC II Characters Printable Characters Error Messages SRQ Data Code Synthesizer Programming Considerations

FOREWORD

1. SCOPE OF MANUAL

This manual contains information for the installation, operation, and maintenance of the Communications System Analyzer.

2. PURPOSE AND USE

The Motorola Communications System Analyzer is a portable test instrument, designed specifically for the service and monitoring of communications equipment. Its functions supersede those of a Service Monitor, expanding the features and capabilities to the point wherein servicing is achieved with a single instrument, rather than a host of separate equipment.

The R2001A is the standard Communications System Analyzer. The R2002A Analyzer, which contains the IEEE-488 Standard interface control bus, is also available. Programming for the R2002A is covered in Secton 22 of this manual.

The Analyzer improves a technician's efficiency and accuracy and reduces servicing time.

The Communications System Analyzer performs the functions of signal generation, signal monitoring, and the tests normally associated with the devices listed below.

- Spectrum Analyzer
- Duplex Generator
- Modulation Oscilloscope
- Frequency Counter
- AC/DC Digital Voltmeter
- RF Wattmeter
- General Purpose Oscilloscope
- Multi-Mode Code Synthesizer
- SINAD Meter
- Sweep Generator

The Analyzer meets the shock and vibration requirements of EIA test RS152B, the same specifications met by Motorola mobile radios. This minimizes failures when the instrument is used in a mobile service van, and means it is as tough as the radios it services.

The Communications System Analyzer is designed to be serviced quickly and easily, should a breakdown occur. The majority of the circuitry is on seven modular plug-in circuit boards which have built-in test points that aid in isolating the problem to a specific board. Simple plug-in replacement gets the instrument back in



Figure 1-1. Communications System Analyzer

SECTION 1

1-1 INTRODUCTION

1-2 This section lists the physical, electrical, and input/output characteristics of the Communications stem Analyzer shown in figure 1-1.

Table 1-1.	Physical	Characteristics

Characteristic	Description	
	•	
Length	20.75 inches (52.7 cm)	
Width	15.75 inches (40.0 cm)	
Height	8.25 inches (21.0 cm)	
Weight	48 pounds (21.9 kg) (Excluding Battery Pack)	
Та	ble 1-2. Electrical Characteristics	
Characteristic	Description	
	Signal Generator Mode	
Frequency		
Range	10 kHz to 999.9999 MHz	
Resolution	100 Hz	
Accuracy	Equal to master oscillator time base	
Output (into 50 ohms)		
Attenuator:	16 dB variable plus 10 dB steps over 13 ranges	
Range:	0.1 <i>u</i> V to 1 Vrms (-127 dBm to +13 dBm)	
Accuracy:	±2 dB accuracy on 0 dB step attenuator range	
	±2 dB across other step attenuator ranges	
	±1 dB over temperature range	
Spectral purity		
Spurious:	<-40dB	
Harmonics:	<-15 dB	
Frequency modulation		
Range:	0 - 50 kHz peak	
Accuracy:	±5% of full scale	
FM residual noise:	100 Hz	
External/internal frequency		
range:	5 Hz - 10 kHz (±1 dB)	
External input:	Approximately 150 mV for 20 kHz deviation	
Modes:	Internal, external, microphone or all simultaneously	

Table 1-2. Electrical Characteristics (Continued)

Characteristic	Description
Amplitude modulation Range: Accuracy: External/internal frequency range: External input: Modes:	0 to 80% from 1 to 500 MHz ±10% of full scale from 0% to 50% AM 5 Hz - 10 kHz (±1 dB) Approximately 150 mV for 80%, BNC connector Internal, external, microphone or all simultaneously
Double sideband suppressed carrier	
Carrier suppression:	> 25 dB (1 MHz - 500 MHz)
Frequency Range: Resolution: Accuracy:	1 MHz to 999.9999 MHz 100 Hz Equal to that of master oscillator time base
Frequency error indicator	Autoranging CRT display. ± 10 Hz resolution for frequency error measurements on 1.5 kHz, 5 kHz and 15 kHz full scale ranges. ± 1 Hz resolution on the 50 Hz full scale range.
Input sensitivity	1.5 <i>ft</i> V for 10 dB EIA Sinad (narrow band \pm 6 kHz mod. acceptance) 7 <i>fjtV</i> for 10 dB EIA Sinad (wide band \pm 100 kHz mod. acceptance) 4 MHz to 1000 MHz. Useable to 1 MHz.
Spurious response	-40 dB typical 0 dB image at ±21.4 MHz -10 dB at L.O. harmonics ±10.7 MHz
Deviation Measurement	
Range:	1, 10, 100 kHz full scale
Accuracy: Peak deviation limit alarm:	±5% of reading ±100 Hz from 500 Hz to 50 kHz deviation; ±10% of reading from 50 kHz to 75 kHz deviation Set via keyboard to 100 Hz resolution (0 kHz to 99.9 kHz). Audible alarm indicates limit condition in all Monitor Modes.
AM modulation measurement	
Range: Accuracy:	0 to 100% ±5% of full scale

Characteristic	Description
RF Wattmeter	
(Autoranging display)	
Frequency range:	1 MHz to 1000 MHz
Power range:	1.0 watts to 125 wattts
Accuracy:	±10%, 1 watt to 125 watts
Protection	Over temp indicator
	General
	Spectrum Analyzer
Dynamic range	>75 dB displayed, - 105 dBm to +30 dBm input range with step attenuator
Frequency	
Range	4 MHz to 1,000MHz
Full scale frequency	Adjustable between 1 MHz and 10 MHz
dispersion:	
	Duplex Generator
Frequency offset	Adjustable from 0 to 10 MHz plus fixed offset of
	45 MHz (high or low side)
Modulation level (FM only)	Adjustable from 0 to 20 kHz peak deviation
	Oscilloscope
Size	8 cm x 10 cm
Frequency response	DC to 0.5 MHz (3 dB point)
External vertical input range	10 mV, 100 mV, 1V, 10V (per division)
Sweep rates	1 [1 s, 10 <i>H</i> s, 0.1 ms, 1 ms, 0.01S, 0.18 (per division)
Sync	Automatic or normal triggering
	Frequency Counter
Frequency range	
	10 Hz to 35 MHz
Readout	5 digit, autoranging
Input sensitivity	30 mV from 10 Hz to 1 MHz
	50 mV from 1 MHz to 35 MHz

Characteristic	Description
	Digital Voltmeter
Readout DC accuracy AC accuracy AC bandwidth	Auto ranging digital display, 1, 10, 100, 300 volts full scale. AC-dBm calibrated across 600 ohms. ±1% of full scale ±1 least significant digit ±5% of full scale 50 Hz to 10 kHz
	Modulation Source
Code Synthesizer Frequency range Resolution Frequency accuracy Distortion	5 Hz to 9.9999 kHz sinewave 0.1 Hz ±0.01% <1%
Signaling sequences	Four fixed 1. Tone only 2. Tone with battery saver 3. Tone and voice 4. Group call Four user programmable
Tone remote access	Remote base access sequence as follows Tone A for 150 msec Tone B for 40 msec 10 dB below Tone A Tone A continuously 30 dB below the first Tone A burst
Digital private line (DPL) Fixed 1 kHz	Codes 000 to 777 and inverted
Distortion External input	<1%
Microphone External Jack	Standard RTM 4000A microphone interface with IDC.
Frequency range	5 Hz to 10 kHz
Level	7 vrms maximum 10 Kobm pominal
Code synthesizer external output level	0-3 vrms into a 600 ohm load
	SINAD Meter
Input level range Sinad accuracy	0.5V to 10 Vrms ±1 dB at 12 dB Sinad

Characteristic	Description			
Ν	Manual Frequency Scan			
Step size	Switch Selectable: 100 Hz, 1 kHz, 10 kHz, 100 kHz and 1 MHz (+ or-)			
Step rate	5 steps/sec.			
	Time Base			
Standard TCXO	Aging: ±1 x 10-° per year Temp: ±1 x 10- ⁶ maximum error over the 0° to 55° C temp. range			
Optional ovenized high stability	Aging: $\pm 1 \times 10^{-6}$ per year Temp: $\pm 5 \times 10^{-8}$ maximum error over the 0° to 55°C temp range (warmup to $\pm 5 \times 10^{-7}$ of final frequency within 20 minutes)			
Power and Environmental				
AC DC Optional battery Temperature range	100-130 VAC, 200-260 VAC 47-63 Hz +11.5 VDC to +16 VDC 13.6V battery - provides 1 hour continuous operation 0° to 55°C operating; -40 to 85°C storage			

Table 1-3.	Input/Output Characteristics
------------	------------------------------

Characteristic	Description		
Input			
Ext mod in	10K ohms nominal, 150 mV typical for 20 kHz dev. FM or 80% AM		
Mic.	Mic input provides bias and IDC limiting suitable for Motorola RTM 9000A handset. PTT switches R2001 from monitor to generate.		
Ext Horiz	1 volt minimum for full screen deflection. Maximum input 10 volts.		
Vert/Sinad/DVM/Counter In	 Meg ohm, 40 pf Nominal; ±300 volts DC max, 300 Vrms max at frequencies below 500 Hz, 10 Vrms max up to 35 MHz Scope vert in: DC to 500 kHz or 50 Hz to 500 kHz AC mode (±3 d8) Sinad in: 0.5 to 10 Vrms in at 1 kHz 		

Characteristics	Description
PE la/Out	 DVM in: 1, 10, 100 and 300V full scale AC or DC. AC bandwidth 50 Hz to 10 kHz for ±5% F.S. accuracy (AC dBm calibrated across 600 ohms) Frequency counter in: 30 mV or greater required from 10 Hz to 1 MHz. 50 mV or greater required from 1 MHz to 35 MHz
	The RF In/Out Jack is protected against RF overload. However, to prevent undue stress on the protected circuits it is advisable to always switch the system to the power monitor mode before applying power in excess of 200 mW. Additional protection is also obtained by making it a practice not to leave the step attenuator in the 0 dB position.
Ext Wattmeter 10 MHz std in (rear panel)	Characteristics suitable for Motorola ST-1200 series Wattmeter Elements 70 to 350 mV rms input required at 10 MHz, impedance greater than 50 ohms.
	Output
Mod out Demod out	Up to 11 vpp into 600 ohms 10 Hz to 10 kHz Typically 3 vpp into 600 ohms for ± 5 kHz deviation narrowband, 4 vpp for ± 75 kHz deviation wideband; DC to 10 kHz response
RF in/out	1.0 Vrms (+13 dBm) to 0.1 Vrms (-127 dBm) 50 ohm nominal source impedance. 10 kHz to 1.0 GHz.
Duplex gen out	-30 dBm typical, 50 ohm nominal source impedance 2 MHz to 1 GHz
10 MHz std out (rear panel)	250 mV rms nominal output into 50 ohms

SECTION 2

DESCRIPTION

2-1. DESCRIPTION

2-2. The Communication System Analyzer is a portable test instrument designed for servicing and monitoring of portable, mobile, and land base communications equipment operating over the frequency range of 1 MHz to 1 GHz. The unit performs the functions of signal generation, frequency error and modulation measurement. It is also capable of a variety of tests normally associated with the following devices:

Spectrum analyzer Duplex offset generator Modulation oscilloscope Frequency counter AC/DC digital-analog voltmeter RF wattmeter General purpose oscilloscope Multi-mode code synthesizer SINAD meter Sweep generator

2-3. MICROPROCESSOR. A Motorola M-6800 series microprocessor permits keyboard entry of data, autoranging of displays, fast frequency access, and permanent storage of often-used frequencies and codes. Generate and monitor RF frequencies, tone codes, and timing sequences can be programmed into a nonvolatile memory, saving time and eliminating entry errors. When one particular type of equipment is continuously serviced, the unit can be programmed to select the mode of operation required when first turned on.

2.4 DISPLAY. All functions, generated or monitored, are presented on an 8 cm x 10 cm cathode ray tube (CRT) in both analog and digital format, with the name of the function being displayed. The CRT also displays control settings eliminating the need for operator search of different equipment panels. Digital readouts are visually aided by the use of the continuously autoranging analog line segments, which are similar to a bar graph. Each has a base line and calibration markers, in addition to the intensified segment showing the measurement. The user selectable displays are listed in a column beneath the DISPLAY heading on the front panel. Choosing a display is accomplished by pressing an arrow button below the column, for up or down movement, as required. When the appropriate arrow is pressed, the LED adjacent to the selected display illuminates. FUNCTION is selected in the same way, providing rapid, accurate changes in service capability at the touch of a button.

2-5. SYSTEM WARNINGS. To aid the technician in servicing, visual warnings will appear on the CRT when certain overload or caution conditions exist. Displays warn of low battery power, overheating of the RF load, or an improper attenuator setting for particular measurements. In addition, a continuous audible alarm sounds when a preset deviation limit is exceeded in monitor modes. This limit is entered by using the keyboard and may be programmed from 0 kHz to 99.9 kHz, with 100 Hz resolution.

2-6. FUNCTIONS. The following paragraphs briefly describe the major functions of the Communications System Analyzer.

2-7. AM, FM, CW, DSB Signal Generation. The built-in general purpose signal generator provides continuous coverage of the HF, VHP, and UHF land mobile spectrum for receiver testing. Many forms of external and internal modulation can be simultaneously impressed on the carrier signal for actual composite signals. The frequency range of the RF signal generator is from 10 kHz to 1000 MHz in 100 Hz steps. The output of up to 1 Volt rms provides sufficient amplitude to get through misaligned tuners and receivers, and is especially effective when changing a receiver's frequency. The high level, clean output is available over the entire frequency range of the Communications System Analyzer. The output frequency is referenced to an internal time base which can be calibrated to the WWV Standard. (See paragraph 4-7.)

2-8. Simultaneous Modulation. Modulation is simultaneously available from an internal 1 kHz tone generator, a multi-mode code synthesizer, and from external inputs. The external modulation can be voice from a standard Motorola mobile radio microphone (which plugs into the front panel of the instrument), as well as a signal applied to the external BNC input. Separate controls are provided for independently setting the levels of the 1 kHz tone, the code synthesizer, and the external modulation sources. The 1 kHz test tone is a convenient source of modulation for making SI NAD measurements. A MOD OUT connector provides external access to all of the modulation signals.

2-9. Modulation Display. The recovered audio waveform, or audio used to modulate the generator carrier, can be viewed on the CRT. It is used to graphically measure deviation, and to aid in waveform analysis.

2-10. Sweep Generation. The sweep generator mode provides an RF output that is swept in frequency across a band centered at the programmed frequency. A synchronized horizontal sweep for the internal oscilloscope allows filter characteristics to be easily determined. This is ideal for in-depth troubleshooting of IF amplifiers and filters.

2-11. SINAD Metering. A comprehensive check of receiver performance can be made with a SINAD measurement. The analog line segment and digital representation of SINAD appear automatically whenever the unit is in the normal generate mode. The only hookups required are from the Communications System Analyzer to the RF input of the receiver under test, and from the audio output of the receiver to the instrument's multipurpose input. The measurement, and appropriate servicing, can then be accomplished withoutthe need for a separate signal generator, SINAD meter or distortion analyzer.

2-12. Multi-Mode Code Synthesizer. The Communications System Analyzer generates Private Line tones (PL), Digital Private Line codes (DPL), two-tone sequential paging codes and tone-remote base signaling tones. All codes are available at the Mod Out jack, as well as being used internally to modulate the RF signal generator. This eliminates the necessity of using separate generators and oscillators for general servicing, setting transmitter deviation, or for checking tone-remote-base control lines. Timing sequences are also stored in the Tone Memory to provide fast set-up and eliminate errors. User programmable timing sequences are also provided to allow the storage of non-standard or future time sequences.

2-13. Off-the-Air Monitor. The 1.5 uV sensitivity of the Communications System Analyzer receiver allows off-the-air monitoring and measurement of transmitter frequency error and deviation to 1000MHz. A variable squelch allows weak signals to be monitored, but can beset higher to ensure the proper signal-to-noise ratio for measurement accuracy. The off-the-air monitor function enables frequent parameter checks without leaving the shop, thus spotting system degradation early and keeping service costs down, Bandwidth can be set Wide for off-channel signal location or wide band FM; or Narrow for maximum sensitivity and selectivity.

2-14. IF Display. When the IF display mode is selected, the Communications System Analyzer's receiver IF envelope is shown on the CRT. This allows the technician **to** qualitatively and quantitatively assess the amplitude modulation envelope of a transmitter.

2-15. Spectrum Analyzer. In this mode of operation the CRT displays a window of the RF spectrum whose bandwidth (from 1 MHz to 10 MHz) is determined by the DISPERSION/SWEEP control. The center frequency of this window ranges from 4 MHz to 1,000 MHz, selectable by entering a specific center frequency with the keyboard. This center frequency is digitally displayed at the top of the CRT screen, eliminating the need for an external signal generator, and counter to provide markers. Once a signal is centered on the screen, positive identification is aided by switching the Analyzer to MONITOR AM or FM and listening to the demodulated output via the built-in audio amplifier and speaker. The spectrum analyzer's center frequency can be scanned up or down at rates varying from 0.5 kHz per second to 5 MHz per second, using the RF scan control. Slow rates are used to precisely determine a subject signal's frequency while faster rates are used for locating intermittent transmissions or viewing large areas of the spectrum in a short time. Uses of the Spectrum Analyzer are: Intermodulation interference identification, IF and RF signal tracing, transmitter harmonics measurements, transmitter spurious checks, and receiver local oscillator radiation.

2-16. RF Burnout Protection. At RF input levels above 200 mW, in any operating mode, the input automatically switches to the internal 125 watt RF load, thus protecting the attenuator and signal generator against damage from a keyed transmitter. If power above 200 mW is applied in any mode except the power monitor mode an audiable alarm sounds and a visual warning on the CRT directs the operator to switch to the power monitor mode.

CAUTION

To prevent undue stress on the protected circuits it is advisable to always switch the system to the power monitor mode before applying power in excess of 200 mW. Additional protection is also obtained by making it a practice not to leave the step attenuator in the 0 dB position.

2-17. Terminated RF Power Measurement. RF power is automatically measured when the Communications System Analyzer is in the Power-Monitor mode. The built-in RF load dissipates up to 50 watts for three minutes and up to 125 watts for one minute. If a high power transmitter should be keyed into the unit for a time long enough to threaten overheating of the power measuring circuitry, the audible alarm sounds and the CRT display changes to read "RF LOAD OVER-TEMP," thus warning the technician to un-key. This instrument function is further enhanced by the simultaneous indication of RF power output, carrier frequency error, and modulation, all on the same CRT display.

2-18. In-Line Power Measurement. Use of the Motorola ST-1200 series Wattmeter elements in conjunction with the analyzer's external wattmeter display provides measurement of forward and reflected antenna power on the CRT display. This capability eliminates the complex hook-ups and the additional instruments normally required for antenna measurements.

2-19. Duplex Generator. In this mode, the Communications System Analyzer simultaneously receives and generates the signals for duplex radio servicing, while generated and monitored frequencies are observed on the CRT. In the 0-10 MHz range, the 'Freq. Set'control tunes the proper offset frequency for the VHF and UHF bands. The 45 MHz mode provides a single offset for the 800 MHz range. A switch is also provided to select high or low side offset, as required. The Duplex Generator provides enhanced capability to service equipment such as repeaters, car telephones and Emergency Medical Telemetry portables.

2-20. 500-kHz Oscilloscope. This general purpose scope is ideal for waveform analysis in two-way communication servicing. Use it for viewing modulation signals (either internally or externally generated), detection of asymmetric modulation or audio distortion, and general purpose signal tracing and troubleshooting.

2-21. Frequency Counter. The frequency counter measures inputs in a range from 10 Hz to 35 MHz. Its 5 digit auto-ranging output is displayed on the CRT and allows precise measurement and setting of offset oscillators, 35 kHz and 455 kHz pager IF's, PL frequencies and other external input signals. This function will also operate simultaneously with the generate or monitor receiver modes of operation. Frequency measurement of transmitted carriers and other signals higher than 35 MHz is easily accomplished with the frequency error readout in the monitor modes.

2-22. AC/DC Voltmeter. Switching to the DVM mode provides a digital-analog voltage presentation on the CRT, along with the corresponding dBm value. The auto-ranging display provides full scale deflections of 1, 10, 100 and 300 Volts. AC or DC measurement is selected on the CRT. The meter's wide dynamic range and three digit display are ideal for setting power supply voltages, checking bias levels, and setting audio levels. Like the Frequency Counter, the DVM will operate simultaneously with generate or monitor operation.

2-23. Power Supply. The Communications System Analyzer may be powered by a variety of sources:

- AC at 110 or 220 Volts, 50/60 Hz
- DC from an external 12 Volt source such as a service vehicle
- DC from an optional battery pack. Servicing can thus be accomplished wherever the equipment under test is located

2-24. ACCESSORIES.

2-25. Table 2-1 lists the accessories supplied with the Communication System Analyzer. Optional equipment available for use with the unit is listed in Table 2-2.



Figure 2-1. Accessories Supplied with Analyzer

Table 2-1. Accessories Supplied with the Communication Systems Analyzer

Equipment	Motorola Part No.	Use	
Front cover	15-80335A70	Front panel and CRT protection, storage of cables, power cord, and other equipment for on-site servicing.	
Sun shade	15-80335A55	Snap over CRT during use in bright sunlight.	
Power cord	30-80336A36	Three conductor cord to supply AC power to unit. Also used when charging optional battery pack.	
Oscilloscope probe	RTL-4058A	A X1 probe with attachments for general servicing.	
In-line wattmeter adapter	RTL-4055A	Allows use of Motorola ST-1200 series in-line watt- meter elements for direct measurement and display of forward and reflected transmitted power.	
Coax adapter	58-84300A98	Adapts front panel "N" connector to BNC female.	
Antenna	TEKA-24A	Plugs into RF in/out connector on front panel with N to BNC adapter. Used for off-the-air transmitter and receiver tests.	
Test microphone	RTM-4000A	Used for voice modulation of signals.	
Connector kit	RPX-4097A	Consists of connector shell, clamp, and four connector pins. Used to fabricate a mating plug for male dc power connector at back of analyzer. Enables user to make a dc power cable to inter- connect separate power source to analyzer. Pins 1 and 2 are positive, pin 3 is the charging line, pin 4 is ground.	
Table 2-2. Optional Equipment for Use with Analyzer			
Equipment	Motorola Part No.	Use	
IEEE-488 Standard interface bus option	Consult factory for retrofit information.	Enables fully automatic testing with the unit by external control from a computer or programmable controller.	
Blower	RTL-4054A	Provides additional cooling in high ambient temperature conditions	

Equipment	Motorola Part No.	Use
Battery pack	RTP-1002A	13.6 volt battery and charger attaches to back of the unit. Provides one hour of continuous operation. Cannot be used with IEEE-488 or Blower options.
High-stability oscillator module	RTL-1007A	Improves stability of the time base as specified in electrical characteristics section.
Protective cover	RTL-4056A	Padded fabric type cover to protect unit from excessive field wear.

SECTION 3

INSTALLATION

3-1. PACKING INFORMATION

3-2. The unit is packaged in a fiberboard carton and protected by foam pieces as shown in figure 3-1. The unit is first packed in a cardboard container and then this carton is packed in a second, larger cardboard container, for further protection. Save the packing container and materials for future use.





3-3. All accessories supplied with the analyzer are packed in the analyzer cover.

3-4. INITIAL SETUP

3-5. ANALYZER. To set up the Analyzer for use, place the unit on workbench or in mobile repair unit. Remove the front cover by operating the two latches on the bottom of the cover. Lift the cover and slide it to the side to separate the hinges. Remove the power cord (AC or DC) that is stored in the cover, Attach the female connector of the power cord to the appropriate connector on the rear panel of the analyzer, and the other end to the power source. For AC power a grounded 3 wire power source of 100-130 Vac or 200-260 Vac, 47-63 Hz must be used.

NOTE

The unit is set for 110-130 Vac operation from the factory. For operation from 1 GO-110 Vac or 200-260 Vac, the voltage selection card must be readjusted before connection to the power source. This is accomplished by the following procedure:

- 1. Remove the power cord from the rear panel connector.
- 2. Slide the selector card cover door over the connector area exposing the selection card and fuse area.
- 3. Pull outward on the fuse ejector tab and remove fuse.
- 4. Remove the printed circuit board voltage selector card by pulling straight to the rear.
- 5. Reinsert the card at the orientation which causes the appropriate voltage range (marked on card) to be displayed.
- 6. Reinstall the fuse.
- 7. Slide the cover plate back to the original position, connect power cord, and proceed with system operation.

Remove the accessories to be used from the cover. Move the POWER switch to the ON position. When the Oven Ready indicator illuminates the unit's frequency standard is stabilized and the unit is ready for use, (instantaneous with standard TCXO).

CAUTION

When installing the analyzer in a vehicle, the DC supply line should be fused close to the vehicle battery. The analyzer is protected against overload by the DC 8A fuse on the rear of the unit, but the vehicle is not protected.

3-6. BATTERY PACK. The battery pack is attached to the rear of the analyzer with two clips and two screws. Align and slide the mounting clips of the battery pack into the slots on the mounting brackets on the left side of the back panel of the analyzer. Align the captive screws with the mounting holes on the right of the panel and tighten. Connect the power plug to the connector at the top right of the rear panel.

3-7. BLOWER ASSEMBLY. The blower assembly comes from the factory wired for continuous operation. That is, the blower will run whenever the analyzer is connected to the AC power line. If thermostatic operation is desired the wire jumper across the thermostat in the blower assembly must be clipped out. The blower will now run only when the back panel exceeds a preset operating temperature. Normally this temperature will only occur in high ambient temperature conditions.

3-8. The blower assembly is mounted onto the rear of the analyzer in the same manner as the battery pack. Align and slide the mounting clips of the blower into the slots of the mounting brackets on the left side of the back panel. Align the captive screws with the mounting holes on the right of the panel and tighten. Connect the power plug to the BLOWER connector on the right side of the rear panel.

SECTION 4

OPERATION

4-1. GENERAL

4-2. This section contains information tor the operation of the Communication System Analyzer.

4-3. CONTROLS, INDICATORS, AND CONNECTORS

4-4. The analyzer controls, indicators, and connectors are shown in Figures 4-1 through 4-3 and listed with their functions in Table 4-1.

Table 4-1. Controls, Indicators, and Connectors

Item	Description	Function
	FRONT PAN	IEL (fig. 4-1)
Keyboard	Twelve-key pushbutton keyboard	Enters variables into memory/enters manual variables/selects variables to be used from the memory.
V	Line cursor key	Moves the cursor down to the next line that may be changed. Preset permanent entries are skipped. Cursor will move down only. When on last line, will return to top line with next entry.
4	Horizontal cursor key	Moves the horizontal cursor left to the next entry position that may be changed. When in the last left position, the cursor will move to the far right with the next entry.
0 through 9	Numerical keys	Used to select from the memory a stored value to be used, or to enter directly a value to be used.
IntensityFocus	Stacked concentric potentiometers • Intensity - center (small) knob • Focus - outside	Controls the intensity of the scope presentation.
	(large) knob	
Dispr/Sweep control	Potentiometer	Controls the frequency span (1-10 MHz) displayed on the CRT when unit is used as a spectrum analyzer. Provides sweep width control when either sweep function (SWP 0.01-1 MHz or SWP 1-10 MHz) is selected.



Figure 4-1. Controls, Indicators, and Connectors, Front Panel



Figure 4-2. Controls, Indicators, and Connectors, Left Side Panel



Figure 4-3. Controls, Indicators, and Connectors, Rear Panel

Item	Description	Function
RF Scan (Hz/Sec) switch	Eleven position switch	Allows automatic scan of the generated or the monitored frequency. The switch setting indicates rate of frequency change. The rate is 5 steps per second, with frequency steps of 100 Hz, 1 kHz, 10 kHz, 100 kHz and 1 MHz.
POWER switch	Three-position toggle	a. Energizes all circuitry in the On position.
	switch.	 At Standby position, removes DC from all circuitry except the frequency standard and battery charger.
		c. At Off, only the battery charging circuitry is operative if an ac power source is being used.
Batt indicator	LED (red)	Illuminates when equipment is using DC power.
AC indicator	LED (red)	Illuminates when equipment is connected to an ac power source. Position of POWER switch has no effect on indicator. Equipment automatically switches to ac power source when connected to ac line voltage.
Oven Ready indicator	LED (red)	Illuminates when optional frequency standard oven has stabilized. Continuously illuminated with the TCXO frequency standard.
DISPLAY indicators	Twelve LEDs (red)	 Illuminate one at a time to indicate the function or type of operation the equipment is performing and the information displayed on the CRT. a. Gen/Mon Mtr — In the generate mode the center frequency, output power, and modulation depth of the RF output is displayed. In the monitor mode the center frequency, input power, frequency error, and modulation depth of the received carrier is displayed. b. Modulation — The modulation audio in the generate mode or the demodulated audio in the monitor mode is displayed. c. Spect Analyzer — The spectrum analyzer mode is enabled. The RF spectrum and the operating center frequency is displayed.

Iter	m	Description	Function
FUNCTION	switch	Three-position toggle switch	 d. Duplex Gen — The duplex generate and monitor frequencies are displayed. The depth of modulation on the generator output or on the received carrier is indicated for the generate and monitor modes respectively. For this display, the function switch only selects which modulation reading is displayed. e. RF Memory — The nine stored RF frequencies or DPL codes with their corresponding PL and the current frequency in use are displayed. f. Tone Memory — The user selectable parameters for the code synthesizer are displayed. f. Tone Memory — The user selectable parameters for the code synthesizer are displayed. These include the tone A and B frequencies, the signaling sequence, and the programming for each of the eight sequences available. g. Freq Counter — The frequency of the signal input to the front panel frequency counter jack is displayed. h. DVM — The AC or DC level of the signal at the front panel DVM jack is displayed. The AC or DC mode is selected with the display cursor and the keyboard. The battery voltage is also displayed. i. Ext Wattmeter — The external wattmeter element selected and the forward and reflected power being passed thru that element are displayed. The element select is changed by entering the appropriate range number with the keyboard. j. IF — The 455 kHz IF signal from the monitor ' receiver is displayed. k. Scope AC — The voltage waveform applied to the front panel vertical input is displayed. The vertical input is AC coupled. I. Scope DC — The voltage waveform applied to the front panel vertical input is displayed. The vertical input is DC coupled.
			 Gen - equipment generates and outputs an RF signal.

Item	Description	Function
		b. Pwr Mon - equipment monitors input signals with the input terminated into the internal power meter. This position must be used for inputs of 0.2 watts and greater.
		c. Monitor - equipment monitors input signals with the input terminated into the receive mixer. This position is used for "off the air" monitoring.
FUNCTION indicators	Six LEDs (red)	Indicates the mode or type of signal the equip- ment is set up to monitor or generate:
		 a. FM - equipment generates or monitors frequency modulated signals.
		 b. CW - equipment generates an unmodulated RF signal. Monitor CW provides frequency error measurement only.
		 AM - equipment generates or monitors amplitude modulated signals.
		d. SSB/DSBSC - equipment generates a double sideband suppressed carrier signal. NOTE: The level of the DSBSC signal generated is not calibrated, it is for use in relative measurements only. Monitor SSB mode receives SSB signals with the use of the BFO.
		e. SWP 1-10 MHz - equipment generates a swept RF signal having a sweep width of 1 to 10 MHz, controlled by the Dispr/Sweep control. Selection of Monitor Sweep has no effect, equipment remains in generate mode.
		 f. SWP 0.01-1 MHz - equipment performs as in e. above except the sweep width limits are 0.01 MHz to 1 MHz.
MODULATION SWITCH	Three position' switch	Controls the Code Synthesizer modulation source. Code Synthesizer mode is shown by the LEDs.
		a. Cont - Continuous modulation signal output.

Item	Description	Function
	FRONT PANEL	(fig. 4-1) (Cont)
		b. Off - Turns off signal. When the mode is DPL or DPL Inv, returning the switch to Off from Cont produces a 133 Hz tone burst for a 120 ms duration.
		c. Burst - For PL, tone A, and tone B modes the output is present for as long as the switch Is held in the burst position. For the A/B mode the burst position causes a single signaling sequence to be output. For the DPL and DPL Inv modes the Burst position causes a 133 Hz tone to be output. For the Tone Remote mode either the Burst or the Cont position causes a tone remote access sequence to be output. The access sequence leaves tone A at a low level for transmit-type commands until the switch is returned to the Off position. This switch is spring loaded to return to the Off position from the Burst position.
CODE SYNTH Mode indicators	Six LEDs (red)	 When illuminated, indicates the selected mode of the Code Synthesizer. a. PL/DPL Indicator PL - Selected Private Line frequency output to 1 kHz DPL - Selected Digital Private Line code output Maximum code number is 777. b. PL/DPL Inv indicator PL - Same as above DPL - Inverted output of selected Digital Private Line code. Maximum code number is 777. The Private Line frequency or the Digital Private Line code is selected from the RF memory display or entered from the keyboard on the Gen Mon Mtr display. c. Tone A indicator Indicates Tone A selected for output d. Tone B indicator Indicates Tone B selected for output

Item	Description	Function
		 e. A/B indicator Indicates Tone A/Tone B signaling sequence will be output. See Tone Memory Table example, figure 4-9. f. Tone Remote indicator Indicates access sequence for Motorola Repeater will be output. Tone A and B frequencies are entered from the keyboard on the Tone Memory Display.
DISPLAY select switches	Two-pushbutton switches	 Selects the function to be displayed by the equipment, as indicated by the DISPLAY LEDs, a. ^ - moves the selection up one step at a time b. V - moves the selection down one step at a time
FUNCTION select switches	Two-pushbutton switches	Selects the type or mode of signal the equip- ment will generate or monitor as indicated by the FUNCTION LEDs. Operation is the same as for the DISPLAY select switches.
Code Synth Mode select switches	Two-pushbutton switches	Selects the Code Synthesizer output mode as indicated by the CODE SYNTH MODE LEDs. Operation is the same as for the DISPLAY select switches.
Code Synth Lvl control	Potentiometer	Controls the level of Code Synthesizer for modulation or MOD Output.
Ext Level control	Potentiometer/switch	Controls modulation level of external input (microphone and other external generators). Switch at full counterclockwise position disables external modulation inputs.
Mic connector	4-pin connector	Microphone input. Provides microphone bias and PUSH TO TALK (GENERATE) connection to equipment.
Ext Mod In connector	BNC connector	External modulation signal input.
1 kHz Level control	Potentiometer/switch	Internal 1 kHz tone modulation level control. Switch at full counterclockwise position disables 1 kHz modulation tone.

Item	Description	Function
Mod Out connector	BNC connector	Output connector for all modulation signals (all signals combined).
Volume control	Potentiometer	Controls speaker output level.
BW switch	Two-position switch	In either Pwr Mon or Monitor modes selects IF bandwidth. NB is ±6 kHz mod acceptance bandwidth. WB is ±100 kHz mod acceptance bandwidth. In Gen FM mode selects modulation range. 0-25 kHz dev in NB mode or 0-100 kHz dev in WB mode.
BFO control	Potentiometer/switch	BFO on/off and beat frequency control for sideband reception. Full Counterclockwise position is off. NOTE: To minimize interference the BFO should be turned off when not in use.
Sig Lvl/Zero Beat indicator	LED (red)	Flashes at a rate equal to the difference between the received carrier frequency and the programmed frequency. Also is used as a squelch indicator.
Squelch control	Potentiometer	Adjusts squelch threshold level, full counter- clockwise position disables squelch. NOTE: Monitor sensitivity is greatly decreased (for high-level use) as the control is increased clockwise beyond the quieting point.
Image/Dplx switch	Two-position switch	In duplex generation mode, controls the duplex frequency output for above (High) or below (Low) the receive programmed frequency. In the monitor mode it selects the frequency of the local oscillator injection above or below the programmed monitor frequency to remove image interference.
Demod Out connector	BNC connector	Receiver audio output.
Oscilloscope Horiz switch	Seven-position rotary switch	When in the oscilloscope mode, selects the horizontal sweep rate or selects the external horizontal input.

Item Description Function Horiz Vernier control Potentiometer Horizontal sweep rate Vernier or external horizontal input gain Vernier. Calibrated position Is fully clockwise. Ext Horiz BNC connector Allows external horizontal inputs for oscilloscope. Tria Level Stacked concentric Selects oscilloscope trigger level and trigger potentiometer and mode. Center knob selects the level of triager. switch Outside (largest) knob controls the trigger mode. In Auto position, continuous sweep with no vertical input signal, syncs on vertical input. Normal position, no sweep unless vertical input is present, syncs on vertical input. Stacked concentric Position controls Controls the position of the CRT display, when controlled potentiometer in the oscilloscope mode. Vert Center (small) control Controls the vertical position of the CRT knob display Horiz Outside (large) control Controls the horizontal position of the CRT knob display Four-position rotary Vert switch Oscilloscope operation uses values marked to switch the right of the switch, indicating volts per division on the CRT. Values marked to the left of the switch are used during modulation display mode, indicating range for calibrated FM deviation. NOTE: Frequency Counter sensitivity is also controlled by this switch. Vernier gain control for vertical inputs to the Vert Vernier control Potentiometer CRT when in the oscilloscope mode. Fully clockwise is the calibrated position. Vert/Sinad/DVM/ BNC connector Signal input to the equipment for the following Counter In connector operations: a. External vertical for oscilloscope operation b. SINAD Meter c. Frequency Counter

Table 4-1. Controls, Indicators, and Connectors (Cont)

d. Digital Voltmeter

ltem	Description	Function
Type N connector	RF In/out connector	RF input in the power monitor or monitor mode, RF output in the generate mode.
Potentiometer	RF Level Variable control	Vernier control of RF output level. Exceeding the AM limit marking in AM generation mode may result in a distorted output.
14-position ganged atten and switch -	RF Level Step switch	Ten dB per step control of RF output level in generate mode. Also serves as RF input level step attenuator in monitor and spectrum analyzer modes.
Ext Wattmeter	Connector	Allows input from Motorola ST-1200 series in- line wattmeter elements for measurement and CRT display of forward and reflected trans- mitted power.
Freq Set controls	Stacked concentric potentiometers	Controls the duplex generator output frequency in the Duplex Generation mode.
Coarse	Inside (small) control	Coarse frequency control.
• Fine	Outside (large) control knob	Fine frequency control.
Frequency offset control (0-10 MHz/Off /45 MHz)	Three-position switch	Selects the offset of the transmitted frequency from the selected receive frequency (Image/ Dpix switch determines side of selected frequency the offset will be). 0-10 MHz position allows frequency offset to be varied between 0-10 MHz. In the 45 MHz position the offset Is variable over a small range around 45 MHz with the use of the Fine frequency control.
Output connector	BNC connector	Output connector for duplex generator output.
SIDE PANEL (fig. 4-2)		
Frequency Standard control	Potentiometer	Allows calibration of the time base frequency (freq std)
REAR PANEL (fig. 4-3)		
BATT 5A	Line fuseholder (5 amp)	Battery charger output line fuseholder.
DC8A	Line fuseholder (8 amp)	DC Input line fuseholder
Item	Description	Function
-----------------------------	-----------------	---
DC IN power connector	4-pin connector	Connects to DC prime power source
AC power connector	3-pin connector	Connects to AC prime power source. Internally patched to accommodate either 100-110 VAC, 110-130 VAC, 200-220 VAC or 220-260 VAC.
AC 1.5A	Linefuseholder	AC line fuseholder.
10 MHz std IN connector	BNC connector	Provides for external 10 MHz time base input. Equipment automatically switches to external time base with an input at this connector.
10 MHz std OUT connector	BNC connector	Provides an output of the internal or external 10 MHz time base for external use.
488 BUS connector		Placement of I/O connector when IEEE-488 Interface Bus option is provided.
Blower power connector		Placement of Blower power connector, when Blower option is provided.

Table 4-1. Controls. Indicators. and Connectors (Cont)

4-5. OPERATION

4-6. The operator may use the CRT display to become familiar with the functions the Communication System Analyzer is capable of performing. The unit may be preset to any of the functions the unit performs. As a function and its parameters are selected they are displayed on the CRT.

The unit contains a nonvolatile memory that stores frequently used data for fast access, reducing setup time. As a function is selected, if data for that function is stored, the data is displayed on the CRT.

One of the stored parameters may be used or the user may manually select (keyboard entry) the parameters required for the function. Selection of stored data or keyboard entry of data is cursor controlled. As a control is changed the CRT display changes to reflect the new parameter being used or function being performed.

4-7. CALIBRATE. The Communication System Analyzer may be calibrated to WWV or other time/frequency standards (figure 4-4). To calibrate the unit's time base (frequency standard) proceed as follows:

- a. Connect antenna to RF In/Out connector.
- b. Set FUNCTION switch to Monitor and DISPLAY to Gen/Mon Mtr.
- c. Enter frequency of time/frequency standards station directly from keyboard.

- d. Select AM function.
- e. Using a tuning tool, adjust time base frequency calibration control (on left side of housing) until CRT frequency error display indicates less than 5 Hz error. Frequency settability to 0.5 part per million can thus be achieved using a 10 MHz frequency standard station.

NOTE

The time base output is also available on the rear panel for external measurement or laboratory calibration to better than the 0.5 ppm achievable with the above method.

NOTE

An external time base input is also provided on the rear panel.



Figure 4-4. System Analyzer Time Base Calibrate Test Setup and CRT Display

4-8. GENERATOR OPERATION. The system generates RF frequencies for FM, AM, CW, SSB, and DSBSC types of transmission covering a range of 10 kHz to 1000 MHz. To generate a signal the FUNCTION switch is placed in the Gen. position.

NOTE

An RF protection circuit to protect against damage due to inadvertent application of RF power to the unit, when in a generate or sensitive monitor mode, is functional over the full monitor frequency range of the equipment (2 to 1000 MHz).

The type of signal is selected using the FUNCTION select LED indicator column. The unit can deliver an output of up to 1 volt into 50 Ohms. When in the AM generate mode the variable control (located in the RF SECTION on the front panel) should not be set above the AM limit mark. Exceeding this may cause distortion in the output.

The RF protect circuit may trip if generator is run at full power output without having a 50-ohm load connected.

4-9. DUPLEX GENERATION. When operating in the duplex generate mode the offset frequency can be set to either 45 MHz or 0 to 10MHz (adjustable). The Image/Dplx switch sets the offset frequency above (high) or below (low) the monitored frequency. When offset is in the 0 to 10 MHz range, the control range may include a foldback region. If the generator is operated in this foldback area erroneous frequency output indications can be given. Avoid areas where backward indication or a jittering display of the offset frequency are incurred. The following is an example of the duplex generator being used to setup repeater levels.

- a. Connect DUPLEX GEN output to repeater receiver antenna input and repeater transmitter signal sample to RF In/Out connector. The Duplex Gen Output level is fixed at -30 dBm nominal.
- b. Set FUNCTION switch to Gen and DISPLAY to Duplex Gen.
- c. Select Duplex Monitor frequency (repeater transmit frequency) from memory table or enter directly from keyboard.
- d. Set DUPLEX GENERATOR frequency to repeater receiver frequency.
- e. Adjust PL and test tone deviation to desired level on display.
- f. Set FUNCTION switch to Monitor and measure the deviation of the repeated signal.

NOTE

Switch function to power monitor and connect repeater transmitter (under 125 watts) directly to the RF In/Out connector to read power and frequency error, as well.





4-10. FREQUENCY COUNTER. The frequency Counter measures inputs in a range from 10 Hz to 35 MHz. The input to the frequency counter is through theVert/Sinad/DVM/Counterin, BNC connector (located in the OSCILLOSCOPE section of the front panel). The counter sensitivity is controlled by the scope Vert switch. The following shows the minimum sensitivity for each switch setting:

Switch setting	Sensitivity		
0.01	50 mV RMS		
0.1	500 mV RMS		
1.0	5V RMS		
10.0	50V RMS		

The autorange output of the counter is displayed on the CRT to a resolution of 0.1 Hz or 5 digits.

NOTE

Do not connect transmitter directly to the frequency counter input. Instead use the RF In/Out connector and the frequency error meter for transmitter frequency measurements.

4-11. SPECTRUM ANALYZER. Input to the spectrum analyzer is through the RF In/Out connector. Select the spectrum analyzer position on the DISPLAY column. Place the FUNCTION switch in the monitor position. Select the desired width of sweep by the Dispr/Sweep control. The center frequency is selected from the memory or entered directly from the keyboard, it is displayed at the top-right of the CRT. The following is an example of locating the frequency of an incoming signal with the spectrum analyzer.

- a. Connect antenna to RF IN/OUT connector.
- b. Set FUNCTION switch to Mon. and DISPLAY to Sped. Analyzer.
- c. Select center frequency from memory table or enter directly from keyboard.
- d. Adjust Disp/Sweep control for desired spectrum span.
- e. Adjust Step attenuator V required to reduce sensitivity.
- f. To determine whether a given displayed signal is valid or being internally generated, flip the Image/Dplx switch to the opposite position. If signal moves in frequency or disappears, it then/represents an internally generated spurious response or received image.
- g. Use the RF Scan control to move desired signal to center of the screen. If the signal is located to the right of screen center line, move the RF Scan control clockwise into one of five positive stepping modes. If the signal is to the left of screen center line, turn the RF Scan control counter clockwise to one of five negative stepping modes.
- h. Adjust Dispr/sweep.control fully counterclockwise for 1 MHz spectrum span.
- i. Again use RF Scan to recenter signal on screen.
- j. Set DISPLAY to Qen/Mon Mtr.
- k. Now adjust the RF scan control to minimize any existing frequency error between the incoming signal and the Monitor frequency.

The frequency indicated at the top of the screen is now that of the desired incoming signal. It can also be monitored for call signs, etc.

NOTE

The spectrum analyzer is functional but uncalibrated for level measurements in Power Monitor mode for transmitter testing with the built-in 125 watt 50 ohm load. (Observe "RF LOAD OVERTEMP" warning for high power levels or extended periods of use.)



Figure 4-6. Spectrum Test Setup and CRT Display

4-12. MONITOR. The analyzer is capable of monitoring the same frequencies that it generates (para 4-9). Select Gen/Mon Mtr in the DISPLAY column and the modulation type in the FUNCTION column. Set the FUNCTION switch to the Monitor position for small signal samples or off the air monitoring. For high power signal monitoring (0.2w to 125w), set the FUNCTION switch to Pwr Mon.

CAUTION

To prevent undue stress on the protected circuits it is advisable to always switch the system to the power monitor mode before applying power in excess of 200 mw. Additional protection is also obtained by making it a practice not to leave the step attenuator in the 0 dB position.

NOTE

High-powered equipment in the 1-30 MHz range, which has unusually fast carrier rise times, may damage the system analyzer with repeated activation of the protect circuit. Ensure the FUNCTION switch is in the Pwr Mon position (this enables the protect circuit) before RF power is applied to the equipment.

In the monitor mode the CRT displays the type of signal being monitored, the selected frequency, power, error of the received frequency, and the modulation level.

4-13. EXT WATTMETER. When the analyzer DISPLAY is set to the Ext Wattmeter mode and the Motorola RTL-4055A in-line wattmeter adapter (supplied) is connected to the Ext Wattmeter jack the analyzer measures both forward and reflected power. The power rating of the wattmeter elements (Motorola ST-1200 series"), to be used, are displayed on the CRT. The following is an example of a test setup for external wattmeter operation. Figure 4-7 shows the test set connections and CRT display.

- a. Select the EXT Wattmeter function by means of the arrow keys located below the DISPLAY column.
- b. Plug the connector of the RTL-4055A In-Line Wattmeter adaptor into the "Ext-Wattmeter" jack located on the RF SECTION of the front panel.
- c. Using the keyboard; enter the single digit which corresponds to the full scale power rating of the ST-1200 series element you plan to use.
- d. Place the ST-1200 element In the In-Line Wattmeter adaptor and install element/adaptor assembly into transmission line.

NOTE

Arrow on In-Line Wattmeter Adaptor must point in the forward direction of the desired rf power flow through the adaptor.

e. Key transmitter and observe magnitudes of forward and reflected power as displayed simultaneously on the 2 analog meter bars and corresponding digital readouts.



Figure 4-7. Wattmeter Test Setup and CRT Display

4-14. SIMULTANEOUS GENERATE AND MEASUREMENT OPERATIONS. The following test setups and CRT displays are examples of simultaneous generating and measurement operations.

- a. FM Mobile radio setup for receiver sensitivity using Generator and SINAD meter.
 - 1. Connect RF In/Out to mobile radio antenna connector and multipurpose measurement (SINAD) input to receiver audio output.

'Contact your Motorola Parts Source for ordering separately.

- 2. Set FUNCTION switch to Gen. and DISPLAY switch to Gen/Mon Mtr.
- 3. Select frequency from RF memory table or enter directly from keyboard.
- 4. Adjust 1 kHz level for 3.0 kHz deviation and RF level for 12 dB SINAD indication. (The mobile radio audio output may be set to the desired level using the DVM AC mode.)





Figure 4-8. Test Setup for FM Receiver Sensitivity Using Generator and SINAD Meter with CRT Display

- b. Test pager decode and alert function, and demonstrate simultaneous modulation.
 - 1. Set FUNCTION switch to Gen and DISPLAY to Tone Mem.
 - 2. Select pager frequency from RF memory table or enter directly from keyboard.
 - 3. Enter pager tone code frequencies and select desired time sequence in memory table.
 - 4. Activate and adjust Code Synth. Lvl. for 3.3 kHz deviation on Gen/Mon Mtr. display. (5 kHz system)

NOTE

Timing sequences 1 through 4 are preset and can not be changed. Timing sequences 5 through 8 are keyboard programmable for testing other pager types, upper and lower timing limits, or future schemes.



Figure 4-9. Test Setup for Pager and Alert Functions with CRT Display

- c. Troubleshooting Receiver audio stages using "DVM and Signal Generate" function simultaneously.
 - 1. Select the DVM function by means of the arrow keys located below the DISPLAY column.
 - Using the keyboard "down" arrow position the CRT cursor adjacent to the "DVM Mode" graphics.
 - 3. Enter a "1" via the keyboard to select AC voltage measurement or a "2" for DC voltage measurement selection.

Set up the desired on-channel RF signal to provide an input to the receiver.

Set Function switch to "Gen". Set appropriate RF output level (as indicated on the CRT screen).

Apply test signals from the receiver audio stages to the instrument's "Vert/Sinad DVM/Counter In" input. DC Voltage measurement points are also applied to this same input. The supplied XI test probe may be used.

Refer to the CRT screen for an auto-ranging and analog/digital indication of either DC voltage or AC voltage and corresponding dBm level.

NOTE

The AC DVM indication of dBm is referred to 600 ohms.



Figure 4-10. Test Setup for Using DVM and Signal Generate with CRT Display

SECTIONV

MAINTENANCE

5-1. SERVICE

5-2. The Motorola Test Equipment Repair Center is charged with the service responsibility for all test equipment supplied by the Motorola Communications Group. The center maintains a stock of original equipment replacement parts and a complete library of service information for all Motorola test equipment.

5-3. Most in-warranty repair are performed at the center. Exceptions include repairs on some equipment not manufactured by Motorola which are performed by the original supplier under the direction of the Test Equipment Repair Center. Out-of-warranty service is performed on a time and materials basis at competitive rates and the maximum turn-around goal is less than ten working days. Customer satisfaction is continually surveyed by reply cards returned with repaired instruments.

5-4. The Test Equipment Repair Center also provides a convenient telephone troubleshooting service. Frequently, a user technician can troubleshoot a piece of equipment and isolate defective components under the direction of the Test Equipment Repair Center via telephone. Required replacement parts are then immediately shipped to the user thereby reducing shipping time and servicing costs. For telephone troubleshooting contact the Test Equipment Repair Center toll free at (800) 323-6967.

5-5. All other inquiries and requests for test equipment calibration and repairs should be directed to the Area Parts Office. They will contact the Test Equipment Repair Center, process the necessary paperwork and, if necessary, have the Center contact you to expedite the repair.

5-6. REPLACEMENT PARTS ORDERING

5-7. Motorola maintains a number of parts offices strategically located throughout the United States. These facilities are staffed to process parts orders, identify part numbers, and otherwise assist in the maintenance and repair of Motorola Communications products.

5-8. Orders for all replacement parts should be sent to the nearest area parts and service center listed below. When ordering replacement parts the complete identification number located on the equipment should be included.

5-9. ADDRESSES

5-10. General Offices

MOTOROLA INC. Communications Division Parts Dept. 1313 E. Algonquin Rd., Schaumburg, Illinois 60196 Phone: 312-397-1000 Executive Offices: 1301 E. Algonquin Rd., Schaumburg, Illinois 60196

5-11. U.S. Orders

WESTERN AREA PARTS

1170 Chess Drive, Foster City, San Mateo, California 94404 Phone: 415-349-3111 TWX: 910-375-3877

MID-ATLANTIC AREA PARTS

7230 Parkway Drive Hanover, Maryland 21076 Phone: 301-796-8600 TWX: 710-862-1941

EASTERN AREA PARTS

85 Harristown Road Glen Rock, New Jersey **07452** Phone: 201-447-4000 TWX: 710-988-5602

SOUTHWESTERN AREA PARTS

3320 Belt Line Road Dallas, Texas 75234 Phone: 214-241-2151 TWX: 910-860-5505

GULF STATES AREA PARTS

8550 Katy Freeway Houston, Texas 77024 Phone: 713-932-8955

5-12. Canadian Orders

CANADIAN MOTOROLA ELECTRONICS COMPANY

Parts Department 3125 Steeles Avenue East Willowdale, Ontario Phone: 516-499-1441 TWX: 610-492-2713 Telex: 02-29944LD

5-13. All Countries Except U.S. and Canada

MOTOROLA INC., OR MOTOROLA AMERICAS, INC.

International Parts 1313 E. Algonquin Road, Schaumburg, Illinois 60196 **U.S.A.** Phone: 312-397-1000 TWX: 910-693-1592 or 1599 Telex: 722433 or 722424 Cable: MOTOL

MIDWEST AREA PARTS

1313 E. Algonquin Rd. Schaumburg, III. 60196 Phone: 312-576-7322 TWX: 910-693-0869

EAST CENTRAL AREA PARTS

12995 Snow Road Parma, Ohio 44130 Phone: 216-267-2210 TWX: 810-421-8845

PACIFIC SOUTHWESTERN AREA PARTS

9980 Carroll Canyon Road San Diego, California 92131 Phone: 714-578-2222 TWX: 910-335-1634

SOUTHEASTERN AREA PARTS

5096 Panola Industrial Blvd., Decatur, Georgia 30032 Phone: 504-981-9800 TWX: 810-766-0876

5-14. MAJOR ASSEMBLIES

5-15. The Communication System Analyzer is designed for ease of maintenance. Most of the circuitry is on seven plug-in circuit boards. A list of all subassemblies is given in table 5-1. The assembly locations are shown in figures 5-1 and 5-2.

Ref. Des.	Item	Part Number As Labeled	Replacement Order Part No.
A1	Low Voltage Power Supply Module	01-P00422N001	RTP-1000A
A2	Scope Amplifier Module	01-P00413N001	RTC-4007A
A3	Scope/DVM Control Module	01-P00409N001	RTC-4008A
A4	Receiver Module	01-P00389N001	RTL-1002A
A5	Synthesizer Module	01-P00385N001	RTC-1001A
A5A*	Digital Synthesizer Card	01-P00358N001	RTC-4009A
A5B*	RF Synthesizer Card	01-P00386N001	RTC-4010A
A6	Audio Synthesizer Module	01-P00426N001	RTC-4011A
A7	Processor Input/Output Module	01-P00405N001	RTC-4012A
A8	IEEE Bus Module (Optional)	01-P00430N001	RTC-4013A
A9	Microprocessor/Character Generator Module	01-P00401N001	RTC-4014A
A10	High Voltage Power Supply Module	01-P00417N001	RTP-1001A
A11	RF Input Module	01-P00394N001	RTC-1002A
A11A1*	Protection/Power Meter Card	01-P00400N001	RTL-4061A^
A11A2*	Converter/Wide Band Amplifier Card	01-P00398N001	RTC-4015A(-
A11A3*	Offset Generator Card	01-P00399N001	RTC-4016A^)
A12	Front Panel Interface Module	01-P00421N001	RTL-4045A
A13	Frequency Standard Module	01-P00368N001	RTL-1004A
A14	Front Panel Assembly	01-P00366N001	01-80304A42
	Motherboard Assembly	01-P00441N001	RTL-4060A

Table 5-1.	List of	Subassemblies
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'These items are solder-in submodules listed for reference purposes. These cards are not normally repaired or replaced individually.



Figure 5-1. Communication System Analyzer, Top View Cover Removed



Figure 5-2. Communication System Analyzer, Bottom View Cover Removed

5-16. THEORY OF OPERATION

5-17. General

5-18. The operation of the Communications System Analyzer can be divided into nine basic functions; Generate, Power Meter, Monitor, Duplex Generator, Code Synthesizer, Frequency Counter, Digital Voltmeter (DVM), Oscilloscope, and Sinad Meter. The general operation of the unit will simultaneously incorporate the basic functions to provide the total capability of the system.

5-19. The following discussion will cover the block diagrams for each of the basic functions pi us a discussion on the processor control of the system. A functional block diagram of the total system is shown in figure 5-3. Only the major signal paths between each of the modules are shown to clarify the total system configuration.

5-20. System Control

5-21. System Control is the primary responsibility of the internal microprocessor. Front panel control and system status inputs to the processor are manipulated by the processor to provide the control for the operating mode. From the front panel the processor monitors the keyboards, the function select switch, the modulation control switch, the RF scan switch, the image switch, the bandwidth switch, the horizontal and vertical range switches, and the step attenuator switch. This information plus internal status information causes the processor to display the appropriate information on the CRT to program the center frequency, to set up the generate or monitor mode, and to make the internal switching arrangements for the selected operating state.

5-22. The interface to and from the microprocessor is via the processor bus. This bus consists of a 16-bit address bus, an 8-bit data bus, and a 7-bit control bus. This bus interfaces the processor to its program memory (ROM), scratch pad memory (RAM), IEEE interface, and the peripheral interface adapters (PIA). The PIA is the mechanism by which the processor interfaces with the system. A PIA consists of a dual 8-bit latch which may be programmed as either an input or output for the microprocessor. System input and control information passes to and from the microprocessor via three system control buses attached to a PIA.

5-23. Each system control bus consists of a 4 bit address bus, a 4 bit data bus, and an enable line. The 4 address bits determine which of 16 possible latches the 4 bits of data is to be sent to or received from. The enable line triggers the actual transfer of data. The three control buses within the system are called the RF control bus and the AF control buses 1 and 2. The RF control bus is as described above while the AF control buses consist of a single 4-bit address and 4-bit data bus and two enable lines. The resulting total input/output capability for the system buses is 16 latches at 4-bits each times 3 buses or 192 bits. A tabulation of buses and the controlling or input function of each bit is shown in table 5-2.

5-24. Systems with the IEEE remote control option interface the IEEE bus to the processor bus through a general purpose interface bus adapter (GPIB) on the IEEE interface module. When enabled all control inputs to the system pass through the IEEE bus and front panel controls are ignored. For more information on IEEE control see section 22.

5-25. Generate Mode

5-26. The generate mode provides a variable level RF output that is phase locked to the internal 10 MHz standard. AM, FM, and Sideband Modulation are possible on the output signal. A block diagram of the generate mode is shown in figure 5-4.



5-7,5-8

Table 5-2. Control Buses and Functions

Data	a RF Bus			AF Bus #1		AF Bus #2			Data				
ADRS	D3	D2	D1	D0	D3	D2	D1	D0	D3	D2	D1	D0	ADBS
0	310-440 PLL	310-440 PLL A0		Audio Synth N0			Display Led's			0			
1	310-440 PLL	N	0		Audio Synth N1			Function Led's			1		
2	310-440 PLL	N	1		Audio Synth N2			Mode Led's			2		
3	60 PLL NO			Audio Synth N3			0.001	Input Sco 0.01	ope Atten 0.1	ୁ 1.0	3		
4	60 PLL	N	1		PL Sel	DPL CLK Enab	DPL Sel	AUDIO Synth N4	O Atten Ext In Int/Ext AC/DC Sel Sel		Ext In AC/DC Sel	4	
5	60 PLL N2			MOD To Spkr Enab	Audio Atten 30 dB	Aduio Atten 20 dB	Audio Atten 10 dB		RF Atten	Position		5	
6	60 PLL N3			DPLX MOD Enab	DSBSC MOD Enab	FM MOD Enab	AM MOD Enab	S	Scan Swite	ch Positio	n	6	
7	7 PLL A1 60 PLL N4						IF Overl'd In	SIG Present In	RF Input <+20 dB In	WB/NB Sw In	7		
8		500-1000 250-500 Out Out Enab Enab		250-500 Out Enab		DVM I Sel	MODE ect	t se Br fundfr	CSSG Cont Sw In	CSSG Burst Sw In	Hi/Lo Image Sw In	Gen Sw In	8
9	WB MOD Enab	(MOD) x (2) Enab	MOD INV/INV Sel	MOD FM/SWP Sel	e service	Pk Det FM MOD Enab	Pk Det AM MOD Enab	Pk Det Demod Enab	Scope 10 V/ DIV	Vertical S 1V-100kH DIV	wi <u>tch P</u> o 0.1V- 10kHz DIV	s In 0.01V- 1kHz DIV	9
A	0.01- 1000 Sel	500-700/ 700-1000 VCO Sel	LOOP INV/INV Sel	MOD Disable		Int DVM x 0.1 Enab	WB/NB Sel	IF/BFO Freq Sel	Mon Sw In	Horiz	Scope Switch P Sw In	os	A
в	3			Horiz Mode	Scope Sel	Vert S Mode	Scope Sel					в	
с	c			Pwr MTR Enab	(Mon + DSB)/ Gen Sel		.01-1 /1-10 Swp Sel		i data A SSC 1		t christ Na christe	с	
D	SSB Demod	FM Demod Enab	AM Demod	Demod To Spkr	Sco SSC3	ope Time	Base C	TL SSC0				D	
E	WB/NB Sel	Demod INV/INV Sel	Alarm Enab	LIN IF/ Log IF Sel	Sc SSC7	ope Time SSC6	Base C SSC5	rL SSC4	DVM AC/DC Freq Cntr Range		E		
F									Ctr/DVM Sel	Counte IF/BFO	er Input S Offset	Sel Ext	F



Figure 5-4. Generate Mode Block Diagram

5-27. The Frequency Standard module (A13) contains a 10 MHz standard oscillator with buffering and switching to provide a 10 MHz signal to the EXTERNAL 10 MHz OUTPUT and to the RF Synthesizer (A5). A provision is made for the application of an EXTERNAL 10 MHz INPUT which causes the internal standard to shutdown and the EXTERNAL 10MHz INPUT to be switched to the EXTERNAL 10 MHz OUT and to the RF Synthesizer.

5-28, The 10 MHz standard input to the RF synthesizer is digitally divided down to provide SYSTEM REFF FREQUENCIES for the frequency counter, the zero beat detector, the second local oscillator in the receiver, and the processor timing reference. Additionally reference frequencies are provided for a fixed 550 MHz locked loop and for a programmable 500 MHz-1000 MHz locked loop. The programming of the 500 MHz-1000 MHz locked loop is provided by the RF CONTROL BUS from the processor. The SELECT SWITCH selects one of three possible output points for the SYNTH RF output signal. The first is from the 500 MHz-1000 MHz loop directly. The second is from a divide by two on the output of the 500 MHz-1000 MHz loop which gives frequencies from 250 MHz to 500 MHz. For outputs below 250 MHz, the output of the 500 MHz-1000 MHz loop is mixed with the fixed 550 MHz signal and the difference signal used for the output. For this output the processor programs the 500 MHz-1000 MHz loop for frequencies between 550.01 MHz and 800 MHz to obtain outputs from 10 kHz to 250 MHz respectively.

5-29. FM and SWEEP Modulation is implemented within the 500 MHz-1000 MHz loop. FM capability is 200 kHz peak which when divided by two gives the 100 kHz peak requirement. Similarly the sweep capability is 10 MHz peak which provides the 5 MHz requirement for the sweep generator and spectrum analyzer requirements.

5-30. The SYNTH RF signal is amplified and leveled in the RF Input module (AH). The signal level at the output of the wideband amp is detected and compared to the AM MOD & DC REF signal from the front panel level control. If there is a difference between the two signal levels, the ALC amp provides an error voltage. The error voltage controls the attenuation of the Voltage Controlled Attenuator (VCA) in the direction that will make the detected RF output equal to the AM MOD & DC REF signal. There are two possible VCA's for the output leveling. The VCA within A11 is used for frequencies from 1 MHz to 1000MHz. For frequencies below 1 MHz, the VCA on A11 is set to minimum attenuation and the VCA on the RF Synthesizer module is used for leveling. Amplitude modulation is incorporated by suming the modulation signal with the DC reference signal to force the leveling loop to vary the output level in proportion to the modulating signal. The signal from the RF level detector (CARRIER + MOD LVL) is used by the processor for the determination of RF output level and the percent AM. The leveled output range of the Wideband Amp is from -3dBm to +13dBm (0.16 to 1.0Vrms).

5-31. The leveled output from the Wideband Amplifier is applied to the Generate/Monitor (T/R) switch. For AM, FM, and CW signals the switch connects the amplifier output to the Step Attenuator. For Double Sideband Suppressed Carrier (DSBSC) the T/R switch is in the "R" position where the amplifier output is connected to the local oscillator port on the receive mixer and the attenuator is connected to the RF port. The DSBSC MOD signal is then used to drive the IF port of the mixer giving a DSBSC signal at the RF port and thus at the Step Attenuator.

5-32. Coarse level control in 10 dB increments is provided by the Step Attenuator. The total range of the attenuator is from 0 dB to 130 dB attenuation. For the basic R2001 A the Step Attenuator is controlled directly by a shaft to the front panel knob. With the IEEE control option the Step Attenuator is electrically programmable and controlled by the processor. The front panel knob in this case is connected only to a rotary switch which directs the processor in setting the attenuation level. Under IEEE control, commands via the IEEE •bus determine the attenuator setting. (See section 22.)

5-33. The RF signal from the Step Attenuator passes through the input protection circuitry to the RF Output jack. A level detector on the RF Output jack monitors the power level at the jack. If power in excess of 200 mW is applied to the Output jack, the protection circuit will activate and switch the RF Output jack to the internal 50 ohm load. This action protects the Wideband Amp and Step Attenuator against burnout. A signal line from the protection network signals the processor that the system is in the protected mode. The processor in turn activates the CRT and alarm warnings.

5-34. Power Meter

5-35. Input power measurements are made with the RF Input terminated into an internal 50 ohm load. This termination is the same one used for the protect mode when in the generate or monitor functions. A block diagram of the power meter is shown in figure 5-5.

5-36. For the power meter mode the processor sets the WATT METER ENABLE line to cause the RF input jack to be switched to the 50 ohm power termination. For modes other than the power meter, an Input Detector on the RF Input jack detects when the input power has exceeded 200 mW and then switches the input to the load.

5-37. The switch is a single pole double throw configuration so that when switched to the RF load the path to the Step Attenuator and Converter is open circuited. However, leakage across the open switch provides sufficient signal for operation of the normal monitor functions.



Figure 5-5. Power Meter Block Diagram

5-38. A sample of the RF voltage being applied to the RF Load is detected by the Power Detector to give a DC output proportional to the peak RF voltage. The amplifier following the detector buffers and gain adjusts the detected voltage to provide the RF INPUT POWER signal to the processor. The processor then determines and displays the RF input power.

5-39. A Temperature Sensor located near the flange of the RF Load alerts the processor when the load temperature exceeds 80° C. The processor reacts to the OVER TEMPERATUREsignal by displaying a warning message on the CRT and by sounding the audible alarm.

5-40. Monitor Mode

5-41. The monitor mode allows RF signals from an antenna or from a transmitter directly to be checked for frequency error, modulation level, and spectral content. AM, FM, and sideband modulations can be accommodated with this system. A block diagram of the monitor mode is shown in figure 5-6.

5-42. The RF signal to be monitored is applied to the RF I nput jack on the RF Input module (AH). If the input level is less than 200 mW the input signal passes directly through the Input Protection circuitry to the Step Attenuator. For input levels greater than 200 mW the protection circuit switches the input to the internal load and signal the operator to switch to the Power Monitor mode. In this case, RF leakage (paragraph 5-37) through the protection circuits provides the input signal to the Step Attenuator.

5-43. For the monitor mode the T/R switch is set so that the RF input from the Step Attenuator is connected to the RF port on the receive mixer. The output from the wideband amp is switched to the local oscillator porton the receive mixer. The processor programs the RF Synthesizer for an output frequency that is offset from the frequency to be monitored by 10.7 MHz. The offset may be above or below the center frequency as selected by the front panel image switch.



Figure 5-6. Monitor Mode Block Diagram

5-44. The 10.7 MHz difference signal at the IF port of the receive mixer is amplified and selected by the first IF Amplifier and Filter. The Amplifier provides sufficient gain so that the overall gain of the RF Input module is 10 ± 2 dB. The IF filter provides a modulation acceptance bandwidth of +100 kHz. The filter output is the 10.7 MHz IF signal to the Receiver module (A4).

5-45. A second mixer in the receiver module down converts the 10.7MHz IF signal to 455 kHz by mixing the input signal with a 10.245 MHz Second Local Oscillator. The Second Local Oscillator is phase locked to the 10 MHz system standard so that its frequency is as accurate as the standard. The phase locked loop for the Second Local Oscillator is split between two modules. A 10.245 MHz SAMPLE signal is compared with the REFERENCE FREQUENCIES from the RF Synthesizer on the Processor I/O module (A7). The comparison provides aTRACKING **VOLTAGEerror signal to the 10.245 MHz oscillator which corrects its** frequency to hold it in lock.

5-46. Immediately following the second mixer is the IF filter. The IFfilter is selectable between a narrowband (\pm 6 kHz mod acceptance) and a wideband (\pm 100 kHz mod acceptance) bandwidth. The bandwidth is under the control of the processor and is selected by the bandwidth switch on the front panel.

5-47. The output signal from the IF filter has two possible paths. The path to the Log Amplifier and Detector provides the spectrum analyzer capability. The other path is the linear IF Amplifier for AM, FM, and SSB demodulation. The output level of the Amplifier is detected to give amplitude modulation and to provide the AGC control on the IF amplifier. The IF signal is applied to the FM Demodulator and issenttotheScope/DVM Control module (A3) for SSB demodulation and for frequency error determination.

5-48. Demodulated audio from the selected demodulator is routed to the Audio Filter by the Select Switch under processor control. The Audio Filter provides post detection filtering for both wide and narrow band modes. The output of the Audio Filter is three signal lines. The Demod Calibration Audio line provides the calibrated audio levels for modulation level determination. A Demod Audio output provides a level adjusted signal to the front panel Demod Out jack. Speaker audio is level adjusted by the front panel volume control and then amplified by the Audio Amplifier on the Receiver module.

5-49. The Audio Amplifier sums the audio from the demodulator with the Alarm audio. The Audio Amplifier provides a 0.5 watt output capability to the system's internal speaker. The Alarm generator is under the control of the system processor.

5-50. SSB demodulation is implemented on the Scope/DVM Control module by multiplying the 455 kHz IF signal from the Receiver with a signal from the Beat Frequency Oscillator (BFO). The BFO is controlled from the front panel and typically has a frequency range of 455 ±3 kHz. The BFO signal is switched with the output of the 455 kHz IF Phase Locked Loop (PLL) to the frequency counter for frequency error determination. The 455 kHz PLL filters and shapes the IF signal to make it suitable for frequency counting.

5-51. When in the spectrum analyzer mode the linear IF Amplifier is shut down and the Log Amplifier is activated. The output of the Log Amplifier and Detector is a DC voltage that is proportional to the log of the 10.7 MHz IF input level. The log circuit has a dynamic range of approximately 80 dB, covering input levels from-100 dBm to -20 dBm. The SPECTRUM ANALYZER signal from the Log Amplifier is the vertical input to the scope for the spectrum analyzer display.

5-52. Duplex Generator

5-53. Simultaneous generate and monitor functions are available with the use of the Duplex Generator. The frequency spread between generate and monitor frequencies is limited to a range of 0 to 10 MHz and a fixed frequency of 45 MHz. A block diagram of the Duplex Generator function is shown in figure 5-7.

5-54. The Duplex Output signal is generated by mixing the local oscillator signal for the first receive mixer with a signal from the Offset Oscillator. The Offset Oscillator is at the frequency equal to the desired spread between generate and monitor frequencies less the 10.7 MHz IF offset. The monitor function is unaffected by the duplex mode and operates as described under paragraph 5-40.

5-55. Frequency modulation of the duplex output is obtained by modulating the Offset Oscillator frequency via the OFFSET MOD signal line. Control of the Offset Oscillator is directly from the front panel of the system. A OFFSET FREQUENCY output from the oscillator provides an input to the frequency counter for the determination of the duplex frequency.



Figure 5-7. Duplex Generator Block Diagram

5-56. Code Synthesizer

5-57. Three simultaneous modulation sources are possible with the internal Code Synthesizer, A private line (PL) or Digital Private Line (DPL) source, a fixed 1 kHz source, and external modulation sources are individually level controllable and sumed together to give the composite modulation audio. The Code Synthesizer provides the modulation source for the system in the generate mode and can be used as an audio frequency source when in the monitor mode. For the IEEE option a provision is made to allow processor control of the modulation levels. A block diagram of the Code Synthesizer is shown in figure 5-8.

5-58. The PL signaling sequence generator is an Audio Synthesizer with an output frequency range from 5 Hz to 10 kHz in 0.1 Hz steps. The frequency is programmed by the processor in response to the operator's request from the keyboard through the CRT display. The Programmable Attenuator following the synthesizer provides 10 dB and 30 dB attenuation levels for the tone remote access sequence.

5-59. DPL Code words are generated by the processor in response to the code entered by the operator. The 23-bit DPL word is stored in the DPL Generator and continuously output when selected. Either PL or DPL signals are switched to the Code Synthesizer Level control on the front panel.

5-60. A 1 kHz reference signal from the RF Synthesizer is bandpass filtered to provide a low distortion 1 kHz sinewave to the front panel 1 kHz Level Control.

5-61. Two sources of external modulation are possible. A standard Motorola microphone interface jack on the front panel and a BNC front panel jack are provided. The microphone input is connected to an IDC circuit .for peak limiting. The composite of the two external modulation sources is the signal to the External Level control on the front panel.

5-62. Systems without the IEEE option will have the wipers of the level control pots jumpered to their respective inputs to the summation amp on the Audio Synthesizer module (A6). Those systems with the IEEE option will select on the IEEE Interface module (A8) either the tops of the level controls or their wipers to the Programmable Attenuators for remote or local control respectively. While in the IEEE Control mode the processor controlled Programmable Attenuator on the IEEE module provides the modulation level control. For the local mode the attenuators are programmed for zero attenuation so that the wipers of the level controls set the modulation levels directly.



Figure 5-8. Code Synthesizer Block Diagram

5-63. The three modulation sources are sumed together on the Audio Synthesizer module after the level controls. The composite modulation signal is then switched to the appropriate modulator and applied to the modulation determination circuitry (MOD CAL AUDIO), the audio amplifier (SPKR AUDIO), and the Modulation Output jack (MOD OUT) on the front panel. The signal to the front panel jack is buffered by a Driver Amplifier to provide a low driving source impedance.

5-64. The AM modulation signal at the output of the Select Switch is sumed with a +5 volt signal. This combination provides a DC level to control the average output power of the wideband amp in the RF Input module, and a superimposed modulation signal to give an AM output. The RF Level control on the front panel for local control or the Programmable Attenuator on the IEEE module provide local or remote RF level control by simultaneously attenuating the DC level and the modulating signal. The resulting signal is the AM MOD & DC REFERENCE signal to the RF Input module.

5-65. Frequency Counter

5-66. Three possible signal sources are made available to the frequency counter for frequency determination. Two of the inputs are from internal system points for the determinations of the offset frequency (OFFSET), and the monitored carrier error frequency (IF/BFO). The third input is the external input (FREQ CNTR INPUT) on the front panel. A block diagram of the frequency counter function is shown in figure 5-9.



5-67. The external input signal is routed to the Front Panel Interface module (A12). A range Attenuator on the Interface module provides variable sensitivity settings according to the vertical range switch setting on the front panel. An Amplifier following the range Attenuator amplifies and limits the signal amplitude for the frequency counter input.

5-68. A Select Switch on the Processor I/O module (A7) routes the desired signal to the Frequency Counter circuitry. The signal selected is controlled by the processor and is determined by the operating mode of the system.

5-69. A 16-bit gated accumulator is used to determine the input frequency. Gate times from 1 msec to 10 sec are automatically selected by the processor to give the maximum possible resolution. The gate times are derived from the RF Synthesizer REFERENCE FREQUENCIES and thus are as accurate as the system time base.

5-70. The 16-bit Frequency Counter output is transferred directly to the processor bus through a Peripheral Interface Adapter (PIA). The processor in turn adjusts the data for the gate time used and then processes the information to obtain the required frequency display.

5-71. Digital Voltmeter (DVM)

5-72. The processor through the DVM circuitry has access to voltage information ata large number of points throughout the system. From this information the processor is able to determine and display parameters such • as; output power level, modulation level, input power level and the like. In addition an external voltage applied to the DVM input jack on the front panel can be measured and displayed for external voltage measurements. A block diagram of the DVM function is shown in figure 5-10.



Figure 5-10. Digital Voltmeter (DVM) Block Diagram

5-73. Switching for the DVM input is contained on the Scope/DVM Control module (A3). One of ten internal measurement points may be selected for measurement. The switching action is controlled by the processor and is performed as required to obtain the information on the CRT. To keep the CRT information current, each of the required measurements are made in sequence at an approximate rate of thirty per second. The net effect is a multiplexing of the voltage information to the processor.

.5-74. Two modulation signals (MOD CAL AUDIO and CARRIER + MOD LVL) and a demodulated signal (DEMOD CAL AUDIO) are made available to the peak detectors. Positive and negative peak determination of the selected signal enables the processor to determine the level of modulation.

5-75. A Lowpass Filter (LPFL) removes the DC component from the CARRIER + MOD LVL signal so that the generate RF output level can be determined. Refer to paragraph 5-30.

5-76. The RF INPUT POWER and OVERTEMP signal lines from the RF Input module provide the processor inputs for the internal wattmeter. (Paragraph 5-38). External wattmeter element inputs (EXT FWD PWR and EXT RFL PWR) from the front panel jack provide the information for the external wattmeter display.

5-77. A signal line from the DC input jack on the rear panel (BATT VOLT) is brought to the processor for battery voltage determination. The voltage is attenuated by a factor of 10 to stay with the 10 volt maximum input to the select switch. The processor uses the battery voltage measurement to warn the operator when the battery is near it's discharged state.

5-78. Sinad determination utilizes the two remaining inputs to the select switch. For a discussion on the sinad function see para 5-96.

5-79. The selected internal measurement signal is then passed through a range attenuator. Signals from the Select Switch have a 0 to +10 volt range while the DVM input has a 1 volt maximum input requirement. The processor automatically determines and sets the correct range on the attenuator so that the input level to the DVM is maintained at less than 1 volt. For levels from the select switch less than 1 volt, the attenuator is ranged to the unity gain position for maximum measurement resolution.

5-80. A select switch following the internal range attenuator gates either the internal measurement points or the external input to the DVM circuitry. External DVM inputs are applied through the front panel jack to the Front Panel Interface module (A12). On the Interface module, a processor controlled switch selects between a direct coupled or a capacitively coupled path for DC and AC measurements respectively. A range attenuator follows the AC/DC switch to provide processor controlled autoranging over a four decade range. Input voltages from 1 millivolt to 300 volt can be handled through the DVM Input.

5-81. For DC measurements a lowpass filter (LPFL) removes AC signal components. The filter provides approximately 25 dB rejection at 50 Hz so that accurate DC measurements can be made with superimposed AC line ripple. When the AC measurement mode is selected the LPFL is reprogrammed for less than 0.5 dB rejection at 10 kHz.

5-82. Positive and negative DVM input levels are full-wave rectified by the Absolute Value circuit on the Processor I/O module (A7). The outputs of the Absolute Value circuit provide a positive voltage level equal to the magnitude of the input voltage and a SIGN BIT indicating the polarity of the input signal. For AC measurements a lowpass filter is switched into the Absolute Value circuit to filter the rectified AC input for it's average level. The processor then multiplies by 1.11 to obtain the RMS value.

5-83. An analog to digital converter (A/D) converts the magnitude voltage level into a 10-bit digital word. This digital word when combined with the SIGN BIT is a binary representation of the input voltage level. The peripheral interface adapter transfers the information to the processor.

5-84. Oscilloscope

5-85. Three basic functions are provided for by the system oscilloscope. The alphanumeric and modulation displays provide operating mode and control information for the system. The external oscilloscope feature augments the total system as a general purpose test instrument. A block diagram of the oscilloscope function is shown in figure 5-11.

5-86. Drive signals for the CRT are provided by circuits on the Scope Amplifier module (A2). Horizontal and vertical signals are amplified by their respective amplifiers from 0.5 volt/division input levels to the levels required on the deflection plates. A Z-Axis Modulator circuit controls the cathode to grid bias voltage on the **CRT to effect intensity control**.



Figure 5-11. Oscilloscope Block Diagram

5-87. The horizontal amplifier input is selected between external and internal scope functions. External functions, Time base Generator or external horizontal input, are switched to a sumation amp where the HORIZONTAL POSITION signal from the front panel is added. The resulting DC offset positions the display horizontally on the CRT.

5-88. Six decade sweep ranges from 1u sec to 100 msec per division are provided by the Time base Generator. Control of the Time base Generator is from the front panel horizontal switch through the processor.

5-89. Front panel external horizontal inputs are applied to the top of the horizontal vernier gain potentiometer. The wiper of the gain potentiometer is the EXTERNAL HORIZONTAL input signal to the

preamp on the Front Panel Interface module (A12). The preampprovittes (he required horizontal input sensitivity and buffers the signal to the select switch on the Scope Amplifier module.

5-90. Internal horizontal signals, Sweep Generator and Character Sweep outputs, are selected on the Scope/DVM Control module (A3). The Sweep Generator provides a sawtooth waveform to the RF Synthesizer module for the sweep generator and spectrum analyzer functions. The sweep signal to the CRT horizontal input causes the scope sweep to be synchronous with the synthesizer sweep for the spectrum and swept filter response displays.

5-91. The Horizontal Character Sweep generator output is a sawtooth waveform that provides the horizontal sweep for the raster scan character display.

5-92. One of four possible vertical signal sources are switched to the Vertical Amplifier input by a Select Switch on the Scope/DVM Control module. The 455 kHz IF and SPECTRUM ANALYZER signals from the Receiver Module provide the IF envelope and spectrum analyzer displays respectively. The Vertical Character Sweep generator gives the vertical sweep for the raster scan character display. The remaining input is the path for external vertical or modulation scope vertical inputs from the Front Panel Interface module.

5-93. A vertical preamplifier on the Interface module gives a vertical sensitivity of 10 millivolt per division and provides positioning and vernier gain capability for its input. The amplifier is proceeded by a four decade range attenuator which is controlled from the front panel vertical switch through the processor. The attenuator provides external vertical input sensitivities from 0.01 to 1.0 volt per division and modulation scope sensitivities from 0.25 to 25 kHz per division.

5-94. A Select Switch ahead of the Attenuator selects between the external vertical input or the modulation scope inputs. The External Vertical input path is further selected between AC and DC coupling before becoming the vertical input jack on the front panel. The modulation scope signal path is switched to one of three possible sources on the Scope/DVM Control module. Demodulation signals from the Receiver are selected via the DEMOD CAL AUDIO path, and frequency and amplitude modulation signals via the MOD CAL AUDIO and CARRIER + MOD LVL signal paths respectively. The Audio Synthesizer module provides the MOD CAL AUDIO signal while the RF Input module gives the CARRIER + MOD LVL signal.

5-95. A Z-Axis Select circuit on the Scope/DVM Control module gates either the CHARACTER GEN signal for character displays or the retrace blanking signal from the Time Base Generator for scope displays to the Z-Axis Modulator on the Scope Amplifier module.

5-96. Sinad Meter

5-97. Sinad, which is defined as the ratio of noise plus distortion to signal plus noise plus distortion, is a measurement of the audio quality at a receiver output. Measurement of the Sinad is implemented with a 1 kHz notch filter. For a receiver receiving a 1 kHz tone the audio output is applied to the 1 kHz notch filter. Sinad is then the ratio of the signal power at the output of the notch filter to the signal power at the input of the notch filter. A block diagram of the Sinad Meter is shown in figure 5-12.

5-98. The Sinad Input from the front panel is AC coupled to the range Attenuator on the Front Panel Interface module (A12). Processor control on the Attenuator allows a wide range of input levels to be automatically handled. The output of the Attenuator is routed to the 1 kHz Notch Filter on the Scope/DVM Control module (A3). Detectors, comprised of fullwave rectifiers and filters, on the input and output of the notch filter determine the respective power levels.'The DC outputs of the detectors are read by processor through the DVM. The processor determises the ratio between the two readings and displays the Sinad.



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Figure 5-12. Sinad Meter Block Diagram

5-99. ALIGNMENT PROCEDURE

5-100. Introduction

5-101. This section provides a basic (para 5-105) and an extended (para 5-118) alignment procedure.

The basic procedure requires only the use of a calibrated oscilloscope. It is expected that the basic alignment be performed whenever service work is performed. The extended alignment procedure requires module extenders and a calibrated digital voltmeter in addition to the oscilloscope. The extended procedure should be performed as required after servicing the system. All adjustments not covered in this procedure are to be performed on suitable module test fixtures only.

5-102. Test Equipment Required

5-103. The test equipment or its equivalent listed in table 5-3 is required for the basic procedure. The additional equipment required for the extended procedure is listed in table 5-4.

Description	Model
'Oscilloscope Test Point Shorting Jumper Nonmetallic Alignment Tool	Motorola R1004A

Fable 5-3.	Basic Test	Equipment	Required
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*A R2001A is a suitable substitute

Table 5-4. Extended Test Equipment Required

Description	Model
Oscilloscope	Motorola R1004A
Digital Voltmeter	Motorola R1001A
*RF Signal Generator	Motorola R1201A
•Modulation Meter	Boonton 82AD
Receiver Test Cover	Motorola 15-P01324V001
Extender Card Set	Motorola67-P01322V001

"A R2001A is suitable for use in place of these separate equipments.

5-104. Preparation for Alignment

- 1. All alignments to be performed at normal ambient temperature.
- 2. Remove the top cover of the unit to be aligned.
- 3. Apply power to the unit to be aligned and allow a warmup time of 15 minutes prior to alignment.
- 5-105. Basic Alignment Procedure
- 5-106. CRT Astigmatism and Geometry
 - 1. Select the Monitor Function and the Gen/Mon Mtr Display on the R2001 A. Set the Intensity Control for a medium intense display.
 - 2. While using the Focus Control to maintain a focused display at the center of the CRT, adjust the Astigmatism and Geometry potentiometers (Figure 5-13) for the best focus at the outer edges of the CRT while minimizing the pincushion and barrel distortion of the display. The two adjustments are interactive so that repeated small adjustments alternated between the two potentiometers will be required to obtain the best display.



Figure 5-13. Scope Amplifier Alignment Points

5-107. CRT intensity Bias

1. Select the Scope DC Display and the Ext Horiz. Input mod» Set the Intensity Control fully counter clockwise.

CAUTION

Do not let a dot stay in one place on the CRT screen for more than 30 seconds as a permanent burn in the phosphor will occur.

- 2. Adjust the Intensity Bias potentiometer (Figure 5-13) until a dot appears on the screen. (The Vertical and Horizontal Position Control on the front panel may have to be used to bring the dot on to the screen.) Then back off the Intensity Bias potentiometer until the dot just disappears.
- 5-108. CRT Intensity Balance
 - 1. Select the Scope DC Display and the 1 mSec/Div Horizontal Sweep rate on the R2001A. Set the Horizontal Timebase Veriner to the Cal position and adjust the Intensity Control for a barely visible horizontal line on the CRT.
 - 2. Adjust the Intensity Balance potentiometer (Figure 5-13) for uniform intensity of the horizontal trace from left to right. The Balance potentiometer affects the intensity on the left side of the trace.
- 5-109. CRT Horizontal Centering
 - 1. Select the Gen/Mon Mtr Display on the R2001A. Adjust the Intensity Control for a comfortable viewing brightness.
 - 2. With the Test Point Shorting Jumper connect TP1 of the Scope Amplifier Board (Figure 5-13) to chassis ground.
 - 3. Adjust the Horizontal Position Potentiometer (Figure 5-13) so that the vertical trace on the CRT screen passes through the graticule center point.
 - 4. Remove the jumper from TP1.
- 5-110. CRT Vertical Centering
 - 1. Select the Gen/Mon Mtr Display on the R2001A. Adjust the Intensity Control for comfortable viewing brightness.
 - 2. With the Test Point Shorting Jumper connect TP4 of the Scope Amplifier Board (Figure 5-13) to chassis ground.
 - 3. Adjust the Vertical Position Potentiometer (Figure 5-13) so that the horizontal trace on the CRT screen passes through the graticule center point.
- 5-111. CRT Trace Rotation
 - 1. Select the Gen/Mon Mtr Display on the R2001A. Adjust the Intensity Control for a comfortable viewing brightness.
 - 2. Adjust the Trace Rotation Potentiometer for a properly rotated CRT display.

5-112. CRT Horizontal Gain

- 1. Connect the Mod Out Jack to the Ext Horiz Jack on the R2001A front panel.
- 2. Set the R2001 A for the Generate FM Function and the Scope DC Display. Set the Horiz Control for Ext Horiz input. Turn the Code Synthesizer off/the Ext Level offhand the 1 kHz Level up about half way.
- Connect an oscilloscope with a calibrated vertical input-to TP1 on the Scope Amplifier Board. (Figure 5-13).
- 4. Using the front panel Horizontal Vernier Control adjust for a 3 V p-p amplitude on the sinewave at TP1.
- 5. With 3V p-p at TP1 adjust the Horizontal Gain Potentiometer (Figure 5-13) for a horizontal trace 6 cm long on the CRT. (Use the front panel controls to position the trace at a convenient place near the center of the CRT).
- 5-113. CRT Vertical Gain
 - 1. Connect the Mod Out Jack to the Vert Input Jack on the R2001A front panel.
 - Set the R2001A for the Generate FM Function and the Scope DC Display. Set the Horiz Control for 1 mSec/Div sweep rate and the Horizontal Vernier to the Cal position. Set the Vert Control for 1 V/Div input sensitivity and the Vertical Vernier to the Cal position.
 - 3. Turn the Code Synthesizer off, the Ext Level off and the 1 kHz Level up about half way.
 - Connect an oscilloscope with a calibrated vertical input to TP4 on the Scope Amplifier Board. (Figure 5-13).
 - 5. Using the front panel 1 kHz Level Control adjust for a 3V p-p amplitude on the sinewave at TP4.
 - 6. With 3V p-p at TP4 adjust the Vertical Gain Potentiometer (Figure 5-13) for a 6 cm p-p sinewave on the CRT. (use the front panel Position Controls to center the waveform on the CRT).
- 5-114. Vertical Input Gain
 - 1. Set the R2001 A for the Generate FM Function and the Scope DC Display. Set the Horiz Control for 1 m Sec/Div sweep rate and the Horizontal Vernier to the Cal position. Set the Vert Control for 1 V/Div input sensitivity and the Vertical Vernier to the Cal position.
 - 2. Connect an oscilloscope with a calibrated vertical input to the Mod Out Jack on the front panel.
 - 3. Turn the Code Synthesizer off, the Ext Level off and adjust the 1 kHz Level Control for a 6 V p-p sinewave on the attached oscilloscope.
 - 4. Disconnect the oscilloscope from the Mod Out Jack and connect the Mod Out Jack to the Vert Input Jack on the R2001A.
 - 5. Adjust the Input Vertical Gain Potentiometer on the Front Panel Interface Board (Figure 5-14) for a 6 cm p-p sinewave on the CRT. (Use the front panel Position Controls to center the waveform on the CRT.)



Figure 5-14. Front Panel Interface Alignment Points

5-115. DVM Zero

- 1. Select the DVM Display and the DC Mode on the R2001A.
- 2. Short the center conductor of the DVM Input Jack to ground.
- Adjust the DVM Zero (Coarse) and the DVM Zero (Fine) Potentiometers on the Front Panel Interface Board (Figure 5-14) for a zero reading on the DVM Display.
- 5-116. Spectrum Analyzer Centering
 - 1. Select the Spect Analyzer Display on the R2001 A. Set the Dispersion Control on the front panel to the 1 MHz position, (full counter clockwise) Set the center frequency of the analyzer to 10.0 MHz.
 - 2. Connect the 10 MHz Output on the rear panel to the RF Input on the front panel. Set the RF Step Attenuator to obtain a convenient spectral display.
 - Adjust the Spectrum Analyzer Centering Potentiometer on the Scope/DVM Control Board (Figure 5-15) so that the spectral line on the CRT is centered about the center graticule line.
- 5-117. Horizontal Time Base
 - 1. Select the Tone Memory Display and the Generate FM Function on the R2001 A. Program tone A for 20.0 Hz and Tone B for 2000.0 Hz.
 - Select the Modulation Display. Set the Oscilloscope Controls for 2.5 kHz/Div vertical range, Auto Trigger, and 10 mSec/Div horizontal sweep range. Set the Horizontal and Vertical Vernier Controls to their Cal positions.
 - Set the Code Synthesizer for Continuous, Tone A, and turn up the Code Synth Level to obtain a nearly full scale sinusoidal waveform on the CRT. Turn the Ext Level and the 1 kHz Level Controls to the off position.



Figure 5-15. Scope/DVM Control Alignment Points

Adjust the Coarse Time Base Calibration Potentiometer on the Scope Amplifier Board (Figure 5-16) so that one cycle of the displayed waveform occurs in 5 cm along the horizontal axis. Use the Vertical and Horizontal Position controls to center and to move the waveform so that the 5 cm are measured in the middle of the screen to avoid nonlinearities near the edge of the CRT.



Figure 5-16. Horizontal Time Base Alignment Points

Set the Oscilloscope Horizontal Control for a 100ft Sec/Div sweep rate and select the Tone B output on'-the Code Synthesizer.

Adjust the Fine Time Base Calibration Capacitor on the Scope Amplifier Board (Figure 5-16) so that one cycle of the displayed waveform occurs in 5 cm along the horizontal axis, Use the Vertical and Horizontal Position controls to center and to move the waveform so that the 5 cm are measured in the middle of the screen to avoid nonlinearities near the edge of the CRT.

5-118. Extended Alignment Procedure

5-119. DVM

- 1. Remove the top and bottom covers of the-R2001A.
- 2. Connect the R2001Atoa primary power source and turn it on. Allow approximatly 15 minutes warm up before proceeding with the alignment procedure.
- Short the center conductor of the DVM Input Jack on the front panel to ground. Connect an external DVM with a floating input between pin 1 and pin 6 of J3 on the bottom side of the motherboard.
- 4. Adjust the Coarse and Fine DVM Zero potentiometers on the Front Panel Interface board (Figure 5-17) for a reading of 0 ±0.5 mV on the external DVM.



Figure 5-17. DVM Input Buffer Alignment Points

 Remove the ground from the DVM Input and connect the DVM Input to TP 12 of the Scope/DVM Control Board. (Figure 5-18)



Figure 5-18. Scope/DVM Control Test Point Numbering

- Disconnect the external DVM from pins 1 and 6 of J3 and connect it to TP 12 of the Scope/DVM Control Board and chassis ground. Note the DVM reading for TP 12.
- 7. Reconnect the external DVM between pin 1 and pin 6 of J3. The external DVM should show a reading equal to one-tenth the voltage at TP 12 noted in paragraph 5-119.6 plus or minus 10 mV. If the reading falls outside this range it will be necessary to physically disconnect the front panel from the chassis in order to adjust the DVM Input Gain Potentiometer on the Front Panel Interface Card (Figure 5-17). Adjust the DVM Input gain for a reading on the external DVM equal to one-tenth the voltage noted for paragraph 5-119.6. Reconnect the front panel to the chassis.

- 8. Repeat paragraphs 5-119.3 and 5-119.4.
- Disconnect the external DVM. With the DVM input jack still shorted adjust the A/D Zero Potentiometer on the I/O Board (Figure 5-19) for a O.tfVDC'reading on the R2001ACRT display.

CAUTION

Do not use the card extender while aligning the Processor I/O board.

- 10. Remove the short from the DVM Input and connect the DVM Input to TP 12 of the Scope/DVM Control Board.
- 11. Adjust the A/D Gain Potentiometer on the Processor I/O Board (Figure 5-19) for a DVM reading on the CRT equal to the voltage measured at TP 12 with the external DVM for paragraph 5-119.6.
- 12. Connect the external DVM to TP11 of the Scope/DVM Control Board and chassis ground. Note the DVM reading for TP11.
- 13. Disconnect he external DVM from TP11 and connect the DVM Input Jack on the front panel to TP11 of the Scope/DVM Control Board.
- 14. Adjust the A/D Balance Potentiometer on the Processor I/O Board (Figure 5-19) for a DVM reading on the CRT equal to the voltage measured at TP11 with. the external DVM in step 13.



Figure 5-19. Processor I/O A/D Alignment Points
5-120. Character Generator

- 1. Perform the Basic Alignment Procedure of para 5-105.
- 2. Turn the R2001A off and extend the Scope/DVM Control Board using the 100 pin extender card.
- 3. Turn the R2001A on and select the Monitor FM Function and the Gen/Mon Mtr Display.
- 4. Adjust the Horizontal Character Sweep Width Potentiometer on the Scope/DVM Control Board (Figure 5-20) so that the right-hand edge of the CRT character display is approximately 4.2 graticule divisions to the right of the graticule center line.



Figure 5-20. Scope/DVM Control Char Sweep and Sinad Alignment Points

- Adjust the Vertical Character Sweep Width Potentiometer on the Scope/DVM Control Board (FigureS-20) so that the bottom edge of the CRT display is approximately 3.3 graticule divisions below the graticule center line.
- 6. Turn the system power off and reinstall the Scope/DVM Control Board into the R2001A,

5-121. Sinad Notch Filter

- 1. Turn the R2001A off and extend the Scope/DVM Control Board using the 100 pin extender card.
- 2. Turn the R2001A on and select the Generate FM Function and the Gen/Mon Mtr Display.
- 3. Set the Modulation Switch and the Ext. Level Control to their off positions. Set the BW Switch to the Narrow position and adjust the 1 kHz Level Control for a 20 kHz deviation reading on the CRT display.
- 4. Connect the Mod Out Jack on the fron panel to the Vert/Sinad/DVM/Counter Input Jack on the front panel.

- AHernatety ad/usl She Iwo SWAD Noich polenf/ome'ters on 8ie Scope/DVW Coniro) Board iF/gare 5-20) for a maximum SINAD reading on the CRT display. A reading greater than 30 dB should be obtained.
- 6: Turn the system power off and reinstall the Scope/DVM Control Board into the R2001A.
- 5-122. Receiver
- 5-123. AM Detector
 - 1. Perform the basic alignment procedure of para 5-105.
 - 2. Turn the R2001 A off and remove the Receiver Module. Remove the Receiver Module cover and install the Receiver Test Cover on the module housing. Extend the Receiver module on the Receiver Extender Card.
 - 3. Turn the R2001 A on and select the Monitor AM Function and the Gen/Mon Mtr Display. Set the monitor frequency to 250 MHz, the RF Step Attenuator to the 0 dB position, and the BW Switch to the Narrow position.
 - 4. Connect the external signal generator to the RF In/Out Jack on the front panel. Adjust the external generator for an output level of approximately -60 dBm and a calibrated 30% AM.
 - 5. Adjust R60 (Marked on the Receiver Test Cover) for a reading of 30% ±5% on the CRT AM display.
- 5-124. FM Detector
 - 1. Select the Monitor FM Function and the Gen/Mon Mtr Display. Set the monitor frequency to 250 MHz, the RF Step Attenuator to the 0 dB position, and the BW Switch to the Wide position.
 - Connect the external signal generator to the RF In/Out Jack on the front panel. Adjust the external generator for a center frequency of 250 MHz at an output level of approximately -30 dBm and a calibrated 20 kHz FM.
 - 3. Adjust R70 (Marked on the Receiver Test Cover) for a reading of 20 kHz ± 1 kHz on the CRT FM display.
 - 4. Set the BW switch to the Narrow position and reset the FM on the external generator to 3 kHz deviation.
 - Adjust R125 (Marked on the Receiver Test Cover) for a reading of 3 kHz ± 150 Hz on the CRT FM display.
 - Turn off the FM on the external generator so that a CW signal of a level of approximately -30 dBm is applied to the R2001A.
 - Connect the Demod Out Jack to the Vert/Sinad/DVM/Counter I nputdaek on the front panel. Select the DVM Display and the DC DVM Mode on the R2001A.
 ...
 - Adjust R68 (Marked on the Receiver Test Qover) for a 0.0 VDC ±100 mVDC reading on the DVM Display.

5-125. Spectrum Analyzer

- 1. Select the Monitor Function and the Spectrum Analyzer Display on the R2001A. Set the monitor frequency to 250 MHz, and the RF Step Attenuator to the 40 dB position.
- Connect the external signal generator to the RF In/Out Jack on the front panel. Adjust the external generator for a center frequency of 250 MHz and a calibrated output level of -30 dBm with no modulation.
- 3. Adjust in succession C2, C83, C88, and C96 (Marked on the Receiver Test Cover) to maximize the amplitude of the spectral line in the center of the CRT display.
- 4. Adjust R124, R91, and R100 (Marked on the Receiver Test Cover) to obtain a uniform change in the spectral amplitude per 10 dB change of the RFStep Attenuator. R124 affects the level of the spectral component when in the top quarter of the screen, R91 affects levels in the third quarter from the top, and R100 affects levels in the bottom quarter.
- Adjust R119 for offset and R121 for gain so that with the step attenuator in the OdBP osition the peak of the spectral line lies on the 30 dB line of the CRT and that successive step increases of the input attenuator move the spectral amplitude downward in 10 dB increments on the CRT. The accuracy required for any one step attenuator position is ±3 dB.
- 6. It will generally be necessary to repeat paragraphs 5-125.4 and 5-125.5 until the best possible accuracy is obtained.
- 7. Turn the power off and remove the Receiver Module and the Receiver Extender for the chassis. Remove the Test Cover from the Receiver Module and replace the module cover. Reinstall the Receiver Module into the system chassis.

5-126. CHECKOUT PROCEDURE

5-127. Introduction

5-128. This section provides a system checkout procedure. This procedure will help isolate system failures when used with the troubleshooting information in para 5-146.

5-129. Test Equipment Required

5-130. The test equipment listed in table 5-5 or its equivalent will be required to perform the checkout procedure.

*RF Signal Generator	Motorola R-1201A
"RF Power Meter	Motorola S-1339A
*SINAD Meter	Motorola R-1013A
"Modulation Meter	Boonton 82AD
RF Power Source	1 watt to 100 watts

Table	5-5	Test	Fauipment	
abie	J-J.	1630	Lyupment	

*An R2001 A is suitable for use in place of these separate equipments.

5-131. Procedure

- 5-132. Power On
 - Check that the AC input power select card is in the 120 V postion. Connect the Unit UnderTest (UUT) to a 120 VAC line source with the front panel power switch off. Verify the presence of an AC indication on the front panel.
 - 2. , Sat the poww swtah to the Stawltsy PosWw.. Verify ttwown ready wdifcatw te Wi.
 - Set the power switch to the on position. Verify that after a warm-up period a display is visible on the CRT.
- 5-133. Keyboard Check
 - 1. Verify that each key has the proper effect by observing the Gen/Mon Mtr Display and entering the frequency 123.4567 MHz and the PL frequency 890. Check for proper cursor key operation.
 - 2. Verify that the up and down display keys perform properly and that the LED at each display illuminates.
 - 3. Verify that the up and down function keys perform properly and, that the LED at each function illuminates.
 - 4. Verify that the up and down modulation keys perform properly and that the LED at each modulation mode illuminates.
- 5-134. Nonvolatile Memory
 - 1. Select some random combination of Display, Function, and Modulation Modes. Simultaneously depress both cursor keys and after a five second delay turn the system power OFF. Turn the system power back ON and verify that the same Display, Function, and Modulation Modes are present.
- 5-135. Modulation Capability
 - Set the UUT to the Generate FM Mode and select the Gen/Mon Mtr Display. On the Gen/Mon Mtr Display enter a DPLcode of 111. Select the Oscilloscope Display and connect the Mod Out Jack to the Vert In Jack. Set the code synthesizer to the Cont PL/DPL Mode. On the scope verify the presence of a DPL waveform whose amplitude is variable with the code synthesizer level control.
 - 2. Move the Modulation Switch from CONT to OFF and verify that a short burst of 133 Hz is output before the output stops.
 - 3. Move the Modulation Switch to the BURST position. Verify that a 133 Hz tone is output as long as the switch is held in the BURST position.
 - 4. Select the Tone A Continuous Mode. Verify a Tone A output on the scope and at the speaker.
 - 5. Select the Tone Remote Mode. Verify that when the Modulation Switch is moved from OFF to BURST that a single Tone Remote Access Sequence is generated.
 - 6. Connect a microphone to the Mic Jack- Turn up the ExtLevel Control and verify that speaking into the mike causes a modulation signat to be output as observed on the scope display.

5-136. Frequency Counter

- 1. Set the UUT to the Gen CW Mode with an output frequency of 35 MHz at a level of 0 dBm as displayed ontheGen/Mon Mtr display. Connect the RF In/Out Jack to the Counter In Jack of the UUT. Select the Frequency Counter Display and verify a frequency reading of 35 MHz.
- Set the UUT to the Generate FM Mode and select the Gen/Mon Mtr Display. Turn the Code Synthesizer and Ext Modulation sources OFF. Select the Narrow Band Mode and adjust the 1 kHz Level Control for a 5 kHzFM deviation reading. Connect the Mod Out Jack to the Counter Input Jack of UUT. Select the Frequency Counter Display and verify a nominal frequency reading of 1 kHz.

5-137. DVM

- 1. Maintaining the same conditions as in paragraph 5-136.2, select the DVM Display and the AC Mode on the display. Verify a DVM reading of 0.707 vrms ±0.04 vrms.
- 2. Select the DC Mode and verify a near zero volt DC reading.

5-138, Scope Mode

- 1. Set the UUT to the Scope AC display mode and connect the scope vertical input jack to the Mod Out Jack. Enable the internal 1 kHz modulation source. Verify the operation of each position of the vertical input range switch and the vertical vernier gain control.
- 2. With the same connection as in paragraph 5-138.1, verify the operation of each position of the Horizontal Control and the Horizontal timebase vernier.
- 3. With the Horizontal Control set to the External Mode, connect the External Horizontal jack to the Mod Out jack. Verify a horizontal line whole length is variable with the Horizontal Vernier.
- 4. Connect the Vert In jack to the Mod Out jack on the UUT. Set the vert and horizontal controls for a convenient display. Verify that a steady sync is obtained in either the Norm or Auto modes and that the point of triggering is adjustable with the level control. Remove the input signal and verify no horizontal sweep in the Norm mode and the presence of a horizontal sweep in the Auto mode.

5-139. SINAD Meter

- 1. Set the UUT for the Generate FM Function, Narrow Band Mode, and the Tone Memory Display. On the Tone Table set Tone A for 2000.0 Hz.
- Select the Gen/Mon Mtr Display and the Tone A Cont Modulation Mode. Turn the Ext Level and the 1 kHz Level Controls OFF. Adjust the Code Synth Lvl Control for an FM deviation of 1.88 kHz as read on the CRT display.
- 3. Without disturbing the Code Synth Lvl Control, turn the Code Synthesizer OFF. Turn ON the 1 kHz Level Control and adjust for an FM deviation of 7.5 kHz on the CRT display.
- 4. Connect the Mod Out Jack to the SINAD Input Jack on the UUT. Verify a SINAD reading greater than 25 dB.
- 5. Set the Code Synthesizer to the Continuous Mode and verify a SINAD reading 12 dB ±1 dB.

5-140. Scan Mode

1. Set the UUTfortheGen/Mon Mtr display. Verify the proper operation of each of the RF Scan, switch positions.

5-141. Generate Mode

- 1. Set the UUTforthe Generate FM Mode at 200 MHz'and select the Gen/Mon Mtr display. Verify an RF level output display on the CRT.
- Connect the RF millivoltmeter with a 50 ohm termination to the RF In/Out Jack on the UUT. Set the RF step attenuator to the OdB position and adjust the Variable Level control to obtain a displayed output level of +13 dBm. Verify that the RF millivoltmeter reads +13 dBm ±2 dBm.
- ' 3. Repeat paragraph 5-141.2 except at a center frequency of 800 MHz.
 - 4. Increase the RF Step Attenuator setting in 10 dB increments and verify that the displayed RF level decreases in 10 dB increments.
 - Set the Code Synthesizer Modulation Switch and the Ext Level Control to their respective OFF positions. Select the Narrow Band mode and adjust the 1 kHz Level Control for a 5 kHz deviation reading on the CRT display. Verify a 1 kHz tone at the speaker output.
 - Connect the Modulation Meter to the RF In/Out Jack on the UUT. Set the Modulation Meter for a deviation display of 5 kHz ±250 Hz.
 - Select the Wide Band mode on the UUT and verify that the CRT displays a deviation of 20 kHz. Also verify that the Modulation Meter shows a peak deviation of 20 kHz ±1 kHz.
 - Select the Modulation Display on the UUT and verify a peak-to-peak modulation display of 40 kHz ±2 kHz.
 - 9. Select the Generate CW Function and verify that no modulation is present on the CRT.
 - Set the UUT for the Generate AM Function, the Gen/Mon Mtr Display, and adjust for an RF output level of 0 dBm. Adjust the 1 kHz Level Control for a 50% AM reading on the CRT. Verify that the Modulation Meter reads 50% ±10% AM.
 - 11. Select the Modulation Display and verify a low distortion 1 kHz sinewave.
 - 12. Set the UUT for the Generate SSB/DSBSC Function and verify a low distortion 1 kHz sinewave on the CRT.
 - 13. Set the UUT for the Generate SWP 1-10 MHz Function and the Scope DC Display. Verify a horizontal trace and a center frequency display on the CRT.
 - 14. Set the UUT for the Generate SWP 0.01 1 MHz Function and verify the same results as paragraph 5-141.13.

5-142. Power Monitor Mode

- 1. Set the UUT to the Power Monitor Mode. Set the RF Step Attenuator to the 30 dB position, and select the Gen/Mon Mtr Display. Connect the RF power source to the RF In/Out Jack. Key the power source and verify a correct power reading on the CRT display. Unkey the power source.
- 2. Set the UUT to the Monitor Function and verify that the RF Step Attenuator is in the 30 dB position. Key the RF power source and verify the presence of an audible alarm and a warning display on the CRT. Unkey the power source.
- 5-143, Monitor Mode
 - 1. Set the UUT to the Monitor FM Function. Set the Squelch Control to the OFF position and verify the presence of a Sig Lvl indication and noise at the speaker. Turn the Squelch Control full on and verify the absence of a Sig Lvl indication and noise at the speaker.
 - 2. Repeat paragraph 5-143.1 except for the AM Function.
 - 3. Repeat paragraph 5-143.1 except for the SSB/DSBSC Function and enable the BFO. After the test turn the BFO off.
 - 4. Select the Narrow Band FM Monitor Function at 300 MHz and set the RF Step Attenuator to the OdB position. Connect the RF Signal Generator to the RF In/Out Jack and the SINAD Meter to the Demod Out Jack. Set the RF Signal Generator for a center frequency of 300 MHz and for 3 kHz FM at a 1 kHz rate. Adjust the RF output level from the Signal Generator for a 10 dB reading on the SINAD Meter. Verify that the Signal Generator's level is less than -103 dBm (1.5 yUVrms).
 - 5. Calibrate the RF Signal Generator for 3 kHz FM at 1 kHz rate using the Modulation Meter. Set the Generator for a nominal output level of 60 dBm and connect it to the RF In/Out Jack of the UUT. Select the Gen/Mon Mtr Display and verify a monitor deviation reading of 3 kHz ±150 Hz.
 - Calibrate the RFSignal Generatorfor50 kHz FM at a 1 kHz rate. SelecttheWide Band Modeon the UUT and verify a reading of 50 kHz ±2.5 kHz on the CRT deviation display.
 - Calibrate the RF Signal Generator for 30% AM at a 1 kHz rate. Set the Generator for a nominal output level of-60 dBm and connect it to the RF In/Out Jack of the UUT. Select the Monitor AM Function and the Narrow Band Mode. Verify a monitor AM reading of 30% ±5%.
 - Monitor the % AM Displayed on the CRT while increasing the RF level out of the Signal Generator. Verify that the IF Overload Warning occurs before the displayed AM exceeds a reading of 30% ±5%.
 - 9. Select the Modulation Display on the UUT and verify the presence of the received modulation signal.
 - 10. Select the Gen/Mon Mtr Display and the Wide Band Mode on the UUT. Vary the center frequency on either the UUT or the Signal Generator and verify that the Frequency Error Display properly represents the difference between the UUT's Center frequency and the Signal Generator's center frequency.
 - 11. Select the IF Display on the UUT and verify the presence of an IF envelope on the CRT.

5-144. Spectrum Analyzer

- Set the UUT for the Monitor Function of 300 MHz the Spectrum Analyzer Display, and 0 dB input attenuation. Set at 300 MHz. Connect the Signal Generator to the RF In/Out Jack on the UUT. Verify a spectral amplitude of -30 dBm ±5 dB on the CRT display. Increase the RF Step Attenuator in 10 dB increments verifying that the spectral amplitude decreases by 10 dB ±3 dB with each step.
- 2. Verify the operation of the Dispersion Control.
- 5-145. Duplex Generator-
 - Select the Duplex Generator Display and the monitor Function at a frequency of IOOMHz.Enable the 45 MHz offset frequency. For an Image Low switch position verify that a displayed duplex frequency of 55 MHz can be obtained. Set the Image Switch to the HIGH position and verify a duplex frequency display of 145 MHz.
 - Enable the 0- 10 MHz offset frequency and verify that displayed duplex frequencies from 100 MHz to 110 MHz can be obtained.
 - 3. Set the UUT to the Generate Function with the Duplex Generator Display. With the Code Synthesizer and the External Modulation sources OFF, adjust the 1 kHz Level Control for a 20 kHz FM deviation reading on the CRT. Select the Monitor Function and adjust the offset frequency for a duplex output of 100 MHz. Connect the Duplex Output Jack to the RF In/Out Jack and verify a 20 kHz ±1 kHz FM deviation reading on the CRT.

5-146. System Troubleshooting

5-147. A troubleshooting procedure is outlined in Table 5-6. Because of the complexity of the system the table covers only the major failures and provides only a guide to the most probable failed module. When using the table it is important to use the checkout procedure at paragraph 5-126 to determine the fault. The troubleshooting table assumes that all tests prior to the failure point have been successfully completed and thus the applicable circuits are okay.

5-148. A list of the system test points and their functions are provided in Table 5-7. Test points are identified on the block diagrams for the Theory of Operation discussion of paragraph 5-16 and for the Module Descriptions to aid in troubleshooting.

Table 5-6. System Troubleshooting

Test Paragraph	Fault	TroubleshootingProcedure
5-132	No AC indication	1. Check AC linecord and line fuse.
		If system powers up normally when on, Replace AC LED.
5-132	No Oven Ready indication	 Check for approximately +15 VDC at E13 of the A13 module. If not present replace the Low Voltage Power Supply (A1).
		 Check E11 of A13 for +9 VDC and E12 for approximately +7.5 VDC. If EU is okay and E12 is 0 VDC, replace the LED. If the +9 VDC is not present on E11 replace A13.

Test Paragraph	Fault	Troubleshooting Procedure
5-132	System won't turn on	 Disconnect the high voltage supply from the low voltage supply at A10P1. Check for +7.9 VDC at pin 1 of J2 on the low voltage supply and for+12 VDC at pin 2. If either volt- age is not present replace the low voltage supply (A1).
		2. Reconnect the low voltage/high voltage inter- face and check for a nominal +9 VDC at C15 on the high voltage supply. (C15 isafeedthru cap on the high voltage supply and can be reached from the top side just beyond the CRT socket.)
		CAUTION
		There is 110V on the rear panel connector even when the power switch is turned off.
		If 9 volts is not present replace the high voltage supply (A1).
		3. If items 1 and 2 check okay replace the low voltage supply (A1).
5-132	System turns on, but no display on the CRT for any display mode	 Check for presence of high voltage by dis- connecting the CRT anode lead and arcing it to the chassis. If no arc, replace the high voltage supply.
		2. If the high voltage supply is okay, replace the CRT.
5-133	More than one key is inopera- tive or has the wrong effect	1. Replace the Processor Module (A9).
5-133	Only one key is inoperative	1. Replace the defective key switch.
5-134	Any part of the nonvolatile memory fails to remember	1. Replace the Processor module (A9).
5-135	No DPL (modulation) signal on CRT	1. Check TP1 of the Audio Synthesizer for the presence of the DPL signal. If not present replace the Audio Synthesizer module.
		2. Check for the DPL signal on pin 64 of the Audio Synthesizer. If not present replace the IEEE Interface module (A8), or check for the presence of the jumpers on J8 for the standard unit.

Fault	Troubleshooting Procedure
	 Check for the DPL signal at TP6 of the Audio Synthesizer. If not present replace the Audio Synthesizer (A6).
	4. Check for the DPL signal at TP4 of the Scope Amplifier module (A2). If not present replace the Scope/DVM control module (A3).
	5. If signal switching is okay tolhe Scope Ampli- fier module proceed to the scope trouble: shooting information.
No external modulation on the CRT	 Check for modulation signal at TP7 of the, Audio Synthesizer module (A6). If not present replace the Audio Synthesizer module.
	 Check for the modulation signal on pin 66 of the Audio Synthesizer. If not present replace the IEEE Interface module (A8), or check for the presence of the modulation jumpers on J8 for the standard unit.
	 Continue troubleshooting at step 3 of the "no ; DPL signal on the CRT".
Frequency Counter inoperative	 Check for presence of a 1 kHz signal at TP9 of' the Audio Synthesizer (A6). If not present check for the 10 MHz signal from the Fre- quency Standard module (A13) to the RF Synthesizer (A5). If present replace the RF Synthesizer. If not present replace the Fre- quency Standard module.
	 If the 1 kHz signal is present check for the presence of the signal to be counted at pins 61 and 63 of the Processor I/O module (A7). If not present replace the Front Panel Interface Module (A12).
	 If signal is okay up to the Processor I/O module replace the Processor I/O module.
DVM AC mode is inoperative	 Check for DVM signal at pin 22 of Processor Interface module (A12). If not present replace the Front Panel Interface module.
	 Check for short bursts of the DVM AC signal at TP8 of the Scope/DVM Control module (A3).

Test Paragraph	Fault	Troubleshooting Procedure
		NOTE
		The DVM AC signal from the external input is multiplexed with the other signals to be mea- sured. Thus only short bursts of the input signal will be observed at TP8.
		If signal is not present at TP8 replace the Scope/DVM Control module.
		 If the signal is okay to TP8 of A3, replace the Processor I/O module (A7).
5-137	DVM DC mode is inoperative	 Check for the DC input level attenuated by factors of 10 to less than 1 volt at pin 22 of the Front Panel Interface module (A12). If not present or if greater than 1 volt, replace the' Front Panel Interface module.
		 If the signal isokay from A12, switch to the AC mode and apply an AC signal to the DVM in- put. Proceed from step 2 under DVM AC mode inoperative.
5-138	No horizontal sweep	 Check for a voltage level between -2.0 VDC and +2.0 VDC at TP4 of the Scope Amplifier module (A2). If the voltage cannot be brought within range with either the vertical range attenuator or the vertical position control replace the Front Panel Interface module (A12).
		If the voltage at TP4 is okay replace the Scope Amplifier module (A2).
5-138	No vertical display	 Check for the input signal at TP4 of the Scope Amplifier module (A2). If not present replace the Front Panel Interface module (A12).
		2. If signal is okay at TP4 replace the Scope Amplifier module.
5-138	No vertical sync	 Check for the presence of sync pulses at pin 12 of the Scope/DVM Control module (A3) and for a nominal zero volt sync present level at pin 76. If eithersignal is not present replace the Scope/DVM Control module.

Table 5-6. System Trouble	eshooting (Cont)
Fault	Troubleshooting Procedure.
	 If sync pulse and the syn present lines are okay replace the Scope Amplifier module (A2).
SINAD meter inoperative	1. If the DVM mode checks okay replace the Scope/DVM Control module (A3).
	 If the DVM mode does not check okay go to the troubleshooting list for DVM AC inoperative.
No generate output	1. Remove the RF cable between the RF Syn- thesizer (AS) and the RF Input module (A11). Check for a nominal -10 dBm level at the Synthesizer output. If no output replace the RF Synthesizer.
	2. If the Synthesizer output is okay replace the RF Input module.
No Frequency Modulation	1. Check for modulation signal at pin 56 of the RF Synthesizer (A5). If the signal is okay replace the RF Synthesizer.
	2. If the modulation signal is not present pro- ceed to the troubleshooting list under "no DPL (modulation) signal on CRT".
Internal wattmeter in error	1. Replace RP input module (A11).
T^o monitor function	 Apply a 10.7MHz modulated carrier td the RF input. Check for normal receiver operation except reduced sensitivity. If receiver is not working replace the Receiver module (A4).
	 If the receiver checks okay and the generate function is okay, replace the RF Input module (A11).
Monitor frequency error display is missing	1. Go to the troubleshooting list under "fre- quency counter inoperative".
Monitor frequency error is in error	 Check for presence of IF signal at pin 91 of the Scope/DVM Control module (A3). If not present replace the Receiver module (A4).
	2. If the IF signal is present replace the Scope/ DVM Control module.

Table 5-6. System Troubleshooting (Cont)

Test Paragraph	Fault	Troubleshooting Procedure
5-144	No spectrum analyzer sweep	 Check pin 6 of the RF Synthesizer module (A5) for a 50 Hz square wave. If not present replace the RF Synthesizer module.
		 If 50 Hz signal is present replace the Scope/ , DVM Control module (A3).
5-144	Spectrum display is in error	1. Replace the Receiver module (A4).
5-145	No duplex output	1. Replace the RF Input module (A11).

Table 5-7. Test Point Identification

All test points are located near the top edge of the card and counted from left to right when facing the; component side of the card.

Module	Test Point No.	Signal Description
A2	1	Horizontal Amp Input
Scope Amplifier	2	Horizontal Deflection Plate
	3	Horizontal Deflection Plate
	4	Vertical Amp Input
	5	Focus Tracking Voltage
	6	Vertical Deflection Plate
	7	Vertical Deflection Plate
	8	Z-Axis Modulator Output
	9	Intensity Tracking Voltage
	10	Time Base Output
A3	1	Vertical Character Sync
Scope/DVM	2	Negative Peak Detector Output
Control	3	Gen Carrier Plus AM Level
	4	Positive Peak Detector Output
	5	Demodulated Calibrated Audio
	6	Not Used
	7	Ground
	8	Multiplexed A/D Signal
	9	Character Generator Reset
	10	Ground
	11	-8VDC
	12	+8 VDC
A6	1	Synth/DPL Audio
Audio Synthesizer	2	DPL Clock
	3	Unfiltered DPL

Module	Test Point No.	Signal Description
	4	Synth. D/A Output
	5	Ground
	6	Composite Modulation Audio
	7	Composite External Mod. Audio
	8	Synthesizer Clock 104, 857.6 Hz
	9	1 kHz Modulation Source
A7	1	A/D Input
Processor I/O	2	Unfiltered 10.245 MHz T.V.
	3	DVM/Freq. Counter Select
	4	Frequency Counter Input
	5	Not Used
A9	1	Ground
Processor	2	Character Clock
	3	Character Row Clock
	4	Character Dot Clock
	5	Enable
	6	Character Line Clock
	7	R/W Select
	8	Char. Gen/Processor Select
A12	1	Attenuator Buffer Output
Front Panel Interface		

SECTION 6

SYSTEM INTERCONNECT AND PARTS LISTS















												N	OTHERBOAR	D ASSEMBLY	
Find No.	Qty. Req.	Part Number	Nomenclature	Part Value	Find No.	Qty. Req.	Part No.	Nomenclature	Part Value				RTL-	4060A	
											Find	Oty	Part No.	Nomenclature	Part Valu
		15-80335A44	COVER, SYSTEM, TOP		050		30-P068058001	CABLE ASSEMBLY ATTA	10./ MHZ,IF		NO.	ney.			
		15-80335A69 55-80335A58	COVEH, SYSTEM HANDLE, BAIL		060		30-P06806R001	CABLE ASSEMBLY, OFFSET	A11/MOTHERBOARD						
	<u>.</u>	55-80335A73	HANDLE, BAIL		061	-	30-P06807R001	CABLE ASSEMBLY 455KHZ	A4/MOTHERBOARD	Ľ	003	-	MP-0100-50-DW-6H	CONNECTOR	
		55-80335A72	HANDLE, MOLDED		063		30-P06809R001	CABLE ASSEMBLY 10.245	A4/MOTHERBOARD	J	004	-	MP-0100-36-DW-6	CONNECTOR	
	N -	15-80335A59	COVER, HANDLE		066	2	29-15159A03	TERMINAL,LUG			005		MP-0100-36-DW-61	4 CONNECTOR	
	- 1	01-P00249N002	FRONT COVER ASSY		068	AR		WIRE	16 WHT		006		MP-0100-50-DW-6	CONNECTOR	
	2	55-80335A89	HINGE		069	AR AR	SN63WRP3	NICH ATION SI FEVING	MULT		800	-	MP-0100-50-DW-6	CONNECTOR	
	3 10	MS35206-213	SCREW	4-40X1/4	071	AR	M20000/0-100-0	WIRE	20 WHT		600		MP-0100-50-DW-6	CONNECTOR	
	4	03-80335A97 MS51957-27	SCREW	6-32X5/16	072	AR		TAPE	NATURAL		010	-	MP-0100-50-DW-6	H CONNECTOR	
	4	MS35338-136	WASHER	NO.6	073	AR		WIRE	24 WHT	F	001	-	25-83127G01	CHOKE,AUDIO	
	4	MS15795-806	WASHER	NO.6	074	AR		INSULATION TAPE, MYLAR	1IN YELLOW		002		25-83127G01	CHOKE AUDIO	
	22	04-80335A99	WASHER, FLAT BLACK	NO.6	076) N	10-80340A24	POOVET			004	• •	MS91189-33	COIL	47UH
	» 22	MS35338-136B	STRIKE CATCH	NO.6	077	- •	MS35489-9	GROMMET		_	005		MS90539-07	COIL	470UH
	2 1	MS24693-C49	SCREW	8-32X.438	078	AR		COMPOUND, THD LKG, BLUE	ETYPE II, GR N, 242	_	. 006	-	MS91189-37	COIL	100UH
	10	MS35338-40	WASHER,LOCK	NO.4	079	AR		ENCAPSULANT SILICONE							
	10	NAS620C4L	WASHER, FLAT	NO.4	080	<u>-</u>	32-P041351001	WIRE	3						
	_	58-84300A98 30-P04147T001	CONN, ADAPTER	24 PIN	082	2	MS35206-213	SCREW	4-40X.250						
	AR		WIRE	26	083	2	MS35206-231	SCREW	6-32X.625						
	AR	11-14167A10	INK	WHITE	084	0 10	9224-A-140-10A	SPACER	.250DX.25L						
	AR	SN63WRP3	SOLDER		980 GBN		33-14232A09 14-P01317V001	INSULATOR	FP/INTERFACEBD						
001	- 1	01-80304A57	FRONT PANEL ASSEMBLY		A 001	·	RTP-1000A	LOW VOLTAGE PWR SUPPI	LAI						
002	-	27-80335A01	CHASSIS, SYSTEM		A 002		RTL-4007A	SCOPE HORZ/VERT AMP							
004	- תי	MS35206-226	SCREW PH	6-39X 250	A 004		RTL-1002A	RECEIVER ASSY A4							
05	4	66602-1	CONTACT, PIN		A 005		RTC-1001A	SYNTHESIZER A5							
006	26	MS35206-214	SCREW,PH	4-40X.312	A 006	-	RTC-4011A	AUDIO SYNTHESIZER A6							
007	88	MS27183-3	WASHER, FLAT	NO.4	A 007		RTC-4012A BTC-4013A	PROCESSOR I/O A7	STEM ONI YI						
600	12	MS24693-S24	SCREW, FLHD	6-32X1/4	A 009	-	RTC-4014 A	MICRO PROC CHAR GEN							
010	AR	01001011000	WIRE	22 WHT	A 015		RTL-4054A RTP-1001A	BLOWER ASSEMBLY	410						
012	- 3	50D83205B03	SPEAKER		A 011	_	RTC-1003A	RF FRONT END	A11						
013	-	26-80335A54	SHIELD, CRT		A 012	-	RTL-4045A	FRT PANEL INTRF ASSY							
014		42-P00481N001	BRACKET, CRT SHIELD		A 013		HIL-1004A	FHEQUENCY STANDARD	A13						
016	N -	20-F00047H001 3-134169	SCREW, THD FORMING		F 002		F02A250V5A	FUSE,CARTRIDGE	250V-5A						
017	15	3-134185	SCREW, THD FORMING	6-32X.250	F 003	-	F02A250V1-1/2A	FUSE,CARTRIDGE	250V-1 1/2A						
021	0 0	348-8-7-7	FUSEHOLDER		J 002		MP-0100-36-DW-6	CONNECTOR	DE DIN						
028	- ~	42-14060A01 RTI -4060A	CLAMP ASSEMBLY, CONN	Y Y	J 014		206061-1	CONNECTOR, BATTERY	4-PIN MALE						
034	2	206903-1	SEALING CAP	:	J 015	-	6J4	CONNECTOR	POWER INPUT						
035	1	36-80335A88	KNOB, SPECIAL		J 016	-	206430-1	CONNECTOR, BLOWER	4 CONTACT						
040	- c	75-80335A51	ISOLATOR, CRT, BOTTOM		T 001		24-P00243N001 95-80335A48	CATHODE BAY TUBE							
042	NN	07-80335A93	FOOT BATTERY HOLDER												
043	2	07-80335A94	FOOT, BATTERY HOLDER,	F											
044	N	75-80335A60	FOOT, PLASTIC												
048	ω υ	MS51957-27B	SCREW,PH BLACK	6-32X.312											
049	28	MS35206-215	SCREW	4-40X.375											
050	12	MS27183-5	WASHER, FLAT	NO.6											
052	ω u	MS35338-136B	WASHER I OCK BLACK	NO.6										ASSEMBLY PAP	ALS LIST
053	12	MS35338-41	WASHER,LOCK	NO.6										System Top	Level
054	10	MS35649-242	NUT,HEX	4-40											
057	2	64-P00301N001	PLATE, THREADED												

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ASSEMBLY PARTS LIST

Part Value

6-11 6-12

SECTION 7

LOW VOLTAGE POWER SUPPLY (A1)

7-1. General. The low voltage power supply converts either an AC line input or a DC supply input to the DC operating voltages required by the system. Appropriate protection circuits are incorporated within the supply to protect both the supply and the system in vhe event of certain common malfunctions. A block diagram of the Low Voltage Supply module is shown in figure 7-1 with its schematic shown in figure 7-2.

7-2. Input Power Control. Whenever AC power is connected to the unit the DC BUS within the supply is supplied by the AC rectifier and filter circuitry. The AC sense circuit provides a control voltage whenever AC is present that isolates the DC input from the DC bus and drives the front panel AC indicator.

7-3. With power on the DC buss the power supply control circuitry determines the operating mode of the power supply. With the unit "OFF", the battery charger control circuit is turned on and the frequency standard control and chopper generator circuits are turned off. When the unit is in "STANDBY", the chopper generator is off and the frequency standard supply and battery charger are enabled. Finally with the unit "ON", the frequency standard supply and the chopper generator are enabled and the battery charger is off. Thus the battery is charged in off and standby modes, and the frequency standard operates in standby and on modes.

7-4. The voltage for the battery charger is boosted above the nominal DC bus voltage to 32 volts by the AC boost winding. This increase in voltage is necessary for proper charge operation.

7-5. For operation from a DC input, the AC power must be removed from the unit disabling the AC sense voltage. With the AC power removed and the unit off, no power is present on the DC bus. When the unit is switched to the standby mode, the DC relay closes, connecting the DC input to the DC bus and the supply voltage to the frequency standard is enabled. Then with the unit turned on the chopper generator is enabled and normal operation occurs.

7-6. DC Output Control. Regulation of the DC output voltages is accomplished by regulating only the +5V output. The transformer winding ratios determing the other output voltages with respect to the +5 volt output. The +5V output is compared with a stable reference voltage and the resultant control voltage is used to determine the on time of the pulse-width modulator, thus regulating the input voltage to the chopper circuits.

7-7. The chopper generator provides the 7.9 volt reference voltage, a 20 KHz square wave chopper drive signal, and a 20 KHz triangle waveform output for pulse-width modulator control. The pulse-width control comparator compares the triangle waveform with the control voltage. If the control voltage is equal to the mean DC voltage of the triangle wave the pulse modulator has a 50% duty cycle. For control voltages above and below the mean value the duty cycle is proportionally increased or decreased.

7-8. The filtered DC output from the pulse-width modulator is chopped through the primary of the output transformer at the 20 KHz rate. The DC output is alternately switched between the upper half and the lower half of the primary winding. The current through the primary center tap is detected by a current transformer and its output used for overcurrent protection.

7-9. Protection Circuitry. The power supply is protected against shorted outputs, high internal temperatures, and low or high DC buss voltages. In each case the protection circuit pulls the control voltage line to ground to open up the pulse-width modulator and shut down the supply.

7-10. Short circuit protection is implemented by monitoring the current in the primary winding of theoutput transformer. If a secondary output is shorted the primary current will increase significantly causing the overcurrent detector to pull the control line low shutting down the supply. However, with the supply shut down primary current will cease and the overcurrent detector will release the control line. With the control line released the supply will come back on. If the short is still present the cycle will repeat itself. Delay is provided in the overcurrent detector so that with a shorted output the supply cycles at about a 0.5 second rate.

7-11. Overtemperature protection is obtained by using a thermal switch mounted on the most heat critical capacitor. If the capacitors temperature exceeds the temperature setting of the thermal switch, the switch closes to ground shorting the control line and shutting down the supply. Normal operation of the supply will be resumed when the temperature returns to a safe operating level.

7-12. Protection for high or low DC and AC line inputs is provided by monitoring the voltage on the DC Bus. If the bus voltage exceeds 20 volts or if the voltage falls below 10 volts, the shutdown circuitry pulls the control line to ground shutting down the supply. When the bus voltage returns to normal limits, supply operation automatically resumes.

7-13. High Voltage Supply Control. A 12 volt regulator from the DC BUS provides the bias voltage for the High Voltage Power Supply (AIO). The primary power for the High Voltage Supply comes from the high voltage control circuitry. A control line from the high voltage supply regulates the input voltage to the high voltage transformer. The current used in the high voltage supply is the bias current for the pulse width modulator circuitry for improved power supply efficiency.





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Figure 7-1. Low Voltage Power Supply A1

Block Diagram

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7-3,7-4

(RTP-1000A)

Schematic Diagram (Sheet 1 of 2)

Figure 7-2. Low Voltage Power Supply A1



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066 1	065 AF	063 1	062 2	061 1	060 1	050 17 2	057 6	054 5	053 8	052 9	051 1	050 1	049 2	047 4	045 10	043 4	042 4	041 5	040 5	038 4	036 1	034 5	033 2	032 2	031 7	030 5	020 s	026 2	025 27	024 AF	022 AF	1 020	019 9	018 4	017 4	016 5	014 6	012 3	011 29	010 4	009 1	008 1	007 - 2	006 1	004 1	003 1	002 1	001 1		NO. HEC	Find Oty			
B51567F025	G-642	B52595F019	B52600F002	B51547F013	MS35206-217	BU8853AUU1	011-1046-000-4	MS35649-242	MS35338-40	MS27183-3	DB-25P	DE-9S	29-15122A10	MS/1000-2	MS35206-230	MS35206-216	MS24693-S2	MS24693-S26	NAS662-2-8	B51547E015	B52200F006	14-15141A01	B09002A001	42-14063A01	C9029-4Z	MD-3452-G	4505-632-A-9	07-P00305N002	29-15106A63	-	SN63WRMAP3	M333409-0	MS35649-262	MS35338-41	MS27183-5	MS35206-228	MS35338-43	MS35207-264 MS27183-8	3-134185	MS35206-248	4586-48	64-P00311N001	4586-97A	15-00330A44	01-80304A40	RTP-4008A	RTP-4001A	27-80335A42		÷	. Part No.		RTP-1000A	Voltane Power S
WASHER, FLAT	COMPOUND, THERMAL	LUG	INSULATOR, MICA	WASHER, TEFLON	SCREW.PH	WASHER, MICA-HECT	79 TERMINAL, FEEDTHRU INS	NUT,HEX	WASHER,LOCK	WASHER, FLAT	CONNECTOR	CONNECTOR	TERMINAL	SCBEW	SCREW	SCREW, PH	SCREW, FH	SCREW	SCREW		WASHER MICA INSTI	INSULATOR	WASHER, RECT	CLAMP ASSEMBLY	CLIP, FASTENER	SOCKET	SPACEB HEX	BRACKET, CONNECTOR	TERMINAL, FEEDTHRU-INS	WIRE	SOLDER	BRACKET CONNECTOR	OBDOMMET	WASHER,LOCK	WASHER, FLAT	SCREW	WASHERLOCK	SCREW WASHER FLAT	SCREW, THD FORMING	SCREW	CLAMP, CAPACITOR	PLATE, PROTECTIVE-SMAL	CLAMP.CAPACITOR		RELAY ASSEMBLY	OUTPUT PWB ASSY	CONTROL CIRCUIT PWB A	CHASSIS ASSY			Nomenclature			innly A1
					4-40X.500			4-40	NO.4	NO.4				110-04Y 375	6-32X.500	4-40X.438	4-40X1/4	6-32X3/8	2-56X.500											22 WHT			6-32	NO.6	NO.6	6-32X.375	NO.10	10-32X.625 NO 10	6-32X.250	8-32X.875											Part Value			•
			Q 008	Q 007	Q 005	L 047	L 040	L 021	L 018	L 016	L 004	CR071	CR061	CR057	CR056	CR025	C 110	C 108	C 107	C 097	C 095	C 087	C 063	200.0	C 050	C 044	C 043	C 021	C 020	093	092	091	090	089	0880 7.80	086	085	084	083	082	080	079	078	077	074	073	071	070	069	068	067	NO.	Find	
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			48-80345A61	48-869302	48-80345A61	10273	10273	10273 250.84148E01	10273	10273	24-P00323N001	48-80345A67	48-6411A15	48-80345A89	48-80345A89	48-80345A91	2499-003X5W502	2499-003X5W502	2499-003X5W502	36D462G015AA2	2499-003X5W502	2499-003X5W502	M39003/01-2283	2499-003A3W302	2499-003X5W502	2499-003X5W502	500D507G025FH	36D462G015AA2	500D507G025FH	ABMM-A-C	MS35333-37	MS35333-37	MS3367-4-9	MS35207-261	MS35206-215	01-80304A56			SST21-M	11-1410/201	MS3367-5-9	M23053/5-103-9		MS77068-1	RTV3145	9317-A-194	MS35649-222	MCOE000 00			B51566F011		Part No.	
			TRANSISTOR	TRANSISTOR	TRANSISTOR	FERRITE BEAD	FERRITE BEAD	COII	FERRITE BEAD	FERRITE BEAD	CHOKE	DIODE	DIODE	DIODE	DIODE	DIODE	AA CAPACITOR	AA CAPACITOR	AA CAPACITOR	A CAPACITOR	AA CAPACITOR	AA CAPACITOR	CAPACITOR	AA CAPACITOR	AA CAPACITOR	AA CAPACITOR	7 CAPACITOR	A CAPACITOR	CAPACITOR	TIE MOUNT, CABLE	WASHER	WASHER	STRAP	SCREW	SCREW	CABLE ASSEMBLY	WIRE	INSULATION SLEEVING	STRAP,CABLE	WIRE BUS	STRAP,CABLE	INSULATION SLEEVING	TAPE	TERMINAL,LUG	ADHESIVE, SILICONE	SPACER, ROUND	NUT HEX	WIRE	WIRE	WIRE	WASHER, LOCK		Nomenclature	
								651 IH			57MH	100V	20V SAMP	20V JAMP	20V 3AMP	40V-40A	5000PE-GMV-500	5000PF-GMV-500	5000PF-GMV-500	4600UF-15V	5000PF-GMV-500	5000PF-GMV-500	2.2UF-10-20	50011E-25V	5000PF-GMV-500	5000PF-GWV-500	500UF-25V	4600UF-15V	500UF-25V	140001 IE-2007	NO.6	NO.6	NATURAL	10-32X.375	16 WHI 4-40X 375	14PIN, DUALSIDE 12LG	20 WHT	22 WHT	8.0	22	NATURAL 4.5	.093 WHT	NATURAL	4		10X.25	2-56 2-56	LHW 02	14 WHT	16 WHT			Part Value	
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(Sheet	Low Vo	ASSEN																											·							24-P00327N001	7BTF5A-14	243E1R05	6S124A49	6S124A49	6S124A39	6S124A25	6S124A25	6S124A01	6S124A25	48-80345A43	48-80345A55	48-80345A56	48-80345A61	48-80345A50	48-80345A50		Part No.	
1)	oltage Power Sup	MBLY PARIS LIS																																		DIODE	THERMOSTAT	RESISTOR	RESISTOR	RESISTOR	RESISTOR	RESISTOR	RESISTOR	RESISTOR	RESISTOR	TRANSISTOR	TRANSISTOR	TRANSISTOR	TRANSISTOR	TRANSISTOR	TRANSISTOR		Nomenclature	
	ply	1	1																																			1-5-5	1K-5-1/4	1K-5-1/4	390-5-1/4	200 6 1/4	100-5-1/4	10-5-1/4	100-5-1/4								Part Value	

1.1.2.1.2

Low Voltage Power Supply Control Assy A1A1



CR062	CR049	CR047	CHU43	CH042	CR041	CR040	CR039	CR038	CR024	CR023	CR003	CR001	C 101	000 0	C 083	C 082	C 081	C 080	C 079	C 070	0.069	C 067	C 066	C 061	C 060	C 059	C 056	C 054	C 048	C 047	C 046	C 045	C 042	C 041	C 039	C 038	C 037	C 036	C 035	010	C 001	010	600	008	007	004	002	001	A1		NO.	Find		
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48-84463K02	48-84463K02	48-84463K02	48-84463KU2	48-84463KU2	48-84463K02	48-84463K02	48-84463K02	48-84463K02	48-80345A68	48-80345A68	48-80345A68	48-80345A68	3439050E104M	3439050E104M	3439050E104M	3439050E104M	3439050E104M	3439050E104M	3439050E104M	3439050E104M	3439050E104M	31063187814	3439050E104M	23D84665F01	3439050E104M	3439050E104M	3439050E104M	23D83441B15	3439050E104M	3439050E104M	3439050E104M	3439050E104M	3439050E104M	3439050E104M	21082187814	21D82428B62	M39014/02-1356	21D82428B62	A39014/02-1338	31084665E01	3439050E104M		MS35649-242	MS35338-40	MS27183-3	SN63WHMAP3	SRS8-10N	84-P00471N001				Part No.	RTP-400	ontrol Circuit F
DIODE	DIODE	DIODE			DIODE	CAPACITOR	CAPACITOR	CAPACITOR	CAPACITOR	CAPACITOR	CAPACITOR	CAPACITOR	CAPACITOR	CAPACITOR	CAPACITOR	CAPACITOR	CAPACITOR	CAPACITOR	CAPACITOR	CAPACITOR	CAPACITOR	CAPACITOR	CAPACITOR	CAPACITOR	CAPACITOR	CAPACITOR	CAPACITOR	CAPACITOR	CAPACITOR	CAPACITOR	CAPACITOR	CAPACITOR	CAPACITOR	CAPACITOR	WIRE	NUT,HEX	WASHER,LOCK	WASHER, FLAT	SCREW PH	SUPPORT, SPACER	PWB,LVPS CONTROL				Nomenclature	1A	WB A1A1							
											50V-1A	50V-1A	.1UF-20-50	1115-20-50	105-20-50	.1UF-20-50	.1UF-20-50	.1UF-20-50	.1UF-20-50	1UF-20-50	.1UF-20-50	1000PF-10-100	1105-20-50	10UF-25V	.1UF-20-50	.1UF-20-50	.1UF-20-50	1.0UF-20-35	.1UF-20-50	.1UF-20-50	.1UF-20-50	.1UF-20-50	.1UF-20-50	.1UF-20-50	1115-20-50	.01UF80-20-200	.22-10-50	.01UF80-20-200	.01UF-10-200	1011E-25V	.1UF-20-50	22	4-40	NO.4	NO.4	4-40X.312						Part Value		
				950 G	H U34	R 033	R 031	R 030	R 029	R 028	R 023	R 022	R 018	R 006		H 003	R 002	R 001	Q 030	Q 028	Q 027	0 025	0 024	Q UZZ	Q 020	Q 018	Q 016	Q 015	0 014	0.013	Q 002	Q 001	L 038	L 034	L 032	L 030	L 029	L 028	L 026	L 025	L 020	L 015	L 014	L 013	L 012	L 011	001	CR072	CR070	CR069	CR064	CR063	No. F	
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			00124710	65124473	00124400	65124A/3	6S124A73	6S124A65	6S124A65	6S124A73	6S125A33	6S125A33	6S124A03	6S124A44	6S124A73	6C124B14	65124A57	6S124A45	48-80335A79	48R00869570	48-80345A59	48R00869570	48900869570	48-80345443	48R00869570	48-80345A43	48R00869570	48R00869570	48-80345A59	48-00343A00	48-80345A58	48R00869570	24-83961B01	24-83961B01	24-83961B01	24-83961801	24-83961B01	24-83961B01	WEE-100	24-83961B01	24-83961B01	24-83961B01	24-83961B01	24-83961B01	24-83961B01	24-83961B01	24-R3961R01	48-84463KU2	48-84463K02	48-84463K02	48-84463K02	48-84463K02	Part No.	
				RESISTOR	DESISTOD	RESISTOR	RESISTOR	RESISTOR	RESISTOR	HESISTOR	RESISTOR	TRANSISTOR	TRANSISTOR	TRANSISTOR	TRANSISTOR	TRANSISTOR	TRANSISTOR	TRANSISTOR	TRANSISTOR	TRANSISTOR	TRANSISTOR	TRANSISTOR	TRANSISTOR	TRANSISTOR	TRANSISTOR	CHOKE, RF	CHOKE,RF	CHOKE.RF	CHOKE, RF	CHOKE, RF	CHOKE, RF	INDUCTOR	CHOKE,RF	CHOKE.RF	CHOKE, RF	CHOKE, RF	CHOKE, RF	CHOKE, RF	CHOKERF		DIODE	DIODE	DIODE	DIODE	DIODE	Nomenclature								
				10K-5-1/4	108-5-1/4	10N-0-1/4	10K-5-1/4	4.7K-5-1/4	4.7K-5-1/4	10K-5-1/4	220-5-1/2	220-5-1/2	12-5-1/4	620-5-1/4	10K-5-1/4	470K-5-1/4	2.28-0-1/4	680-5-1/4																					100UH														Part Value	
										VR006	VR005	VR004	VR002	VR001	U 007	U 006	U 005	11 004	002	R 090	R 089	R 088	R 087	R 086	R 084	P 082	F 081	R 080	R 072	R 070	R 065	R 064	R 063	R 062	R 061	R 060	R 059	R 057	R 056	R 055	R 054	R 053	B 050	R 050	R 049	R 048	R 046	R 045	R 040	R 039	R 038	R 037	No.	Find
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(She	LOW	A O	100							48-818459	48-82256C45	48-82256C25	48-86850C95	48-82256C25	51-84621K22	51-80345A08	51-80345A24	51-80345A01	51-80345A2/	6S124A89	6S124A09	6S124A49	6S124A27	6S124A27	6S124A73	6S124A92	6S124A35	6S124B46	6S124A57	6S124A65	6S124A89	6S124A03	6S124A81	6S124A83	6S124A97	6S124A73	18D83452F11	18D83459E15	6S124B11	6S124B11	18D83452F17	6S124A89	65124449	6S124A65	6S124A73	6S124B30	6S124A79	6S124A73	6S124A65	6S124A73	6S124B22	6S124A73		Dart No.
et 2)	Voltage Power Su		YEMBI V DADTO I I							DIODE	DIODE,ZENER	DIODE,ZENER	DIODE,ZENER	DIODE, ZENER	INTEGRATED CIRCUIT	ITERGRATED CIRCUIT	INTEGRATED CIRCUIT	INTEGRATED CIRCUIT		RESISTOR	RESISTOR	RESISTOR	RESISTOR	RESISTOR	RESISTOR	RESISTOR	RESISTOR	RESISTOR	RESISTOR	RESISTOR	RESISTOR	RESISTOR	RESISTOR	RESISTOR	RESISTOR	RESISTOR	RESISTOR, VARIABLE	RESISTOR VARIARI F	RESISTOR	RESISTOR	RESISTOR, VARIABLE	RESISTOR	RESISTOR	RESISTOR	RESISTOR	RESISTOR	RESISTOR	RESISTOR	RESISTOR	RESISTOR	RESISTOR	RESISTOR		Nomenclature
	ippiy		P T								9.1V-54	12V-54	20V-55	12V-54				n di	-	47K-5-1/4	22-5-1/4	1K-5-1/4	120-5-1/4	120-5-1/4	10K-5-1/4	100K-5-1/4	270-5-1/4 69K-5-1/4	10M-5-1/4	2.2K-5-1/4	4.7K-5-1/4	47K-5-1/4	10K-5-1/4	22K-5-1/4 NOMIN	27K-5-1/4	100K-5-1/4	10K-5-1/4	58	20K	360K-5-1/4	360K-5-1/4	50K	47K-5-1/4	1K-5-1/4	4./K-5-1/4	10K-5-1/4	2.2M-5-1/4	18K-5-1/4	10K-5-1/4	4.7K-5-1/4	10K-5-1/4	1M-5-1/4	10K-5-1/4		Part Value

Low Voltage Power Output Assy A1A2



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VK200-10/3B	VK200-10/3B	40-00340A00 VK200-10/3B	48-00345408	48-80345A69	48-80346A66	48-80346A66	48-80346A67	48-84250L01	48-80346A67	48-80346A66	48-80345A70	48-80345A70	48-80345A70	48 B034EA70	21C82543H03	500D507G025FH7	500D507G025FH7		3CZ5U105D8500C5	3CZ5U105D8500C5	3CZ5U105D8500C5	3CZ5U105D8500C5	21C82543H03	21C82543H03	21C82543H03	500D157G050EH7	500D507G025FH7	21C82543H03	21C82543H03	500D507G025FH7	500D507G025FH7	39D258G015GP4	21C82543H03	21C82543H03	TVA1413	14-P04128T001	11-14167A01			RTV3145	01-80304A55	MS35649-242	MS35338-40	MS27183-3	MS35206-214	2-0-440-0-2	2052-8-440-8-2	14-P041281002	84-P00239N001			Part Number	RTP-4008A	Output PWB A
COIL	COIL	COIL		DIODE1A	DIODEA	DIODEA	DIODEA	DIODEA	DIODEA	DIODE1A	DIODE1A	DIODE1A	DIODE1A	CAPACITUR	CAPACITOR, FEEDTHRU	CAPACITOR	CAPACITOR	CAPACITOR	CAPACITOR, CERAMIC	GAPACITOR,CERAMIC	CAPACITOR, CERAMIC	CAPACITOR, CERAMIC	CAPACITOR, FEEDTHRU	CAPACITOR, FEEDTHRU	CAPACITOR FEEDTHRU	CAPACITOR	CAPACITOR	CAPACITOR, FEEDTHRU	CAPACITOR, FEEDTHRU	CAPACITOR	CAPACITOR	CAPACITOR	CAPACITOR FEEDTHRU	CAPACITOR, FEEDTHRU	CAPACITOR	INSULATOR	INK	WIRE,BUS		ADHESIVE, SILICONE	CABLE ASSEMBLY	NUT,HEX	WASHER,LOCK	WASHER, FLAT	SCREW.PH	INSTITUTION TAPE	SDACEB	SOLDEB	PWB,LVPS OUTPUT			Nomenclature	-	1A2
		154-100	100V-1A	100V-1A	40V-1A	40V-1A	20V-5A	30V-3A	20V-5A	40V-1A	400V-1A	400V-1A	400V-1A	400V-1A	5000PF80-20-500	500UF-25V	500UF-25V	50011E-25V	1UF-50V	1UF-50V	1UF-50V	1UF-50V	5000PF80-20-500	5000PF80-20-500	5000PF80-20-500	150UF-25V	500UF-25V	5000PF80-20-500	5000PF80-20-500	500UF-25V	500UF-25V	2500UF-15V	5000PF80-20-500	4011E-150V	40UF-150V		BLACK	22			16PIN, DUALSIDE 3LG	4-40	NO.4	NO.4	4-40X.312	1IN VELLOW						Part Value		
CR021	C 106	C 105	C 104	C 102	C 101	C 092	C 091	025	023	022	021	020	019	017	016	015	014	013	012	010	009	800	007	006	005	004	003	100	001		No.	Find								T 002	P 070	H U//	R 076	R 075	R 074	R 073	L 046	L 027	L 010	L 009		L 006	Find No.	
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48-80345A66	2499-003X5W502A	6PS-P22	2499-003X5W502A	2499-003X5W502A	2499-003X5W502A	39D507G030FL4	39D507G030FL4	NAS671C6		SN63WRMAP3	G-642	RTV3145			MW-688-195-002	29-15106A63	011-1046-000-479	2104-632-A-9	B51547F012	B51568E015	B51566F020	B52595F025	MS77068-2	MS35338-41	MS27183-5	MS35649-262	MS35206-227	27-P00237N001	07 000007560001			Part No.			01-80304	Relay Assemb				24-P00324N001	65124A59	6S124A35	6S124A56	6S124A76	6S125A96	6S125A96	VK200-10/3B	24-P00326N001	VK200-10/3B	VK200-10/3B	VK200-10/38	24-P00326N001	Part Number	
DIODEF-GMV-500	A CAPACITOR	CAPACITOR	A CAPACITOR	A CAPACITOR	A CAPACITOR	CAPACITOR	CAPACITOR	NUT	INSULATION SLEEVING	SOLDER	COMPOUND	ADHESIVE, SILICONE	WIRE	WIRE	WASHER, MICA	TERMINAL, FEEDTHRU-IN	TERMINAL, FEEDTHRU IN	SPACER, HEX	WASHER, TEFLON	NUT10	WASHER,LUCK	TERMINAL,LUG	TERMINAL, LUG	WASHER,LOCK	WASHER, FLAT	NUT,HEX	SCREW	BBACKET BELAV				Nomenclature			40	IV A1A3				TRANSFORMER	RESISTOR	RESISTOR	RESISTOR	RESISTOR	RESISTOR	RESISTOR	COIL	CHOKE	COL		COIL	CHOKE	Nomenclature	
30V-15A	5000PF-GMV-500	.22UF-600V	5000PF-GMV-500	5000PF-GMV-500	5000PF-GMV-500	500UF-30V	500UF-30V	1380-32	16 WHT	-			16 WHT	14 WHT		S	S		10 CF	10-32	NO.10	N0.10	N0.6	NO.6	NO.6	6-32	6-32X.312					Part Value								TBANSEOBMED	2.7K-5-1/4	270-5-1/4	2.0K-5-1/4	13K-5-1/4	91K-5-1/2	91K-5-1/2		20UH				20UH	Part Value	

Find No. CR022 CR054 CR055	CR054 CR055	CR066	CR067	CR068	K 001	L 017	L 048	L 049	L 050	L 051	V 001
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Nomenclature DIODE5A DIODE5A DIODE5A	DIODE5A DIODE5A	DIODE5A	DIODE	DIODE	RELAY	CHOKE	FERRITE BEAD	FERRITE BEAD	FERRITE BEAD	FERRITE BEAD	VARISTOR
Part Value 30V-15A 30V-15A 30V-15A	30V-15A 30V-15A	100V	100V	100V	12V						VARISTOR



ASSEMBLY PARTS LIST Low Voltage Power Supply (Sheet 3)

SECTION 8

SCOPE AMPLIFIER (A2)

8-1. General. The Scope Amplifier module contains the horizontal and vertical deflection amps, the horizontal timebase generator, focus and intensity control circuitry, and miscellaneous CRT bias adjustments. A block diagram of the Scope Amplifier module is shown in figure 8-1 with its schematic shown in figure 8-2.

8-2. Deflection Amplifiers. The vertical and horizontal deflection amplifiers are identical. The input signal is initially amplified and split into two signals 180° out of phase. Each of the two signals is then further amplified to become the CRT deflection plate signals. The amplifiers provide up to 200 volts peak-peak signal capability with a 1 MHz frequency bandwidth.

8-3. Horizontal Timebase Generator. The horizontal timebase generator provides calibrated sweep rates over a six decade range from 111 sec to 100 msec per division. Sweep rate selection is from the processor via the SCOPE SWP CONT 0-7 signal lines. Veriner control over the sweep rate is via the SWP VERN VOLT input from the front panel. Sweep triggering is either the auto or normal mode as selected by the AUTO/NOR TRIG SEL line from the front panel. In the auto mode if the SYNC PRESENT input is high indicating no sync, the scope sweep is self triggered after a hold off time. If there is a sync present, the sweep will wait for a pulse on the TRIG PULSE line to start the sweep after the hold off time. For the normal trigger mode the sweep will always wait for a TRIG PULSE input.

8-4. A sweep cycle consists of two parts, the sweep and the hold off. During the sweep the CRT is unblanked via the SWP BLANKING line and the horizontal trace is made. At the end of the sweep the CRT is blanked and the hold off time begins. During the hold off time, which is equal to the sweep time, the sweep generator and trigger circuits are reset in preparation for the next sweep.

8-5. Horizontal Switching. The input to the horizontal deflection amp is selected between two sources. The INT HORIZ IN signal line provides the horizontal character sweep and the horizontal spectrum analyzer sweep. The other source is the scope mode signal path from the horizontal positioning suming amp. The scope mode signal is either the output of the Horizontal Timebase Generator or the EXT HORIZ INPUT from the front panel. Selection between internal horizontal and scope mode horizontal inputs is via the SCOPE MODE EN line from the processor. Selection between the two scope mode signals is via the EXT HORIZ EN line.

8-6. Intensity Control. A crossover network is used to provide CRT Z-axis modulation from DC to 1 MHz. The INTEN LVL signal from the front panel control is gated with the SCOPE Z-AXIS signal by the Intensity Level Gate. The gated signal is sumed with the HV REF and INTEN SMPL VOLT signals to provide the INTEN TV signal. The INTEN TV (Intensity Tracking Voltage) is the low frequency control path which drives the intensity optoisolator in the High Voltage Supply.

8-7. The high frequency modulation path is via the Z-Axis Modulator circuit, line. Ine. The resulting CRT Z-AXIS signal is capacitively coupled to the CRT grid.

8-8. Focus Control. The FOCUS TV (Focus Tracking Voltage) signal is obtained by comparing the FOCUS LEVEL control line to the FOCUS SAMPLE VOLT signal. The tracking voltage signal drives an optoisolator circuit in the High Voltage Supply which controls the CRT focus voltage.

8-9. Astigmatism, Geometry, and Trace Rotation. These three CRT alignment controls are obtained from the respective wipers of three potentiometers. Each potentiometer is connected between supply voltages equal to the adjustment range required.



Figure 8-1. Scope Amplifier A2 Block Diagram





8-2 2 Schematic Diagram (RTL-4007A)

Scope Amplifier A2

Figure

Scope Horz/Vert Amplifier A2 RTL-4007A

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68V-55	39V-55	39V-55	33V-5-5	004-0-0	397-2-2	33V-5-,5		68V-55	39V-55	39V-55	33V-55	0.0	68V-55	29V-2-2	33V-55						75V-55	2000PF-10-200	.1UF-10-100	.01UF80-20-200	2PF5PF-500	9PE- 5PE-500	5600E-10-500	560PF-10-500	560PF-10-500	.01UF80-20-200	.01UF80-20-200	.01UF80-20-200	011 1580-20-200	560PE-10-500	.01UF80-20-200	.01UF80-20-200	.01UF80-20-200	.01UF80-20-200	.1UF80-20-25	.1UF80-20-25	.1UF80-20-25	2PF5PF-500	2PF5PF-500	.01UF80-20-200	.01UF80-20-200	.01UF80-20-200	0111F80-20-200	560PF-10-500	560PF-10-500	.01UF80-20-200	.01UF80-20-200	01UF80-20-200	.01UF80-20-200	.01UF80-20-200	 10 10 10 10 10 10 10 10 10 10 10 10 10		Part Value
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ASSEMBLY PARTS LIST Scope Amplifier Parts (Sheet 1)

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Scope Amplifier (Sheet 2)

ASSEMBLY PARTS LIST
SECTION 9

SCOPE/DVM CONTROL MODULE (A3)

9-1. General. A primary function of the Scope/DVM Control Module is to route the required measurement and viewing signals to the DVM and scope circuitry. A large portion of the displayed data is determined by the DVM measurements on internal signal points. Thus for a rapid update of several data displays it is necessary to time division multiplex several measurement points to the DVM. The DVM control circuitry and the system processor provide this function.

9-2. The scope control circuitry allows the system to display data information, internal modulation or demodulated signals, and external scope inputs as selected by the user. Provisions are also made for external horizontal inputs and a horizontal sweep that is coherent with the sweep generator for spectrum analyzerand filter alignment displays.

9-3. The control module also contains circuitry forsingle sideband demodulation and a IF phase locked loop for filtering and waveshaping the IF signal for frequency counting. A block diagram of the Scope/DVM Control module is shown in figure 9-1 with a schematic shown in figure 9-2.

9-4. Scope Vertical Control. The input to the scope vertical amplifier is switched between four different sources; the range switch (VERT FROM RNG SW), the vertical character sweep, the spectrum analyzer (SPECT ANA VERT), or the 455 kHz IF. Range switch inputs are from either the scope vertical input jack on the front panel or the internal modulation signals as selected by the modulation display control on this module. The vertical character sweep is a sawtooth waveform generated by the Vertical Character Sweep Generator and synced by the VERT CHAR SYNC signal from the character generator. The detected and amplified output of the receiver logarithimic IF is the vertical input for the spectrum analyzer. The remaining signal source is the second IF signal from the receiver for IF envelope observation.

9-5. For the spectrum analyzer and the scope sweep displays the Dual Display Control and Character Sweep Counter circuitry allow a single row of characters at the top of the CRT. This function is implemented with the Vertical Sweep Control by alternating the spectrum analyzer or the range switch signal with the vertical character sweep signal.

9-6. The dual display sequence of events starts with the Synthesizer Sweep Generator which is common to both display modes. When the synthesizer sweep is near its peak (scope horizontal sweep is at the edge of the screen) the Dual Display Control activates the CHAR GEN RST line and switches the scope vertical and horizontal inputs to their character generator sweeps. When the first character line has been traced,, a transition on the LINE 1 input from the character generator resets the character generator sweeps and the character generator, increments the Character Sweep Counter, and thus causes line! to be traced again. This process repeats until four traces.as counted by the Character Sweep Counter, have been completed. At that point the counter resets the scope inputs back to the spectrum analyzer or range switch input. During the character display time the synthesizer sweep generator is reset and held until a transition on the SYNTH SWP SYNC line restarts the sweep. The timing of the process allows for the four character traces to be completed before the sweep sync occurs.

9-7. SSB Detection. Single Sideband (SSB) modulation is recovered by multiplying the 455 KHz IF signal with a 455 KHz beat frequency oscillator (BFO) signal. The BFO is controlled directly from the front panel and is adjustable over a 6 KHz frequency range. SSB AUDIO from the multiplier is routed to the receiver for post

detection filtering. A sample of the BFO signal is made available to the frequency counter on the IF/BFO FREQ line for sideband frequency error determination.

9-8. 455 KHz PLL. For monitor frequency error determination a 455 KHz Phase Locked Loop (PLL) is used to filter and to shape the IF signal. The cleaned up signal is switched with the BFO signal to the frequency counter.

9-9. Scope Horizontal Control. Switching for the scope horizontal input is divided between two modules. The time base generator and the external horizontal input are selected on the scope amplifier module. The. Horizontal Character Sweep Generator and the Synthesizer Sweep Generator signals are selected on the Control Module to the INT SCOPE HORIZ signal line.

9-10. For the dual display modes (characters and synthesizer sweep) the Horizontal Switch Control switches the horizontal input between the synthesizer sweep and the character sweep. This switching occurs simultaneously with that occurring in the scope vertical control as described in paragraph 9-6. The Horizontal Switch Control also provides the SCOPE MODE EN line to the scope avplifier to enable the scope mode horizontal inputs.

9-11. Synthesizer Sweep Control. The sweep signal generated by the Synthesizer Sweep Generator is controlled in amplitude and in range across the front panel sweep width control. Attenuations of 1.0 or 0.1 are provided by the Sweep Width Select circuitry to the sweep signal at the DISPERSION SWP signal line to the top of the width control. The bottom side of the width control is returned to the Sweep Width Select circuitry via the DISPERSION SWP RTN line. A 10 to 1 resistor change is made in the return line simultaneously with the attenuator change to give sweep ranges of 1-10 MHz and 0.01-1 MHz.

9-12. Scope Z-Axis Control. The SCOPE Z-AXIS signal has three possible sources as selected by the Z-Axis Control circuit. For character displays the Z-Axis signal is the CHAR GEN Z-AXIS from the character generator. The SWP BLANKING signal from the horizontal timebase generator is switched to the scope Z-Axis for the scope modes. For the remaining modes, spectrum analyzer and scope sweep, a logic zero level is gated to the Z-Axis input.

9-13. Modulation Display Control. Internal modulation or demodulated signals are displayed on the scope by switching the desired signal source to the input ranging switch and then switching the ranging switch output to the scope vertical input. One of two modulation sources or a demodulation output can be switched to the INT SCOPE TO RNG SW signal line for display on the CRT. Each of the signals are gain adjusted prior to the selection switch for scope calibration.

9-14. The DEMOD CAL AUDIO signal from the receiver is either AM, FM, or SSB as determined by the operating mode. The peak signal level on this line is calibrated to 10kHz/voltforFMand 10%/voltforAM.SSB signals are not calibrated.

9-15. For AM the CARRIER + MOD LVL input from the generator output detector provides a direct display of the modulation. This input is a DC level representative of the average output level plus an AC signal representative of the amplitude modulation on the output. For the scope modulation display the DC level is blocked so that only the AC component is observed. This input is uncalibrated for absolute AC levels, but the processor by determining the peak AC and average DC levels can determine the modulation depth.

9-16. For FM the MOD CAL AUDIO input from the audio synthesizer is calibrated to 5 kHz/volt for narrow band and to 20 kHz/volt for wide band. Correspondingly the display calibrating attenuator has two gain ranges to maintain the same display calibration for both narrow and wide band.

9-17. **Peak Detector.** Each of the modulation and demodulation inputs can be selected to the peak detecting circuitry for the determination of % AM or kHz deviation. The peak detector circuitry provides DC outputs equal to the negative and positive peak values of the input signal relative to the average DC level of the signal. These levels are then digitized by the DVM and input to the processor where the modulation level is determined.

9-18. DVM Control. Any one of ten internal or one external measurement point may be switched to the DVM for level digitization. Switching is controlled by the processor so that measurements are made to provide current display data. In general several measurement points must be input to obtain all the displayed data. Therefore the processor continuously cycles the switch through the required inputs stopping at each one long enough to digitize and input its level.

9-19. The Internal DVM Select switch is followed by a range attenuator. As the processor cycles through the inputs it sets the range attenuator according to the last cycle reading made at that input. Thus each internal input is auto ranged over two decades to give three digit accuracy up to a maximum input of 10 volts. The internal DVM inputs and their function are listed in table 9-1.

+ Peak Voltage	Positive modulation measurements
- Peak Voltage	Negative modulation measurements
Carrier Level	RF output level
RF INPUT PWR	Power level applied to the RF input/output port
EXT FWD PWR	Forward power level on external inline wattmeter element.
EXT RFL PWR	Reflected power level on external inline wattmeter element.
BATT VOLT	voltage level at DC input jack on the rear panel
TEMP SENS VOLT	+5V level signal the processor that the RF load temperature is too high.
SINAD OUT	DC level proportional to the signal power at the output of the SINAD notch filter.
SINAD IN	DC level proportional to the signal power at the input of the SINAD notch filter.

Table 9-1. Internal DVM Inputs

9-20. External DVM inputs to the front panel jack are ranged by the processor over a four decade range before being routed to the DVM switch. At the Internal/External DVM Select switch the external DVM FROM RNG SW signal or the internal signal from the x0.1 Attenuator is selected to the DVM to A/D signal line for digitization.

9-21. SINAD Detection. The SINAD of a signal on the DVM FROM RNG SW line is determined by taking the ratio of the input to the output signal power on the 1 kHz Notch Filter. Signal power is determined by Rectifier and Filter circuits whose outputs are DC levels proportional to the input signal levels. The DC levels, SINAD IN and SINAD OUT are digitized and input to the processor where the SINAD is calculated.

9-22. Module Control. Processor control of the Scope/DVM Control module is via the AF ADD BUS 0-3, the AF DATA BUS 0-3, and the AF BUS EN 1 signal lines. The four address bits are decoded by the Address Decode to determine which Control Latch the fours bits of data will be latched. The latching process is synchronized by the enable line. Control latches in addition to those necessary for controlling the module provide control for the Scope Amplifier module and part of the RF Input module.

Figure 9-1. Scope/DVM Control Module A3 Block Diagram (Sheet 1 of 2)







Figure 9-2. Scope/DVM Control Module A3 Schematic Diagram (Sheet 3 of 4) (RTC-4008A)



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Figure 9-2. Scope/DVM Control Module A3 Schematic Diagram (Sheet 4 of 4) (RTC-4008A)



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			00121022	6S124B22	06D83175C04	06D84444A40	06-10621C42	06-10621C59	6S124B22	6S124622	60404020 14	18D83459F14	6S124B22	6S124A63	06D83175C03	06-10621D38	06-10621886	06-10621C45	6S124A57	6ST24A77	6S124A73	6S124A63	05124A49	00124400	00124200	60104762	60124745	6C104440	6S124A53	6S124A49	6S124A77	6S124A73	6S124A56	6S124A63	6S124A97	6S124A97	6S124A97	6S124A65	6S124A65	6S124A73	6S124A73	06D84444A45	06D83175C10	6S124A57	65124441	6S124A89	6S124A53	65124281	6S124A25	6S124A25	6S124A66	6S124A97	6S124A79	06D84444A27	06-10621D24	18D83452F12	6S124A73	6S124B38	65124A49	06-10621E27	06D84444A16	6S124A66				
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		1017-1-1/4		1M-5-1/4	10.5K-1-1/4	6.34K-1-1/4	3090-1-1/8	4640-1-1/8	1M-5-1/4	1M-5-1/4		INK	1M-5-1/4	3.9K-5-1/4	10K-1-1/4	30.1K-1-1/8	825-1-1/8	3320-1-1/8	2.2K-5-1/4	15K-5-1/4	10K-5-1/4	3.9R-5-1/4	1K-5-1/4	3.9K-5-1/4	3.97-3-1/4	3.0K-3-1/4	3 6K 5 1/4	14-6-1/4	1.5K-5-1/4	1K-5-1/4	15K-5-1/4	10K-5-1/4	2.0K-5-1/4	3.9K-5-1/4	100K-5-1/4	100K-5-1/4	100K-5-1/4	4.7K-5-1/4	4.7K-5-1/4	10K-5-1/4	10K-5-1/4	5 60K-1-1/20	12.4K-1-1/4	2 2K-5-1/4	470-5-1/4	47K-5-1/4	1.5K-5-1/4	4/0-5-1/4	100-5-1/4	100-5-1/4	5.1K-5-1/4	100K-5-1/4	18K-5-1/4	42.2K-1-1/4	21.5K-1-1/8	5K	10K-5-1/4	4 7M-5-1/4	1K-5-1/4	249K-1-1/8	2000-1-1/4	5.1K-5-1/4		Part Value		
																																																															1			Í.

ASSEMBLY PARTS LIST Scope/DVM Control Module (Sheet 1)

Scope/DVM Control A3 RTC-4008A

Scope/DVM Control Module (Sheet 2)

ASSEMBLY PARTS LIST

	Find	Qty.		Nomenclature	Part Value	Find No.	Aty.		Nomenclature	Part Value	Find No.	Req.	Part No.	Nomenclature	Part Value
t				3											
	R 076		06D83175C03	RESISTOR	10K-1-1/4 1M-5-1/4	R 145	1	6S124A65	RESISTOR	4.7K-5-1/4	∪ 049	_	51-84887K10	INTEGRATED CIRCUIT	
	R 078	-	18D83452F14	RESISTOR, VARIABLE	10K	R 146		06-10621D21	RESISTOR	20K-1-1/8	U 050		51R84887K09-	INTEGRATED CIRCUIT	
	R 079	-1	6S124B22	RESISTOR	1M-5-1/4	R 147		06-10621048	RESISTOR	38.3K-1-1/8 20K-1-1/8	U 052		51-84887K73	INTEGRATED CIRCUIT	
	R 080		6S124B22	RESISTOR	1M-5-1/4	R 149	-	06-10621D21	RESISTOR	20K-1-1/8	U 054	-	5180345A10	INTEGRATED CIRCUIT	
	R 081		06-10621D88	RESISTOR	100K-1-1/8	R 150	-	06D83175C03	RESISTOR	10K-1-1/4	U 055	-	51-84118K13	INTEGRATED CIRCUIT	
	R 083	-	6S124A80	RESISTOR	20K-5-1/4	R 151		6S124A73	RESISTOR	10K-5-1/4	U 056	•	51R84887K09	INTEGRATED CIRCUIT	
	R 084	_	6S124A80	RESISTOR	20K-5-1/4	R 152		6S124A73	RESISTOR	10K-5-1/4 20K-1-1/8	U 058		51-80345A18	INTEGRATED CIRCUIT	
	R 085		06-10621092	RESISTOR	10K-1-1/8	R 154		06-10621D52	RESISTOR	42.2K-1-1/8	U 059	-	5180345A10	INTEGRATED CIRCUIT	
	R 089		6S124A73	RESISTOR	10K-5-1/4	R 155	-	06-10621D21	RESISTOR	20K-1-1/8	U 060		51-80345A01	INTEGRATED CIRCUIT	
	R 092	_	6S124A49	RESISTOR	1K-5-1/4	R 156		06-10621D21	RESISTOR	20K-1-1/8	VR001		21-10043A00 48-82556C15	DIODE,ZENER	5.1V-104
	R 093	-	6S124A53	RESISTOR	1.5K-5-1/4	R 157		06D83175C03	RESISTOR	10K-5-1/4	VR002	_	48-82556C15A	DIODE,ZENER	5.1V-54
	R 095	· -	6S124A29	RESISTOR	150-5-1/4	R 159	- -	6S124A73	RESISTOR	10K-5-1/4	VR003	-	48-82556C15A	DIODE,ZENER	5.1V-54
	R 096	. .	6S124A29	RESISTOR	10K-5-1/4	R 160	1	6S124A73	RESISTOR	10K-5-1/4					
	R 098	_	6S124A73	RESISTOR	10K-5-1/4	R 161	-	6S124A73	RESISTOR	10K-5-1/4					
	R 099-	-	6S124A57	RESISTOR	2.2K-5-1/4	R 162	-	06-10621D01	RESISTOR	12.4K-1-1/8					
	R 100	-	6S124A57	RESISTOR	2.2K-5-1/4	H 163		06-10621067	RESISTOR	8/20-1-1/8					
	R 101	• -•	6S124A73	RESISTOR	10K-5-1/4	R 165		06-10621C67	RESISTOR	5620-1-1/8					
	R 103	. .	18D83452F13	RESISTOR, VARIABLE	10K	R 166	-	6S124A65	RESISTOR	4.7K-5-1/4					
	R 104	-	06-10621E85	RESISTOR	1M-1-1/8	F 167		18D83452F01	RESISTOR, VARIABLE	2K					
	R 105	_	6S124A43	RESISTOR	12 04 1-1/4	H 1001		18D83452F19	NTEGRATED CIRCUIT						
	H 106		06D83175C12	RESISTOR	13.0K-1-1/4	U 002		51R84320A80	INTEGRATED CIRCUIT						
	R 108	-	06D84444A42	RESISTOR	7.15K-1-1/4	U 003	-	51R84320A80	INTEGRATED CIRCUIT						
	R 109		6S124A65	RESISTOR	4.7K-5-1/4	U 005		51R82822F35	INTEGRATED CIRCUIT						
	R 111		6S124A66	RESISTOR	5.1K-5-1/4	U 006	1	51-80345A05	INTEGRATED CIRCUIT						
	R 112	-	06-10621D52	RESISTOR	42.2K-1-1/8	800 U		51-80345A19	INTEGRATED CIRCUIT						
	R 113	.	06D84444A16	RESISTOR	2000-1-1/4	U 010		5180345A10	INTEGRATED CIRCUIT						
	R 115	<u> </u>	06-10621D36	RESISTOR	28.7K-1-1/8	U 011	-	51-84928F02	INTEGRATED CIRCUIT						
	R 116	-	06D83175C76	RESISTOR	90.9K-1-1/4	U 012		51R82822F35	INTEGRATED CIRCUIT						
	R 117		06-10621898	RESISTOR	1100-1-1/8	U 014		51-06472A26	INTEGRATED CIRCUIT						
	R 121	-	6S124A65	RESISTOR	4.7K-5-1/4	U 016		51-80345A04	INTEGRATED CIRCUIT						
	R 122	-	6S124A65	RESISTOR	4.7K-5-1/4	U 017		51-84887K73	INTEGRATED CIRCUIT						
	R 123		6S124A73	RESISTOR	10K-5-1/4	U 020	-	51-80345A05	INTEGRATED CIRCUIT						
	R 125	-	6S126B61	RESISTOR	4.7-5-1	U 021	-	51-80345A01	INTEGRATED CIRCUIT						
	R 126		6S124A63	RESISTOR	3.9K-5-1/4	U 022		51-80345A04	INTEGRATED CIRCUIT						
	R 127		6S124A68	RESISTOR	0.27-3-1/4 1M-5-1/4	U 024		51-84887K73	INTEGRATED CIRCUIT						
	R 129		6S124A45	RESISTOR	680-5-1/4	U 026	-	51-80345A01	INTEGRATED CIRCUIT						
	R 130	-	6S124A56	RESISTOR	2.0K-5-1/4	U 028	. <u>.</u>	51-84887K73	INTEGRATED CIRCUIT				1		
	R 131		6S124A56	RESISTOR	2.0K-5-1/4	620.0	-	51-80346A53							
	R 133	- 	06-10621C95	RESISTOR	11K-1-1/8	U 034	-	51-84887K72	INTEGRATED CIRCUIT						
	R 134	-	6S124A73	RESISTOR	10K-5-1/4	U 037	-	51-84887K10	INTEGRATED CIRCUIT						
	R 135	1	6S124A73	RESISTOR	10K-5-1/4	850 U		51-82822F64	INTEGRATED CIRCUIT						
	R 136		6S126B61	RESISTOR	4./-5-1 3.9K-5-1/4	U 040		51R84320A80	INTEGRATED CIRCUIT						
	R 138		6S124A68	RESISTOR	6.2K-5-1/4	U 041	-	51-84887K10	INTEGRATED CIRCUIT						
	R 139	- -	6S124B22	RESISTOR	1M-5-1/4	U 042	-	51-84887K10	INTEGRATED CIRCUIT						
	R 140	-	6S124A45	RESISTOR	680-5-1/4	U 043		51R84320A80	INTEGRATED CIRCUIT						
	R 141		6S124A56	RESISTOR	2.0K-5-1/4	U 045		51-84928F02							
	R 143		06-10621F55	RESISTOR	6.81K-1-1/8	U 046	-	51-80345A16	INTEGRATED CIRCUIT						
						U 047	-	51-84887K10	INTEGRATED CIRCUIT						
	5					0 048	-	51-8488/K10							

SECTION 10

RECEIVER (A4)

10-1. General. The Receiver down converts the 10.7 MHz first IF signal to 455 kHz. Following the down conversion a linearoralogarithimic IF amplifier provide the gain prior to AM and FM detectors or the spectrum analyzer detector respectively. Post detection filtering provides the wide or narrow band responses for the audio outputs. The audio amplifier for the speaker and the alarm generator are also contained on this module. A block diagram of the Receiver is shown in figure 10-1 and its schematic in figure 10-2.

10-2. Down Converter. The 10.7 MHz IF signal is converted to 455 kHz by mixing with a 10.245 MHz local oscillator. The local oscillator is phase locked to the system 10 MHz frequency standard. A sample of the 10.245 MHz VCO signal is output to the Processor I/O module. There the VCO signal is mixed with 10 MHz, the difference is divided by 49, and the result compared with a 5 kHz reference obtained from the 10 MHz. Any frequency difference causes a correction to be made to the VCO frequency via the 10.245 MHz VCO TV line through the Loop Filter.

10-3. The IF filter following the mixer provides the selectivity for the system. Two bandwidths, ±100 kHz wideband and ±13 kHz narrowband, are processor selectable to correspont the front panel bandwidth control.

10-4. Linear IF Amplifier and Detectors. The linear IF Amplifier amplifies the 455 kHz signal to the AM and FM detectors. The DC signal from the AM detector is fed to the AGC Amplifier and Squelch Detection circuitry. There it is compared to the AGC reference with the resulting AGC signal controlling the gain of the IF Amplifier. For signal present indication and squelch operation the SQUELCH LVL from the front panel is compared to the AGC voltage. When the AGC voltage fall below the squelch level, indicating a strong signal, the SIG PRESENT line is activated. With the SIG PRESENT active the audio is allowed through the select switch and the signal present light on the front panel is illuminated. To warn the operator when the IF input level is beyond the linear range of the IF amplifier, the AGC voltage is also compared to a fixed IF overload level. When this level is exceeded, the IFOVLD line is activated causing the processor to flash the warning on the CRT display.

10-5. The AC component from the AM detector is buffered by the Audio Buffer and then passed to the Audio Select switch. The lower 3 dB corner on the AM audio response is approximately 100 Hz.

10-6. Frequency modulation is recovered by a dual bandwidth phase locked loop discriminator. The bandwidth, wide or narrow, is selected coincident with the IF Filter bandwidth. Audio from the discriminator is applied to the Audio Select switch.

10-7. A 455 kHz Buffer amplifier provides an interface between the IF Amplifier output and the IF processing circuits on the Scope/DVM Control module.

10-8. Audio Switching and Filtering. The output of the AM or FM detector or the SSB AUDIO signal from the Scope/DVM Control module can be selected as the demodulated audio output. Selection is made by the processor depending on the operating mode and the presence of the active state on the SIG PRESENT line. If the SIG PRESENT line is not active, the Audio Select switch is opened squelching the audio signal.

10-9. The Audio Filter provides either wide or narrow band filtering on the recovered audio. For wideband a 0.5 dB bandwidth of 100 kHz is provided.while narrowband hasa0.5dB bandwidth of 3kHz. Theoutput of the filter is separately buffered to three signal lines. The DEMOD CAL AUD signal is used on the Scope/DVM

Control module for modulation determination, the DEMOD OUT signal goes to the front panel jack, and the VOL CNTL ADD provides the drive to the speaker audio amplifier.

10-10. Logarithmic Amplifier and Detector. For the spectrum analyzer function the logarithmic IF amplifier processes the input signal level over an 80 dB range. The Amplifier is composed of four 20 dB sections summed together. Amplitude detection at the output of the amplifier provides the SPECT ANA VERT signal to the Scope/DVM Control module.

10-11. Alarm Generator and Audio Amplifier. An astable multivibrator operating at 1.2 kHz is the Alarm Generator. The Alarm signal is controlled by the processor and is summed with the VOL CNTL AUD RTN signal at the input of the Audio Amplifier. The SPKR ADD output of the amplifier has 0.5 watt capability and is connected directly to the system speaker.

10-12. Module Control. Address decoding for the two control latches on this module is performed on the Synthesizer module. The two decoded lines, RF LCHADD13and RFLCH ADD 14, determine which Control Latch the four bit data bus, RF DATA BUS 0-3, will be stored.



Figure 10-3. Receiver Parts Locator



Figure 10-1. Receiver A4 Block Diagram







	C 027	C 026	C 025	C 024	C 023	C 022	C 021	C 020	C 019	C 018	C 017	C 016	C 015	C 014	C 013	210.0	0 013	0.011	C 010	C 009	C 008	C 007	C 005	C 004	C 003	C 002	C 001	020	019	010	010	015	014	013	012	011	600	008	007	006	005	001		NO.	Find	!					1 003	1 002	A 001	014	013	012	011	010	600	007	006	005	003	001			No.	Find			
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ASSEMBL	21D84494B06	21C82372C10	210823720.09	21D82428836	21C82372C09	21C82372C09	21D82187B14	21D84494B46	21C82372C09	21C82372C09	21D82187B14	21D84494B46	21C82372C09	21C82372C09	21D82187B14	21D84494B44	21002372009	21082272000	21082372000	21C82372C09	21D82428B62	21D84494B14	21D84494B40	21D84494B37	21D84494B12	CV31D250	21C82372C09		26-P00367N011	26-P00367N010			20-1 00307 19007	20-1 00007 1000	26-D00367N005	20-1003071004	26-P00367N003	26-P00367N002	26-P00367N001	11-14167A01	SN63WRMAP3	84-P00466N001			Part No.	•		RTL-4	Receiver F		5634-5015-00	5604-5014-00	HTL-4047A		30-14349A04	30-84421F13	MS35206-214	42C84284B01	5C84500B03	3S134169	11-14167A01	SN63WRMAP3	15-80335A37	27-80335A38				Part No.		RTL-10	Receive
Y PARTS LI	CAPACITOR	CAPACITOR	CAPACITOR		CAPACITOR	CAPACITOR	CAPACITOR	CAPACITOR	CAPACITOR	CAPACITOR	CAPACITOR	CAPACITOR	CAPACITOR	CAPACITOR	CAPACITOR	CAPACITOR	CAPACITOR		CAPACITOR	CAPACITOR	CAPACITOR	CAPACITOR	CAPACITOR	CAPACITOR	CAPACITOR	CAPACITOR	CAPACITOR	INSULATION SLE	SHIELD	SHIELD	SHIELD						SHIELD	SHIELD	SHIELD	INK	SOLDER	PWB, RECEIVER			Nomenclatu			047A	WB A4A1	CONVECTOR, HE	CONNECTOR RE	CONNECTOR, RF	PWB ASSY RCVR	WIRE,BUS	CABLE	CABLE, RF	SCREW,PH	RETAINER	EYELET	SCREW, THD FOR	INK	SOLDER	COVER, DIGITAL :	CHASSIS, RCVR				Nomenciatu		102A	er A4
ST	120PF-5-50	.10Fau-20-2	11 1580-20	- 101 00-20-	1UF80-20-	.1UF80-20-	1000PF-10-	180PF-3-50	.1UF80-20-	1UF80-20-	1000PF-10-	180PF-3-50	.1UF80-20-	.1UF80-20-	1000PF-10	47PF-5-500	.10+80-20-	.10100-20-	11500.20	11 JER0-20-	01UFR0-20	270PF-5-5(21PF-5-50	11PF-5-500	220PF-5-50	7 TO 25PF	.1UF80-20-	EEVING 22 WHT												BLACK		P/O A4			re Part Va									24	.085	WHITE	4-40X.312			IMING 4-40X.250	BLACK		SYNTHES RECIEVER					re Part Va			
	8		-200 C 089	C 088	25 0.08/	25 0 000	-100 0.000	0000	25 C 084	25 C 083	-100 C 082	00 000	25 C 080	·25 C 079	-100 C 078	C 077	25 C 076	C 075	C 074	.95 C 073	1-200 C 072	00	0 000	0 000	00 000	-200 C 068	-25 C.067	C 066	C 065	C 064	C 063	C 062	C 061	C 060	C 059	C 056	C 055	C 054	C 053	C 052	C 051	C 050	C 049	C 048	lue C 047	C 046	C 046	C 046	C 045	C 044	C 043	C 042	C 041	C 040	C 039	C 038	C 037	C 035	C 034	C 032	C 031	C 030	C 029	C 028			No.	lue Find			
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			21D82428B62	CV31D350	21C82372C09	21002372009		21002420002	010000000000000000000000000000000000000	CV31D350	21082372000	21082372000	23D84665E01	23D84665E01	23D84665F01	23D84665F01	21C82372C09	21C82372C09	21C82372C09	21D84426B21	21D84494B46	21082187806	21082187814	M39014/02-1342	41002107014	21082187814	M30014/02-1350	21C82372C09	21C82372C09	21D82428B10	21D82187B06	21D82428B62	21D82428B62	21D82428B62	21C82372C09	21C82372C09	21D82428B62	UK16-104	21C82372C09	UK16-104	21082372009	21D84426B51	CV31D350	21082372009	21K865452	CM04FC971.03		21084494812 CM04ED151.03	21084494806	21082428862	21D82428B62	21C82372C09	23D84665F01	21C82372C09	21C82372C10	21D84494B37	21D84494B30	21D84494B06	21D84494B15	21C82372C09	21C82372C09	21D84494B38	21D84494B24	21D84494B15				Part No.			
			CAPACITOR	CAPACITOR	CAPACITOR	CAPACITOR	CAFACILOR	CAPACITOR	CARACITOR	CAPACITOR	CAPACITOR	CABACITOR	CAPACITOR	CAPACITOR	CAPACITOR	CAPACITOR	CAPACITOR	CAPACITOR	CAPACITOR	CAPACITOR	CAPACITOR	CAPACITOR	CAPACITOR	CAPACITOR	CAFACITOR	CABACITOR		CAPACITOR	CAPACITOR	CAPACITOR	CAPACITOR	CAPACITOR	CAPACITOR	CAPACITOR	CAPACITOR	CAPACITOR	CAPACITOR	CAPACITOR	CAPACITOR	CAPACITOR	CAPACITOR	CAPACITOR	CAPACITOR	CAPACITOR	CAPACITOR	CAPACITOR	CAPACITOR	CAPACITOR	CAPACITOR	CAPACITOR	CAPACITOR	CAPACITOR	CAPACITOR	CAPACITOR	CAPACITOR	CAPACITOR	CAPACITOR	CAPACITOR	CAPACITOR	CAPACITOR	CAPACITOR	CAPACITOR	CAPACITOR	CAPACITOR				Nomenclature			
			.01UF80-20-2	9 TO 35PF-20	.1UF80-20-25	.1UF80-20-25	.1UF80-20-25	UTUF80-20-2	9 10 3047-20	0 TO 250-20	111500 00 05		10UE 25V		10115-254	10UE-25V	.1UF80-20-25	.1UF80-20-25	.1UF80-20-25	80PF-5-500	180PF-3-500	560PF-10-500	1000PF-10-10	.022UF-10-10	10004-10-10	.105-10-100	115 10 20 20	11 1580-20-25	11 JER0-20-25	3300PF-10-10	560PF-10-500	.01UF80-20-2	.01UF80-20-2	.01UF80-20-2	.1UF80-20-25	.1UF80-20-25	.01UF80-20-2	1115-20	11 JEB0-20-25	111E-20	111500-00-05	3 DE-2-200	9 TO 35PE-20	111580-20-25	270PF-5-300	180PF-5-500	150PF-5-500	220PF-5-500	120PF-5-500	.01UF80-20-2	.01UF80-20-2	.1UF80-20-25	10UF-25	.1UF80-20-25	.05UF-20-25	11PF-5-500 -	34PF-5-500	120PF-5-500	300PF-5-500	.1UF80-20-25	1UF80-20-25	15PF-5-500	39PF-5-500	300PF-5-500				Part Valu			
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			680UH	2200UH	470UH	560UH	4700UH	2.2UH	HUZH															.01UF80-20-200	.01UF80-20-200	10UF-25V	10UF-25V	10UF-25V	10UF-25V		10115-254	101 IE-25V	101 JE-25V	11 JF80-20-25	10UF-25V	01UF80-20-200	05116-20-20	.01UF80-20-200	.1UF80-20-25	.1UF80-20-25	.1UF80-20-25	330PF-5-500	.1UF80-20-25	.1UF80-20-25	2000PF-10-200	.01UF80-20-200	.1UF80-20-25	.1UF80-20-25	.01UF80-20-200	.01UF80-20-200	10UF-25V	.05UF-20-25	100UF-25WVDC	1UF80-20-25	1UF80-20-25	1000PF-10-100	3300PE-10-100	9 TO 35PE-200	1UF80-20-25	111F80-20-25	11 1520-20-20	111580-20-23	11 IE80-20-25	011 IER0-20-200				Part Value			
			R 026	R 025	R 024	R 023	R 022	R 020	R 019	R 018	R 017	R 016	R 015	R 014	R 013	H U12	0.019	R 011	R 010	R 009	R 008	R 007	R 006	R 005	R 004	R 003	R 002	R 001	Q 022		0 020	0.000	0 019	0.018	0.017	0.016	0 014	Q 013	Q 012	Q 011	Q 010	Q 009	Q 008	Q 007	Q 006	Q 005	Q 004	Q 003	Q 002	Q 001	L 022	L 021	L 020	L 019	L 018	L 016	1 015	1 014	L 013	1 012	- 011		- 000	800 1			No.	Find			
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			S124A65	S124A45	S124A77	S124A81 I	S124A25	S124A49	iS124A73 I	S124A85	S124A25	S124A75	S124A73	S124A73	S124A85	S124A25	10442100	S104467	S124A73	IS124A56	IS124A69	IS124A25 F	IS124A53 F	IS124A79 F	IS124B10 F	S124A89 F	S124B22	S124A25	8R00869571	0756980048	0-04302730		BD00860571	8000860570	BENNARO570	8-83837731	8R00869571	8R00869570	8R00869571	8R00869571	8R00869571	8R00869570	8R00869570	8R00869570	8R00869571	8R00869570	8-80345A42	8-80345A42	8-80345A42	8-80345A42	AS90539-07 (4S90539-07 (AS90539-15 (AS90539-15 0	AS90539-15 0	AS90539-15 (1390541-03	1090071-00	AS90541-03	1000071-00	101013U-0	100000-10	4000500 1E	1500530-11				Dart No.			
		1000.011	RESISTOR	RESISTOR	RESISTOR	RESISTOR	RESISTOR	RESISTOR	RESISTOR	RESISTOR	RESISTOR	RESISTOR	RESISTOR	RESISTOR	RESISTOR	RESISTOR		DESISTOD	RESISTOR	RESISTOR	RESISTOR	RESISTOR	RESISTOR	RESISTOR	RESISTOR	RESISTOR	RESISTOR	RESISTOR	TRANSISTOR	IRANSISTOR		TRANSISTOR	TRANSISTOR	TRANSISTOR	TRANSISTOR	TEANSISTOP	TRANSISTOR	TRANSISTOR	TRANSISTOR	TRANSISTOR	TRANSISTOR	TRANSISTOR	TRANSISTOR	TRANSISTOR	TRANSISTOR	TRANSISTOR	FRANSISTOR	TRANSISTOR	TRANSISTOR	TRANSISTOR	COIL	COIL	COIL															Nomenclature			
			4.7K-5-1/4	680-5-1/4	15K-5-1/4	22K-5-1/4	100-5-1/4	1K-5-1/4	10K-5-1/4	33K-5-1/4	100-5-1/4	12K-5-1/4	10K-5-1/4	10K-5-1/4	33K-5-1/4	100-5-1/4	0.07-0-1/4	5.6K-5-1/4	10K-5-1/4	2.0K-5-1/4	6.8K-5-1/4	100-5-1/4	1.5K-5-1/4	18K-5-1/4	330K-5-1/4	47K-5-1/4	1M-5-1/4	100-5-1/4																							470UH	470UH	1000UH	1000LIH	1000114	1000114		100011	4700LIH	100011	HUI		100011					Dart Value	0		
			R 092	R 091	R 090	R 089	R 088	R 087	H 086	CBUR	H 084	H 083	2000			B 080	R 079	R 077	R 076	R 075	R 074 S	R 074 S	R 074	R 073	R 072	H 071				E 068	B 067	R 065	R 062	R 061	R 060	R 059	R 058	B 067	056	H U54	R 053	H U52	H USI	R 050	R 049	R 048	R 047	R 046	R 045	R 044	R 043	R 042	R 041	R 040	R 039	R 038	R 037	R 036	R 035	R 034	R U43	R 031	R 030	R 029	R 027		No. Re	Find			
			6S124A25	18D83452	6S124A15	6S124A15	6S124A73	6S124A73	6S124A41	0S124A//	6S124A/5	6S124A59	0010801/5	00-106210		06083175	06D83175	6S124A66	6S124A66	06-106210	06-106210	06-106210	06-106210	06-106210	6S124A66	6S124A37	10/03452	00124400	10003432	10002124700	AC104465	6S124A73	6S124A89	6S124A89	18D83452	6S124A73	6S194473	10002460	65124A33	6S124A17	6S124A/3	65124A65	65124A41	6S124A97	6S124A73	6S124A85	6S124A73	6S124A73	6S124A57	6S124A73	6S124B02	06-106210	06-10621E	06-10621[06-106210	06-10621[6S124A97	06-106210	06-106218	06-10621	06-10621[6S124A25	RS104AR0	6S124A73	6S124A37		÷ ب	u Part No			
			RESISTOR	F04 RESISTOR,V	RESISTOR	RESISTOR	RESISTOR	RESISTOR	RESISTOR	RESISTOR	RESISTOR	RESISTOR	CU5 HESISI UH	J88 HESISTOR	Los RESISIUR		CB3 RESISTOR	RESISTOR	RESISTOR	040 RESISTOR	36 RESISTOR	24 RESISTOR	32 RESISTOR	148 RESISTOR	RESISTOR	RESISTOR	F12 HESISTOR,V	RESISTOR	FI4 RESISTOR,V		DESISTOR	RESISTOR	RESISTOR	RESISTOR	F14 RESISTOR.V	RESISTOR	RESISTOR	HESISTOR	RESISTOR	RESISTOR	RESISTOR	HESISTOR	HESISTOR	RESISTOR	RESISTOR	RESISTOR	RESISTOR	RESISTOR	RESISTOR	RESISTOR	RESISTOR	53 RESISTOR	17 RESISTOR	048 RESISTOR	HAR RESISTOR	188 RESISTOR	RESISTOR	B8 RESISTOR	05 RESISTOR	048 RESISTOR	RESISTOR	RESISTOR	RESISTOR	RESISTOR	RESISTOR			Nomenc			
			100-5-1	ARIABLE 100	39-5-1/	39-5-1/	10K-5-1	10K-5-1	470-5-1	15K-5-1	12K-5-1	2.7K-5-	11K-1-1	100K-1	5.11K-1	5.11V-1	5 11K-1	5.1K-5-	5.1K-5-	31.6K-1	28.7K-1	21.5K-1	26.1K-1	38.3K-1	5.1K-5-	330-5-1	AHIABLE 5K	2.48-5-	ANIABLE IUK	4./N-3-	174.6	10K-5-	47K-5-	47K-5	ARIABLE 10K	10K-5-		20K-5-	220-5-1	47-5-1/	10K-5-	4.7K-5-	470-5-1	100K-5	10K-5-	33K-5-	10K-5-	10K-5-	2.2K-5-	10K-5-	150K-5	4020-1-	196K-1	38.3K-1	38.3K-1	° 100K-1	100K-5	100K-1	147K-1	38 3K-	3R 3K-	100-5-1	3 AK-5	10K-5-	330-5-1			latura Part			
			/4		-	-	/4	/4	14	/4	/4	1/4	/4	1/8	-1/4	-1/4	-1/4	1/4	1/4	-1/8	-1/8	-1/8	-1/8 NOMINAL	-1/8	1/4	/4		1/4		1/4		14	14	14		/4	14	/4	4		/4	1/4	/4	1/4	/4	/4	/4	/4	1/4	/4	-1/4	1/8	-1/8	-1/8	-1/8	-1/8	-1/4	-1/8	-1/8	-1/8	1/0	14	1/4	/4	/4			Velna			
						R 149 1	R 148 1	R 147 1	R 146 1	H 145 1	H 144 1	R 143 1	H 142 1	H 141 1	H 139 1	1 1 30 1	R 138 1	R 137 1	R 136 1	R 135 1	R 134 1	R 133 1	R 132 1	R 131 1	R 130 1	R 129 1	H 128 1	H 12/ 1	1 200		1 1 2 2	R 124 1	R 123 S01	R 123 AR	R 122 1	R 121 1	H 120 SUI	H 120 1	H 119 . 1	R 118 1	R 117 1	R 116 1	R 115 1	R 114 1	R 113 1	R 112 1	R 111 1	R 110 1	R 109 1	R 108 1	R 107 1	R 106 1	R 105 1	R 104 1	R 103 1	R 102 1	R 101 1	R 100 1	R 099 1	B 008 1	1 1000	B 006 1	B 005 1	R 094 1	R 093 1		No. Req.	Find Otv.			
						6S124A73	6S124A73	6S124A97	6S124A73	6S124A45	6S124A6	6S124A6	6S124A6	6S124A8	65124A/U	074421C0	6512447	6S124A59	6S124A59	6S124A75	6S124A97	6S124A73	6S124A73	6S124A15	6S124A73	6S124A15	06-10621	6S124A4	03124A/	10000402	19092450	19083455	06-106211	_	6S124A25	18083452	06-106211	06-10621	18083452	6S124A55	06D83175	6S124A62	6S124A57	6S124A41	6S124A57	6S124A49	6S124A71	6S124A61	6S124A49	6S124A57	6S124A73	6S124A73	6S124A01	6S124A91	6S124A97	6S124B06	6S124A25	18082450	6S124A15	65194415	50104D1	60124430	BC104 A50	65194450	6S124A73	-		Part No	1		
						3 RESISTOR	B RESISTOR	RESISTOR	RESISTOR	HESISTON	RESISTOR	RESISTOR	HESISIOH	RESISTOR	RESISTOR		BESISTOR	RESISTOR	RESISTOR	RESISTOR	7 RESISTOR	B RESISTOR	3 RESISTOR	5 RESISTOR	B RESISTOR	RESISTOR	D36 HESISTOR	RESISTOR	RESISION			DE16 RESISTOR V	R94 RESISTOR	WIRE	RESISTOR	DE12 RESISTOR V		D28 RESISTOR	2FT0 HESISTOR,V	RESISTOR	C03 RESISTOR	RESISTOR	RESISTOR	RESISTOR	RESISTOR	RESISTOR	RESISTOR	RESISTOR	RESISTOR	RESISTOR	B RESISTOR	RESISTOR	RESISTOR	RESISTOR	RESISTOR	RESISTOR	A RESISTOR	PENA RESISTOR V	RESISTOR	BESISTOR	DECICTOR	DECICTOR	DESISTOR	RESISTOR	RESISTOR			Nomenc			
						10K-5-	10K-5-	100K-5	10K-5-	1K-5-1	3.3K-5-	4.7K-5-	5.1K-5	27K-5-	1.5K-5	7.3 7- 5-	7.55-5-	2.7K-5-	2.7K-5	12K-5-	100K-5	10K-5-	10K-5-	39-5-1/	10K-5-	39-5-1/	28./K-	1K-5-1	-c-NUL			ARIARI F 20K	1000-1	24 NO	100-5-	ARIARI F 5K	28 7K-	23.7K-	AHIABLE 1K	1.8K-5-	10K-1-	3.6K-5-	2.2K-5-	470-5-	2.2K-5-	1K-5-1	8.2K-5-	3.3K-5-	1K-5-1.	2.2K-5-	10K-5-	10K-5-	10-5-1/	56K-5-	100K-5	220K-5	100-5-1		39-5-1/	30-5-1	1017-0-	2./ 104-5-	0 TK 5	0 7K-5-	10K-5-			Inture Dari			
						1/4	1/4	5-1/4	1/4	/4	-1/4	-1/4	-1/4	1/4	-1/4	-1/4	-1/4	-1/4	-1/4	1/4	5-1/4	1/4	1/4	/4	1/4	14	1-1/8	/4	1/4				-1/8	MINAI	1/4		1-1/8	1-1/8 NOMINAL		-1/4	1/4	-1/4	-1/4	1/4	-1/4	/4	-1/4	-1/4	/4	-1/4	1/4	1/4	4	1/4	-1/4	-1/4	1/4	1	4		1/4	- 1/4		- 1/4	1/4		t value	4 Valua			
													VHUID	VHU14	VIDII	VENT	VR010	U 028	U 027	U 026	U 024	U 023	U 022	U 021	U 020	019					11015	11014	11 013	11 012	U 011	01010		U 007	0006	U 005	U 004	003	U 002	U 001	R 171	R 170	R 169	R 168	R 167	R 166	R 165	R 164	R 163	R 162	R 161	R 160	R 159	B 158	R 157	B 156		B 154	0 153	B 153	R 151		No. Re	Ci>1			
													48-8.	48-8-	40-0	40-0	48-82	1 51-80	1 51-80	1 51-80	1 51-84	1 51-84	1 51R8	1 51R8	1 51-8L	1 51-8.	70-1C	51-00	51-00		51_84	51-R4	51-84	51-80	51-80	51-80	51-80	51-8-	51-8	1 51-8	1 51-8-	1 51R6	1 51-8-	1 51-84	1 6S12	1 6S12	1 6S18.	1 6S18	1 6S18.	1 6S12	1 6S12	1 6S12	1 6S124	. 6S124	6S124	6S124	65124	AS124	6S124	AS134	ne-10	1 00.10	6012	82124	6S124		i, i,				
													V2556C15A DIOL	14302A09 DIOL	NUS45A94A DIUL		2556C15A DIOD	1345A18 INTEC	1345A04 INTEL	0345A97 AMPL	4887K10 INTEL	4887K10 INTEL	34320A80 INTEL	94320A80 INTEL	10345A09 INTEL	14928F02 INTE	NU34DAZ3 INTE	NUS45A23 INTE	10345A04 INTE	MODINIO INTEC	19971/01/01 INTEC	VIIBKOI INTEG	VAR7K54 INTEG	1345A04 INTEC	1345A10 INTEG	1345A02 INTEG	101-5 INTER	14887K73 INTE	10345AU4 INTE	14887K73 INTE	14887K73 INTE	84887K09 INTE	14887K73 INTE	14887K73 INTE	24A37 RESIL	24A37 RESIL	35A59 RESIL	15A59 RESIL	15A97 RESIL	14A61 RESIL	14A51 RESIS	V4A25 RESIS	4A25 RESIS	4A39 RESIS	4A25 RESIS	4A25 RESIS	4A73 RESIS	4473 BESIS	4A61 RESIS	4440 BESIS				4A73 BESIS	4A41 RESIS		NO.	Nor			
													JE,ZENEH	JE,ZENEH	DE,ZENEH	C TENED	F ZENER	GRATED CIRCUIT	GRATED CIRCUIT	IFIER	GRATED CIRCUIT	GHATED CINCUT					SRATED CIRCUIT	GRATED CIRCUIT	GRATED CIRCUIT	GRATED CIRCUIT	SRATED CIRCUIT		GRATED CIRCUIT	GHATED CINCUIT	GRATED CIRCUIT	STOR	STOR	STOR	STOR	STOR	STOR	STOR	STOR	STOR	TOR	TOR	TOR	TOR	TOP	STOR	TOP			TOP	TOP	TOR															
													5.1V-54	6.2V-54	10V-04	J. IV-D4	5 1V-5- 4																												330-5-1/4	330-5-1/4	2.7K-5-1/8	2.7K-5-1/8	100K-5-1/8	3.3K-5-1/4	1.2K-5-1/4	100-5-1/4	100-5-1/4	390-5-1/4	100-5-1/4	100-5-1/4	10K-5-1/4	10K-5-1/4	3.3K-5-1/4	16-5-1/4	1.0N-1-1/0	207-0-1/4	DOK E 1/4	1016-5-1/4	470-5-1/4		Part Value	Paul Value			

Receiver

SECTION 11

RF SYNTHESIZER (A5)

11-1. General. The RF Synthesizer provides an RF signal source for the frequency range from 10kHzto1 GHz in 100 Hz steps. The output frequency is programmed by the processor through the RF control bus and is phase locked to the 10 MHz frequency standard. A reference divider in the module produces outputs of 500 kHz, 50 kHz, 5 kHz, 1 kHz, 100 Hz, and 50 Hz (SYNTH SWP SYNC) each having the same accuracy as the frequency standard. A block diagram of the RF Synthesizer is shown in figure 11-1 and its schematic is shown in figure 11-3.

11-2. Frequency Synthesis Scheme. Four phase locked loops are used to generate the output frequency; a 60.5 MHz loop, a 310-440 MHz loop, the 500 MHz-1000 MHz loop, and the 550 MHz loop. Two of these loops contain programmable dividers, controlled by the microprocessor for varying the frequency. The 310-440 MHz loop is controlled by the four most significant digits of the required frequency and operates in discrete 50 kHz loop is control led by the three least significant digits of the required frequency and operates in discrete 50 Hz increments.

11-3. The output is derived from three sources, covering the ranges of 10 kHz to 250 MHz, 250 MHz to 500 MHz, and 500 MHz to 1000 MHz. In the first range, 10 kHz to 250 MHz, the output is derived by mixing the fixed 550 MHz signal with 500-1000 MHz signal programmed for frequencies from 550.01 MHz to 800 MHz. For the second range, 250 to 500 MHz, the output is a divide by two of the 500-1000 MHz signal. The final range is the 500-1000 MHz signal directly. The appropriate frequency source is switched to the SYNTH RF output by the Output Select switch.

11-4. A basic flow diagram for programming the RF Synthesizer is shown in figure 11-2. This diagram includes generate and monitor considerations, wideband amplifier control, and modulation control.

11-5. 310-440 MHz Phase Locked Loop. A single 310-440 MHz VCO is phase locked to the 100 kHz reference input using a straight forward loop. The VCO output is divided down to 50 kHz using a programmable two modulus prescalerand divider. Programming of the divider is controlled by the processor to give output frequencies from 310 to 440 MHz in 50 kHz steps.

11-6. 60.5 MHz Phase Locked Loop. The 60.5 MHz loop is programmable over a \pm 100 kHz range in 50 Hz increments. The 60.5 MHz VCO output is mixed with a 50 MHz signal from the 550 MHz loop. A programmable divider following the mixer divides the 10.5 MHz \pm 100 kHz signal down to the 50 Hz reference frequency. A comparison between the divider output and the reference signal by the Phase/Frequency detector results in an error voltage to the VCO which maintains the phase lock.

11-7. 550 MHz Phase Locked Loop. A fixed frequency of 550 MHz is obtained by dividing the 550 MHz VCO by 55 to obtain 10 MHz. The 10 MHz from the divider is compared with the 10 MHz frequency standard in the Phase/Frequency Detector. The resulting error signal is filtered and used to correct the 550 MHz VCO to maintain it in lock.

11-8. A Voltage Controlled Attenuator (VCA) follows the 550 MHz output to level the generator output for frequencies below 1 MHz. The leveling loop in the RF Input module provides the ALC VOLT control signal to maintain the required output level at the front panel RF jack. See paragraph 5-31 for a further description of output leveling.

11-9. 500-1000 MHz Phase Locked Loop. The 500-1000 MHz output is locked to either the sum or the difference of the 310-440 MHz and 60.5 MHz loop output frequencies. In the locked condition, mixing the divide by two output of the 500-1000 MHz VCO's with the 310-440 MHz signal gives a difference frequency equal to the 60.5 MHz output. There are two frequencies at the divide by two output, the 310-440 MHz frequency plus or minus the 60.5 frequency, which will mix down to the correct frequency. However, the sense of the loop is inverted for one compared to the other. Thus the phase switch following the Phase/Frequency Detector determines at which frequency the loop will lock.

11-10. Modulation Control. Modulation of the tuning voltage for the 60.5 MHz VCO provides the frequency modulation of the RF output. Since the modulation sensitivity changes by a factor of two when the 250-500 MHz source is selected, the modulation control provides programmable gain control to maintain constant sensitivity at the FM MOD and SWEEP inputs. Additionally, the wideband modulation mode requires a gain of four beyond that for the narrowband mode. Thus under the control of the processor the Modulation Control selects between the SWEEP and FM MOD inputs, provides gains of 1, 2, 4, and 8 for the FM MOD input and gains of 1 and 2 for the SWEEP input. Input modulation sensitivities are 5 kHz/volt and 20 kHz/volt for narrow and wideband FM input and 2 MHz/volt for the sweep input.

11-11. Module Control. Control information is latched in four bit control latches which are loaded by the processor through the RF control bus. The four bit RF ADD BUS 0-3 is decoded by the Address Decoder to determine which Control Latch the four bit RF DATA BUS 0-3 is to be stored. Synchronization of the data transfer is the function of the RF BUS EN line. Two decoded address outputs, RF LATCH 13 and 14, select latches on the receiver module for receiver control. One control latch output, LO/HI BAND SEL, goes to the RF Input module to control the frequency range of the output amplifer.







11-7,11-8







NOTES:

L PARTIAL REFERENCE DESIGNATIONS ARE SHOWN FOR COMPLETE DESIGNATION PREFIX WITH IAS.



Figure 11-8. RF Synthesizer A5A2 Parts Location





(RTC-4010A) Schematic Diagram (Sheet 2 of 3)











	_	RF Synthesize RTC-1	yr Module A5 001A																
Find No.	Oty. Req.	Part No.	Nomenclature	Part Value	Find No.	Qty. Req.	Part Number	Nomenclature	Part Value	Find No.	Qty. Req.	Part Number	Nomenclature	Part \	/alue	/alue Find No.	falue Find Qty. No. Req.	alue Find Oty. Part Number No.: Req.	ralue Find Qty. Part Number Nomenclature No. Req.
01	-	27-80335A34	CHASSIS, SYNTHESIZERA	2	C 104	-	21D82187B14	CAPACITOR	1000PF-10-100	L 101	- - -	VK200-10/3B	COIL			R 153	R 153 1	R 153 1 6S124A23	R 153 1 6S124A23 RESISTOR
002	⊾ _	15-80335A37 50:84500B03	COVER,DIGITAL SYNTHE	S RECIEVER	C 105		21D82187B14	CAPACITOR	1000PF-10-100	L 102	• -•	VK200-10/3B	COIL			R 154	R 154 1	R 154 1 6S124A77	R 154 1 6S124A77 RESISTOR
004	4	42C84284B01	RETAINER		C 107		21C82372C10	CAPACITOR	.05UF-20-25	C 101		WEE-12000 48-84308A92	TRANSISTOR	12MH		R 155	R 155 1	R 155 1 6S124A77	R 155 1 6S124A77 RESISTOR
005	28	3-134212	SCREW, THD FORMING	4-40X.312	C 108	_	23D84665F01	CAPACITOR	10UF-25V	Q 102	_ _	48R00869570	TRANSISTOR			R 157	R 157 1	R 157 1 6S124A09	R 157 1 6S124A69 RESISTOR
2006 006	AR	CALCOMODIANDS	WIRE	24 WHT	C 109		21D82428B62	CAPACITOR	.01UF80-20-200	Q 103	-	48R00869571	TRANSISTOR			R 158	R 158 1	R 158 1 6S124A49	R 158 1 6S124A49 RESISTOR
600	AR	SN63WHMAP3 11-14167A01	INK	BLACK	C 110		MMJ-035-106R-20	CAPACITOR	10UF-20-35	Q 104	•	48R00869570	TRANSISTOR			R 159	R 159 1	R 159 1 6S124A49	R 159 1 6S124A49 RESISTOR
010	-	15-80335A36	COVER, RF SYNTHESIZER		C 112		21D82428B62	CAPACITOR	01UF80-20-200	Q 100		48-84308A92 48-869044A	TRANSISTOR			R 160	R 160 1	R 160 1 6S124A73	R 160 1 65124A73 RESISTOR
011	-	26-P00211N001	SHIELD		C 113	-	21D82428B62	CAPACITOR	.01UF80-20-200	Q 107	1	48R00869570	TRANSISTOR			R 162	R 162 1	R 162 1 6S124A49	R 162 1 6S124A49 HESISTOH
012	-	64-P00230N001	PLATE, CONNECTOR		C 114	-	21D82428B62	CAPACITOR	.01UF80-20-200	Q 108	1	48-2089C01	TRANSISTOR			R 163	R 163 1	R 163 1 6S124A73	R 163 1 6S124A73 RESISTOR
013	4	MS35206-214	SCREW,PH	4-40X.312	C 115	-	21D84494B37	CAPACITOR	11PF-5-500	R 101		6S124A73	RESISTOR	10	K-5-1/4	K-5-1/4 R 164	K-5-1/4 R 164 1	K-5-1/4 R 164 1 6S124A65	K-5-1/4 R 164 1 6S124A65 RESISTOR
014	10 38	MS35338-40	SPACER M/F 4-40	NO.4	C 116		21D84494B37	CAPACITOR	11PF-5-500	R 102		6S124A49	RESISTOR		1K-5-1/4	1K-5-1/4 R 165	1K-5-1/4 R 165 1	1K-5-1/4 R 165 1 6S124A41	1K-5-1/4 R 165 1 6S124A41 RESISTOR
016	AR	30-84421F13	CABLE,RF4	WHITE	C 117		21D84494B24 21D84494B24	CAPACITOR	39PF-5-500	R 104	- - -	6S124A73	RESISTOR		10K-5-1/4	10K-5-1/4 R 166	10K-5-1/4 R 166 1	10K-5-1/4 R 166 1 6S124A56	10K-5-1/4 R 166 1 6S124A56 RESISTOR
017	19	MS35206-213	SCREW,PH	4-40X.250	C 119		21D82428B10	CAPACITOR	3300PF-10-100	R 105	_ . .	6S124A73	RESISTOR		10K-5-1/4	10K-5-1/4 R 167	10K-5-1/4 B 168 1	10K-5-1/4 D 168 1 62124A41	10K-5-1/4 B 168 1 65124A41 RESISTOR
018	AR	30-15068A34	CABLE,RF	WHITE	C 120	-	21D82428B10	CAPACITOR	3300PF-10-100	R 106	1	6S124A73	RESISTOR		10K-5-1/4	10K-5-1/4 R 169	10K-5-1/4 R 169 1	10K-5-1/4 F 169 1 6S125A13	10K-5-1/4 R 169 1 6S125A13 RESISTOR
019	AR	30-15068A29	CABLE, RF, SGLE SHLD	WHITE	C 121	-	21D82428B62	CAPACITOR	.01UF80-20-200	R 107	1	6S124B16	RESISTOR		560K-5-1/4	560K-5-1/4 R 170	560K-5-1/4 R 170 1	560K-5-1/4 R 170 1 6S124A47	560K-5-1/4 R 170 1 65124A47 RESISTOR
020	7	03-134169	SCREW, THD FORMING	4-40X.250	C 122	-	21C82372C09	CAPACITOR	.1UF80-20-25	R 108		6S124A49	RESISTOR		1K-5-1/4	1K-5-1/4 R 171	1K-5-1/4 R 171 1	1K-5-1/4 R 171 1 ' 6S125A43	1K-5-1/4 R 171 1 6S125A43 RESISTOR
022	AR -	29-14070A91	WIRE	24	C 123		21D82187B07	CAPACITOR	470PF-10-500	R 109		6S124A53 6S124A90	RESISTOR		1.5K-5-1/4	1.5K-5-1/4 R 172	1.5K-5-1/4 R 172 1	1.5K-5-1/4 R 172 1 6S124A63	1.5K-5-1/4 R 172 1 6S124A63 RESISTOR
A 001	-	RTC-4009A	DIGITAL SYNTHESIZER A		C 125		21D82187B14	CAPACITOR	1000PF-10-100	R 111		6S124A56	RESISTOR		2.0K-5-1/4	2.0K-5-1/4 H 101	2.0K-5-1/4 H 101 1	2.0K-5-1/4 H 101 1 51-80221AR0	2.0K-5-1/4 II 101 1 51-80321ARA INTEGRATED CIRCUIT
A 002	-	RTC-4010A	RF SYNTHESIZER ASSY A		C 126	-	21D82187B14	CAPACITOR	1000PF-10-100	R 112	-	6S124A56	RESISTOR		2.0K-5-1/4	2.0K-5-1/4 U 102	2.0K-5-1/4 U 102 1	2.0K-5-1/4 U 102 1 51-80345A13	2.0K-5-1/4 U 102 1 51-80345A13 INTEGRATED CIRCUIT
C 001		21C82543H03	CAPACITOR	5000PF80-20-500	C 127	-	23-82397D04	CAPACITOR	15UF-20-15	R 113	-	6S124A56	RESISTOR		2.0K-5-1/4	2.0K-5-1/4 U 103	2.0K-5-1/4 U 103 1	2.0K-5-1/4 U 103 1 51-84887K73	2.0K-5-1/4 U 103 1 51-84887K73 INTEGRATED CIRCUIT
003		21C82543H03	CAPACITOR	5000PF80-20-500	C 128		M39014/02-1326	CAPACITOR	1011E 20 25	R 114		6S125A45	RESISTOR		680-5-1/2	680-5-1/2 U 104	680-5-1/2 U 104 1	680-5-1/2 U 104 1 51-80345A04	680-5-1/2 U 104 1 51-80345A04 INTEGRATED CIRCUIT
004	-	21C82543H03	CAPACITOR	5000PF80-20-500	C 130		21D82428B62	CAPACITOR	01UF80-20-200	R 116		6S124A41	RESISTOR		2.0K-5-1/4 470-5-1/4	470-5-1/4 U 105	470-5-1/4 U 105 1	470-5-1/4 U 105 1 51-80345A15	470-5-1/4 U 105 1 51-80345A15 INTEGRATED CIRCUIT
005	-	21C82543H03	CAPACITOR	5000PF80-20-500	C 131	-	M39014/01-1575	CAPACITOR	01UF-10-100	R 117	-	6S124A56	RESISTOR		2.0K-5-1/4	2.0K-5-1/4 U 107	2.0K-5-1/4 U 107 1	2.0K-5-1/4 U 107 1 51-80345A01	2.0K-5-1/4 U 107 1 51-80345A01 INTEGRATED CIRCUIT
007		21C82543H03 21C82543H03	CAPACITOR	5000PF80-20-500	C 132		21D82187B14	CAPACITOR	1000PF-10-100	R 118		6S124A41	RESISTOR		470-5-1/4	470-5-1/4 U 108	470-5-1/4 U 108 1	470-5-1/4 U 108 1 51-84887K73	470-5-1/4 U 108 1 51-84887K73 INTEGRATED CIRCUIT
008	-	21C82543H03	CAPACITOR	5000PF80-20-500	C 134		21D82187B14	CAPACITOR	1000PE-10-100	R 120	<u> </u>	6S124A41	RESISTOR		470-5-1/4	470-5-1/4 U 110	470-5-1/4 U 110 1	470-5-1/4 U 109 1 51-83819K43	470-5-1/4 U 110 1 51-83819K43 INTEGRATED CIRCUIT
009	. <u> </u>	21C82543H03	CAPACITOR	5000PF80-20-500	C 135	1	21D82187B14	CAPACITOR	1000PF-10-100	R 121	-	6S124B19	RESISTOR		750K-5-1/4	750K-5-1/4 U 111	750K-5-1/4 U 111 1	750K-5-1/4 U 111 1 51-84118K77	750K-5-1/4 U 111 1 51-84118K77 INTEGRATED CIRCUIT
010		21C82543H03	CAPACITOR	5000PF80-20-500	C 136	•	21D82187B14	CAPACITOR	1000PF-10-100	R 122		18D83452F19	RESISTOR, VARIABLE		100K	100K U 112	100K U 112 1	100K U 112 1 51-84887K10	100K U 112 1 51-84887K10 INTEGRATED CIRCUIT
C 012	<u> </u>	21C82543H03	CAPACITOR	5000PF80-20-500	C 138		21D82187814 23D84665F01	CAPACITOR	1000PF-10-100	R 125		6S124A49	RESISTOR		1K-5-1/4	1K-5-1/4 U 113	1K-5-1/4 U 113 1	1K-5-1/4 U 113 1 51-83819K43	1K-5-1/4 U 113 1 51-83819K43 INTEGRATED CIRCUIT
C 014	-	21C82543H03	CAPACITOR	5000PF80-20-500	C 139	<u> </u>	21D82187B14	CAPACITOR	1000PF-10-100	R 126	-	6S124A49	RESISTOR		1K-5-1/4	1K-5-1/4 U 115	1K-5-1/4 U 115 1	1K-5-1/4 U 115 1 51-84887K73	1K-5-1/4 U 115 1 51-84887K73 INTEGRATED CIRCUIT
C 015		21C82543Hu3 21C82543Hu3		5000PF80-20-500	C 140	-	23D84665F01	CAPACITOR	10UF-25V	R 127		6S124A41	RESISTOR		470-5-1/4	470-5-1/4 U 116	470-5-1/4 U 116 1	470-5-1/4 U 116 1 51-80345A01	470-5-1/4 U 116 1 51-80345A01 INTEGRATED CIRCUIT
C 017		21C82543H03	CAPACITOR	5000PF80-20-500	C 141		21D82187B14	CAPACITOR	1000PF-10-100	H 128		6S124A41 6C194A53	RESISTOR		470-5-1/4 1 5K-5-1/4	1.5K-5-1/4 U 117	1.5K-5-1/4 U 117 1	15K-5-1/4 U 117 1 51-80345A02	4/0-5-1/4 U 117 1 51-80345A02 INTEGRATED CIRCUIT
C 018	-	21C82543H03	CAPACITOR	5000P.F80-20-500	C 143		MMJ-U35-106H-20 21D82187B04	CAPACITOR	270PF-10-500	R 130		6S124A53	RESISTOR		1.5K-5-1/4	1.5K-5-1/4 U 119	1.5K-5-1/4 U 119 1	1.5X-5-1/4 U 119 1 51-80345A02	1.5K-5-1/4 U 119 1 51-80345A02 INTEGRATED CIRCUIT
FL001		91-80346A11	FILTER, RF-20-500	1250-003	C 144	-	VJ0805A330JF	CAPACITOR	33PF-5-100	R 131	-	6S124A73	RESISTOR		10K-5-1/4	10K-5-1/4 U 120	10K-5-1/4 U 120 1	10K-5-1/4 U 120 1 51-84118K77	10K-5-1/4 U 120 1 51-84118K77 INTEGRATED CIRCUIT
J 001		5658-5007-00	CONNECTOR, COAX RF	1250-003	C 151		23D83441B15	CAPACITOR	1.0UF-20-35	R 132		6S124A53	RESISTOR		1.5K-5-1/4	1.5K-5-1/4 U 121	1.5K-5-1/4 U 121 1	1.5K-5-1/4 U 121 1 51-84118K77	1.5K-5-1/4 U 121 1 51-84118K77 INTEGRATED CIRCUIT
J 002	-	5634-5015-00	CONNECTOR, RF		C 158	_	23D84665F01	CAPACITOR	10UF-25	R 134	_	6S124A39	RESISTOR		390-5-1/4	390-5-1/4 U 123	390-5-1/4 U 123 1	390-5-1/4 U 123 1 51-84887K10	390-5-1/4 U 123 1 51-84887K10 INTEGRATED CIRCUIT
L 001	-	MS90538-12	COIL	100UH	C 161	-	23D84665F01	CAPACITOR	10UF-25V	R 135		6S124A56	RESISTOR		2.0K-5-1/4	2.0K-5-1/4 U 124	2.0K-5-1/4 U 124 1	2.0K-5-1/4 U 124 1 51-84118K77	2.0K-5-1/4 U 124 1 51-84118K77 INTEGRATED CIRCUIT
	Dig	gital Synthesiz	zer Card A5A1		C 162		21D82187B14 21D82187B14	CAPACITOR	1000PF-10-100	R 137		6S124A29	RESISTOR		2./R-5-1/4 100-5-1/4	2./K-5-1/4 U 125	2./K-5-1/4 U 125 1	2./K-5-1/4 U 125 1 51-84118K77	2./K-3-1/4 U 125 1 51-84118K77 INTEGRATED CIRCUIT
		RTC-40	A600		C 164	- - -	21D82187B14	CAPACITOR	1000PF-10-100	R 138	<u> </u>	6S124A61	RESISTOR		3.3K-5-1/4	3.3K-5-1/4 U 127	3.3K-5-1/4 U 127 1	3.3K-5-1/4 U 127 1 51-84118K77	33K-5-1/4 U 127 1 51-84118K77 INTEGRATED CIRCUIT
					C 165	-	21D82187B14	CAPACITOR	1000PF-10-100	R 139		6S124A71	RESISTOR		8.2K-5-1/4	8.2K-5-1/4 U 128	8.2K-5-1/4 U 128 1	8.2K-5-1/4 U 128 1 51-848877K55	8.2K-5-1/4 U 128 1 51-848877K55 INTEGRATED CIRCUIT
Find	Qty.	Part Number	Nomenclature	Part Value	C 166	- -	21D82187B14	CAPACITOR .	1000PF-10-100	R 141		6S124A39 6S124A49	RESISTOR		390-5-1/4 1K-5-1/4	1K-5-1/4 U 129	1K-5-1/4 U 129 1	390-5-1/4 U 129 1 51-84118K20	390-5-1/4 U 129 1 51-84118K20 INTEGRATED CIRCUIT
No.	Req.				C 168	- -	21D82187B14	CAPACITOR	1000PF-10-100	R 142	-	6S185A18	RESISTOR		51-5-1/8	51-5-1/8 U 131	51-5-1/8 U 130 1	51-5-1/8 U 131 1 51-84118K20	51-5-1/8 U 131 1 51-84118K20 INTEGRATED CIRCUIT
					C 169	-	TE-1211	CAPACITOR	100UF-25V	R 143		6S124A49	RESISTOR		1K-5-1/4	1K-5-1/4 U 132	1K-5-1/4 U 132 1	1K-5-1/4 U 132 1 51-84118K01	1K-5-1/4 U 132 1 51-84118K01 INTEGRATED CIRCUIT
001		84-P00359N001	PWB, DIGITAL SYNTHES	-	C 170		TE-1211	CAPACITOR	100UF-25V	R 144		6S124A41 6S124A41	RESISTOR		470-5-1/4	470-5-1/4 U 133	470-5-1/4 U 133 1	470-5-1/4 U 133 1 51-84118K01	470-5-1/4 U 133 1 51-84118K01 INTEGRATED CIRCUIT
002	AR	3N63WHMAP3	INK	BLACK	C 172		23D84665F01	CAPACITOR	10UF-25V	R 146	<u> </u>	6S124A51	RESISTOR		1.2K-5-1/4	1.2K-5-1/4 U 134	1.2K-5-1/4 U 134 1	12K-5-1/4 U 134 1 51-84887K23	1/0-5-1/4 U 134 1 51-64887K23 INTEGRATED CIRCUIT
004	- 3	MS35206-214	SCREW,PH	4-40X.312	CR101		48D84616A01	DIODE	0104-10-100	R 147	<u> </u>	6S124A07	RESISTOR		18-5-1/4	18-5-1/4 U 136	18-5-1/4 U 136 1	18-5-1/4 U 136 1 51-84887K72	18-5-1/4 U 136 1 51-84887K72 INTEGRATED CIRCUIT
005	2	MS27183-3	WASHER, FLAT	NO.4	CR102	-	48-84463K02	DIODE		R 148	-	6S124A53	RESISTOR		1.5K-5-1/4	1.5K-5-1/4 U 137	1.5K-5-1/4 U 137 1	1.5K-5-1/4 U 137 1 51-84887K10	1.5K-5-1/4 U 137 1 51-84887K10 INTEGRATED CIRCUIT
6	-	MS35338-40	WASHER,LOCK	NO.4	CR103	-	48-84463K02	DIODE		R 149	_	6S124A35	RESISTOR		270-5-1/4	270-5-1/4 U 138	270-5-1/4 U 138 1	270-5-1/4 U 138 1 51-84887K10	270-5-1/4 U 138 1 51-84887K10 INTEGRATED CIRCUIT
	; -	MS35649-242	NUT,HEX	4-40	CR104		48-84463K02	DIODE		R 150		6S124A41	RESISTOR		470-5-1/4	470-5-1/4 U 139	470-5-1/4 U 139 1	470-5-1/4 U 139 1 51-84887K10	470-5-1/4 U 139 1 51-84887K10 INTEGRATED CIRCUIT
	AR AR		WIRE	22 WH1	CR105		48-86850C47 48-84463K02	DIODE		R 152	- - -	6S124A41	RESISTOR		470-5-1/4 2.2K-5-1/4	2.2K-5-1/4 U 140	470-5-1/4 U 140 1 2.2K-5-1/4 U 141 1	470-5-1/4 U 140 1 51-84887K10	470-5-1/4 U 140 1 51-84887K10 INTEGRATED CIRCUIT
	-	TE-1158	CAPACITOR	30UF-1075-16													0		
		21D82187B14	CAPACITOR	1000PF-10-100															ACCEMPIN DADTO LICT
	-	21082187814	CAPACITOR	10001-10-100															
																		RF Synthesiz	RF Synthesizer
																		(Sheet 1)	(Sheet 1)

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48-83193A59 48-83193A59 48-82286C50 51-80345A25 RF Synthesizer	Part Number		84-P00461N001	SN63WRMAP3	26-P00210N001	26-P00210N002	26-P00210N003	26-P00210N005	26-P00210N006	26-P00210N007	26-P00240N001	11-P14459A002	M23053/5-103-9 M23053/5-205-C			RTV3145	21D82187B14	21D82187B14	21D82428B62 21D82428B62	21D82428B62	21D82428B62	21D82428B62	21D82187B08	21D82187B08		21D84494B04	21D84494B04 21D84494B04	21D84494B04 21D84494B04 21D82428B62	21D84494B04 21D84494B04 21D82428B62 23D84665F01	21D84494604 21D84494604 21D82428662 23D84665F01 VJ0805X101MF VJ0805A2201F	21084494604 21084494604 21082428662 23084665601 VJ0805X101MF VJ0805X101MF VJ0805A20JF	21084494604 21084494604 2108448662 23084665F01 VJ0805A220JF 21082187814 21082187814	21D94494694 21D944948962 23D94469561 VJ0865X101MF VJ0865X220JF 21D82187814 VJ0865X220JF VJ0865X220JF	21194494804 21198494804 211984948062 21098458601 V.0805A20.JF V.0805A20.JF V.0805A20.JF V.0805A20.JF V.0805X222M	21194494804 211964948062 21092428062 23094665F01 V.06605X101MF 21092167614 V.06605X222MF V.06605X222MF V.06605X220JF	21104494604 211084494604 21094494604 21094294605F01 VJ0805X101MF VJ0805X20JF VJ0805X20JF VJ0805X220JF VJ0805X220JF VJ0805X20JF VJ0805X20JF	2:1094494804 2:1094494804 2:1094494804 2:1094494806 V.08605X101MF V.08605X220JF V.08605X220JF V.08605X220JF V.08605X220JF V.08605X220JF V.08605X102MF	21194494804 21198494804 211984948062 210984865F01 V.0865K20.JF V.0865K20.JF V.0865K220.JF V.0865K220.JF V.0865X220JF V.0865X220JF V.0865X101MF V.0865K102MF
DIODE,ZENERCIRCUIT DIODE,ZENER DIODE,ZENER INTEGRATED CIRCUIT r Card A5A2	r Nomenclature		PWB, RF SYNTHESIZER	SOLDER	SHIFLD	SHIELD	SHIELD	SHIELD		SHIELD,CAN	SHIELD,CAN	ADHESIVE	INSULATION SLEEVING	INSULATION SLEEVING	INSULATION TAPE	ADHESIVE	WIRE, TEFLON CAPACITOR	CAPACITOR	CAPACITOR	CAPACITOR	CAPACITOR	CAPACITOR	CAPACITOR	CAPACITOR	CAPACITOR	CAPACITOR	CAPACITOR	CAPACITOR	CAPACITOR			CAPACITOR	CAPACITOR	CAPACITOR CAPACITOR CAPACITOR	CAPACITOR CAPACITOR CAPACITOR CAPACITOR	CAPACITOR CAPACITOR CAPACITOR CAPACITOR CAPACITOR	CAPACITOR CAPACITOR CAPACITOR CAPACITOR CAPACITOR CAPACITOR	CAPACITOR CAPACITOR CAPACITOR CAPACITOR CAPACITOR CAPACITOR CAPACITOR
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C 362 C 363 C 364 C 365 C 365 C 365 C 365	C 369 C 370 C 371 C 372	C 373	C 374	C 375	C 376	C 378	C 379	C 381	C 382	C 384	C 385	C 387	C 388	C 390	C 391	C 393	C 394	C 396	C 397	CBONI	CR202	CR203	CR204 CR205	CR206	CR207	CR208	CR209	CR210	CR211	CR212	CH214	CH215	CH216	CH21/	01210	0000	CR219	CR219 CR220
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RF Synthesizer (Sheet 2)

ASSEMBLY PARTS LIST

L 240		L 247	L 246	L 245	L 244	L 243	L 242	L 241	L 240	L 239	L 238	L 237	L 236	L 235	L 234	L 233	L 232	L 231	L 230	L 229	L 228	L 227	L 226	L 225	L 224	L 222	1 221	L 219	L 218	L 217	L 216	L 215	L 214	L 213	L 211	L 210	L 200	L 207	L 206	L 205	L 204	L 203	L 202	L 201	K 201	CH242	CR241	CR236	CR235	CR233	CR230	CR229	CB227	CR225	CR224	CR223	CR222	CR221	No.	Find (
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1075000 E	MS75084-16	MS90538-12	MS75083-1	MS75083-1	MS75083-1	MS90538-12	MS75083-1	WEE-12000	24C83961B01	MS75083-3	MS75083-3	74-15169A01	MS75083-5	MS90538-12	MS90538-12	MS75084-12	DD-0.47-1	WEE-12000	WEE-12000	MS75083-7	MS75083-9	MS75083-1	MS75083-1	MS75083-7	MS75083-5	24-P00338N004	24-P00338N004	24-P00338N001	MIS/2003-4	MS75083-1	MS75083-1	24C83961B01	24-P00338N003	24-P00338N003	MS75083-3	24C83961B01	24C83961B01	24-P00338N002	24-P00338N002	MS75083-1	MS75083-1	24-P00338N003	MS75083-1	MS75083-1	228-4-1A	48-80345A62	48-80345A62	48-80345A74	48-80345A74	48-84463K02	48-80345A74	48-80345A74	48-84463K02	48-80345A62	48-80345A62	48-80345A62	48D84616A01	48D84616A01		Part No.
	COIL	COIL	COIL	COIL	COIL	COIL	COIL	INDUCTOR	CHOKE, RF	COIL	COIL	BEAD, FERRITE	COIL	COIL	COIL	COIL	COIL	INDUCTOR	INDUCTOR	COIL	COIL	COIL	COIL	COIL	COIL	COIL AIR WOUND	COIL AIR WOUND				COIL	CHOKE, RF	COIL, AIR WOUND	COIL, AIR WOUND	COIL	CHOKE, RF	CHOKE RF	COIL, AIR WOUND	CUIL, AIR WOUND	COIL	COIL	COIL, AIR WOUND	COIL	COIL	RELAY		DIODE	VARACTOR	VARACTOR	DIODE	VARACTOR	VARACTOR		DIODE	DIODE	DIODE	DIODE	DIODE		Nomenclature
	22UH	100UH	.1UH	.1UH	.1UH	100UH	.1UH	12MH		.15UH-10	.15UH-10		.22UH	100UH	100UH	10UH	.47UH-1	12MH	12MH	.33UH	.47UH	.1UH	.1UH	.33UH	22UH	4T 24	4T 24	31 24 31 94	31 34		.1UH		· 1T 20	1T 20	.15UH-10		0 20	5 20	5 20	HUL	.1UH	1T 20	.1UH	.1UH		10MH7-3-DOI E														Part Value
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6S124A25	6S185A73	6S124A29	65124A29	00124740	6210440	6513475	65104040	65124739	60124409	66104 460	65124479	6S124A59	6S124A59	6S124A41	6S124A41	65124415	65124431	65124431	00124741	05124A41	6S124A41	6S124A41	6S124A41	6S124A41	6S185A25	6S124A25	6S124A41	6S124A41	6S124A18	48-2089C01	48R00869870	48-869044	48-84308A92	48R00869571	48-80345A57	48-84309A61	48-84309A61	48-84309A61	48-80345A57	48R00869570	48HUU869870	48-2089C01	48-80346A65	48-6123A22	48R00869571	48R00869870	48R00869870	48R00869870	48-80345 447	48R00869571	48R00869570	48R00869570	48R00869870	48-80345A54	48-84309A61	48R00869571	48R00869571	48-80345A46		Part No.
RESISTOR	RESISTOR	RESISTOR	RESISION	DESISTOD	BESISTOR	DESISTOR	BESISTOR	DESISTOD	RESISTOR	BESISTOR	RESISTOR	RESISTOR	RESISTOR	RESISTOR	RESISTOR	RESISTOR	RESISTOR	RESISTOR	RESISTOR	RESISTOR	RESISTOR	RESISTOR	RESISTOR	RESISTOR	RESISTOR	RESISTOR	RESISTOR	RESISTOR	RESISTOR	TRANSISTOR	TRANSISTOR	TRANSISTOR	TRANSISTOR	TRANSISTOR	TRANSISTOR	TRANSISTOR	TRANSISTOR	TRANSISTOR	TRANSISTOR	TRANSISTOR	TEANSISTOP	TRANSISTOR	TRANSISTOR	TRANSISTOR	TRANSISTOR	TRANSISTOR	TRANSISTOR	TRANSISTOR	TRANSISTOR	TRANSISTOR	TRANSISTOR	TRANSISTOR	TRANSISTOR	TRANSISTOR	TRANSISTOR	TRANSISTOR	TRANSISTOR	TRANSISTOR		Nomenclature
100-5-1/4	10K-5-1/8	150-5-1/4	100-0-1/4	150-5-1/4	560-5-1/4	18K-5-1/4	16-5-1/4	19K-5-1/4	2.78-5-1/4	0 7K-5-1/4	18K-5-1/4	2.7K-5-1/4	2.7K-5-1/4	470-5-1/4	470-5-1/4	39-5-1/4	180-5-1/4	180-5-1/4	1.5K-5-1/8	470-5-1/4	470-5-1/4	470-5-1/4	470-5-1/4	470-5-1/4	100-5-1/8	100-5-1/4	470-5-1/4	470-5-1/4	51-5-1/4																															Part Value
R 296	R 295	R 294	H 293	E 202	0 202	0 201	D 200	000 0	107 U	D 287	R 286	R 285	R 284	R 283	R 282	R 281	R 279	R 278	H 277	072	H 2/4	R 273	R 272	R 271	R 270	R 269	R 268	R 267	R 266	R 265	R 264	R 263	R 262	R 261	B 260	R 258	R 257	R 256	R 255	R 254	R 253	R 252	R 251	R 249	R 248	R 247	R 246	R 245	R 244	R 243	R 241	R 240	R 239	R 238	R 237	R 236	B 235	R 233	No.	Find
	_	_		• -	• -	• -	• -	• -	• -	• -		. .	-	<u>.</u>		.		. .						-	1	-	-1	-	-	-	-	 .	<u> </u>	. .				-	-	-	-				_	-	-	-	-	- -	- -	• -•	-	-	_	_ 1	• -	•	Req.	Qty.
80185A38	6S185A36	6S185A39	6S185A63	BONCOLCO	60100/00	60105000	60100710	65195712	60100421	60100721	65185407	6S185A05	6S185A05	6S185A33	6S185A33	6S185A53	65185405	6S185A17	65185425	60105A07	65185A/5	6S185A71	6S185A29	6S185A67	6S185A75	6S185A59	6S185A65	6S185A57	6S124A65	6S124A73	6S185A49	6S185A49	6S185A49	6S185A49	65185A49	6S185A21	6S185A09	6S185A09	6S185A45	6S185A53	6S185A49	6S185A57	6S124A91	6S185A05	6S185A05	6S185A25	6S185A33	6S185A33	6S185A53	6S185A43	65185043	6S185A18	6S124A25	6S185A49	6S185A47	6S185A95	60195A95	6S185A33		Part Number
RESISTOR	RESISTOR	RESISTOR	HESISTOH	RESISTOR	DESIGTOR	DECISTOR	DESIGTOR	DECISTOR	DESISTOD	DESISTOR	RESISTOR	RESISTOR	RESISTOR	RESISTOR	RESISTOR	RESISTOR	RESISTOR	RESISTOR	RESISTOR	DESISTOR	RESISTOR	RESISTOR	RESISTOR	RESISTOR	RESISTOR	RESISTOR	RESISTOR	RESISTOR	RESISTOR	RESISTOR	RESISTOR	RESISTOR	RESISTOR	RESISTOR	RESISTOR	RESISTOR	RESISTOR	RESISTOR	RESISTOR	RESISTOR	RESISTOR	RESISTOR	RESISTOR	RESISTOR	RESISTOR	RESISTOR	RESISTOR	RESISTOR	RESISTOR	RESISTOR	RESISTOR	RESISTOR	RESISTOR	RESISTOR	RESISTOR	RESISTOR	DESISTOR	RESISTOR		Nomenclature
300-5-1/8	300-5-1/8	390-5-1/8	3.9K-5-1/8	6.8K-5-1/8	0.07-0-1/0	0 01 -C-01	JJ-J-1/0	0/1-C-U21	100-3-1/0	120-J-1/0	100-5-1/8	15-5-1/8	15-5-1/8	220-5-1/8	220-5-1/8	1.5K-5-1/8	15-5-1/8	47-5-1/8	100-5-1/8	330-5-1/8	12K-5-1/8	8.2K-5-1/8	150-5-1/8	5.6K-5-1/8	12K-5-1/8	2.7K-5-1/8	4.7K-5-1/8	2.2K-5-1/8	4.7K-5-1/4	10K-5-1/4	1K-5-1/8	1K-5-1/8	1K-5-1/8	18-5-1/8	1K-5-1/8	68-5-1/8	22-5-1/8	22-5-1/8	680-5-1/8	1.5K-5-1/8	1K-5-1/8	2 2K-5-1/8	100K-5-1/4	15-5-1/8	15-5-1/8	100-5-1/8	220-5-1/8	220-5-1/8	1.5K-5-1/8	560-5-1/8	4/0-5-1/8	51-5-1/8	100-5-1/4	1K-5-1/8	820-5-1/8	82K-5-1/8	0/1-C-22	220-5-1/8		Part Value
B 350	R 358	R 358	R 358	R 358	H 358	R 358	H 357	R 356	R 355	R 354	1 333	H 352	0 250	B 351	B 350	H 340	H 34/	H 346	R 345	R 344	R 343	R 342	R 341	R 340	R 339	R 338	R 337	R 336	R 335	R 334	R 333	H 331	R 330	R 329	H 328	R 327	R 326	R 325	R 324	R 323	R 322	R 321	H 300	R 315	R 314	R 313	R 312	R 311	R 310	R 309	R 308	R 305	R 304	R 303	R 302	R 301	H 200	R 298	No.	Find
•	S01	S01	S01	S01	SOI	-									• -						-	-	-	-	-	-	-	-	-	-							-		-	-	_ _ _		•		-	-	-	_	-	- -	. .		_	-	-	<u> </u>	. -		Req.	Qty.
06-10621036	06-10621D68	06-10621D64	06-10621D60	06-10621D52	06-10621048	06-10621056	65185A05	6S124A83	6S124B09	18083452F16	00124400	6C104A01	65104461	60185435	65185450	60105A21	05105441	65185A43	6S185A43	6S185A49	6S185A47	6S185A27	6S185A13	6S185A31	6S185A18	6S185A05	6S185A05	6S185A53	6S185A18	6S185A07	6S185A36	6S124A41	6S185A75	6S185A41	6S185A65	6S124A43	6S124A36	6S124A07	6S124A36	6S185A73	6S185A49	6S185A97	AC185404	6S124A49	6S124A49	6S124A73	6S124A73	6S124A73	6S124A73	6S124A59	65124A0	6S124A35	6S124A45	6S124A33	6S124A33	6S124A15	00100/44	6S185A44		Part Numbe
DECICTOR	RESISTOR	RESISTOR	RESISTOR	RESISTOR	RESISIOR	RESISTOR	RESISTOR	HESISIOH	RESISTOR	RESISTOR, VARIABLE		DESISTOD	BESISTOR	RESISTOR	RESISTOR			RESISTOR	RESISTOR	RESISTOR	RESISTOR	RESISTOR	RESISTOR	RESISTOR	RESISTOR	RESISTOR	RESISTOR	RESISTOR	RESISTOR	RESISTOR	RESISTOR	RESISTOR	HESISTOR	RESISTOR	RESISTOR	RESISTOR	RESISTOR	RESISTOR	RESISTOR	RESISTOR	RESISTOR	RESISTOR	RESISTOR	RESISTOR	RESISTOR	RESISTOR	RESISTOR	RESISTOR	RESISTOR	RESISTOR	RESISTOR	RESISTOR	RESISTOR	RESISTOR	RESISTOR	RESISTOR	DESISTOD	RESISTOR		Pr Nomenclature
28.7K-1-1/8	61.9K-1-1/8	56.2K-1-1/8	51.1K-1-1/8	42.2K-1-1/8	JO.JN-1-1/0	40.4K-1-1/0 NUMINA	10-0-1/0	2/18-3-1/4	300K-5-1/4		200	970-5-1/4	3.3K-5-1/4	270-5-1/B	2 7K-5-1/8	3 34-5-1/8	41 U-U-1/0	170-5-1/8	560 £ 1/8	1K-5-1/8	820-5-1/8	120-5-1/8	33-5-1/8	180-5-1/8	51-5-1/4	15-5-1/8	15-5-1/8	1.5K-5-1/8	51-5-1/8	18-5-1/8	300-5-1/8	4/U-3-1/4 300-5-1/8	128-5-1/8	470-5-1/4	4.7K-5-1/8	560-5-1/4	300-5-1/4	18-5-1/4	300-5-1/4	10K-5-1/8	1K-5-1/8	100K-5-1/8	2.2K-5-1/8	1K-5-1/4	1K-5-1/4	10K-5-1/4	10K-5-1/4	10K-5-1/4	10K-5-1/4	2 7K-5-1/4	39-5-1/4	270-5-1/4	680-5-1/4	220-5-1/4	220-5-1/4	39-5-1/4	02U-5-1/8	620-5-1/8		Part Value

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ASSEMBLY PARTS LIST RF Synthesizer (Sheet 3)

JIST

ASSEMBLY PARTS LIST RF Synthesizer (Sheet 4)

					1.2K-5-1/8	RESISTOR	6S185A51	-	R 424
					1.5K-5-1/8	RESISTOR	6S185A53		H 422
					51-5-1/8	RESISTOR	6S185A18		R 421
					120-5-1/8	RESISTOR	6S185A27	-	R 420
					120-5-1/8	RESISTOR	6S185A27	-	R 419
					220-5-1/8	RESISTOR	6S185A33	-	R 418
					100-5-1/8	RESISTOR	6S185A25	-	R 417
					4.7K-5-1/8	RESISTOR	6S185A65		R 416
					330-5-1/4	RESISTOR	6S124A37		R 414
					8/L-9-089	RESISTOR	6S185A45		R 413
					470-5-1/8	RESISTOR	6S185A41	_	R 412
					1K-5-1/8	RESISTOR	6S185A49	-	R 411
					3.3K-5-1/8	RESISTOR	6S185A61	-	R 410
					1K-5-1/8	RESISTOR	6S185A49	-	R 409
					100-5-1/4	RESISTOR	6S124A25	-	R 408
					2.0K-5-1/8	RESISTOR	6S185A56		R 407
					1K-5-1/4	RESISTOR	65124A49	. .	B 406
					10K-5-1/4	RESISTOR	6519449		H 404
					18-5-1/8	RESISTOR	6S124A49		R 403
111-24	DIODE	40-00343A/0	-	VHZIO	1K 5 1/4	RESISTOR	6S124A49		R 402
5.1V-54	DIODE	48-82556C15A	•	VECTO	1K-5-1/4	RESISTOR	6S124A49	-	R 401
11V-54	DIODE	48-80345A78		VH208	10-5-1/4	RESISTOR	6S124A01	_	R 400
10V-54	DIODE, ZENER	48-80345A94A		VR207	1.2K-5-1/4	RESISTOR	6S124A51	-1	R 399
10V-54	DIODE, ZENER -	48-80345A94A	_	VR206	820-5-1/4	RESISTOR	6S124A47	_	R 398
7.5V-54	DIODE,ZENER	48-82256C44	-	VR205	10K-5-1/4	RESISTOR	6S124A73	-	R 397
7.5V-54	DIODE, ZENER	48-82256C44	-	VR204	10-5-1/4	RESISTOR	6S124A01	-	R 396
7.5V-54	DIODE, ZENER	48-82256C44	_	VR203	12K-5-1/4	RESISTOR	6S124A75	-	R 395
7.5V-54	DIODE, ZENER	48-82256C44	-	VR202	12K-5-1/4	RESISTOR	6S124A75	-	R 394
5.6V-54	DIODE,ZENER	48-83193A59	-	VR201	100-5-1/4	RESISTOR	6S124A25		R 393
	INTEGRATED CIRCUIT	51-06472A26	-	U 219	18K-5-1/4	RESISTOR	6S124A79	- -	191
	INTEGRATED CIRCUIT	51-80345A23		U 218	100K-5-1/4	RESISTOR	6S124A25		R 390
		51-80345401		0 210	100-5-1/4	RESISTOR	6S124A73		R 389
	INTEGRATED CIRCUIT	51-80345A04		U 215	91-5-1/8	RESISTOR	6S185A24	-	R 387
	INTEGRATED CIRCUIT	51-82764K29		U 214	100-5-1/4	RESISTOR	6S124A25	-	R 386
	INTEGRATED CIRCUIT	51-84887K67	-	U 213	10K-5-1/4	RESISTOR	6S124A73	-	R 385
	INTEGRATED CIRCUIT	51-80346A54	-	U 212	120K-5-1/4	RESISTOR	6S124A99	-	R 384
	MICRO CIRCUIT	51-80345A30	-	U 211	22K-5-1/4	RESISTOR	6S124A81		R 383
	INTEGRATED CIRCUIT	51-80345A04	-	U 210	2.0K-5-1/4	RESISTOR	65124731		1 00 1
	MIXER	51-80346A05		U 209	56K-5-1/4	RESISTOR	6S124A61		R 380
	INTEGRATED CIRCUIT	51H8432UA8U		102.0	22K-5-1/4	RESISTOR	6S124A81	_	R 379
	MIXER	51-80346A05		U 206	91K-5-1/4	RESISTOR	6S124A96	_	R 378
	INTEGRATED CIRCUIT	51-80323A60	_	U 205	51K-5-1/4	RESISTOR	6S124A90	-	R 377
	INTEGRATED CIRCUIT	51-80345A01	-	U 204	15-5-1/8	RESISTOR	6S185A05	_ .	R 376
	INTEGRATED CIRCUIT	51-80321A69	-	U 203	10K-5-1/4	RESISTOR	65124473		D 375
	INTEGRATED CIRCUIT	51-80345A14	- - -	U 202	34.8K-1-1/8	RESISTOR	06-10621D44		H 3/3
21 32 UN 38 BEAD	INTEGRATED CIECI IIT	24-P06611B016		T 202	71.5K-1-1/8	RESISTOR	06-10621D74		R 372
2T 32 ON 3B BEAD	TRANSFORMER	24-P06611B016	-	T 201	143K-1-1/8	RESISTOR	06-10621E04	-	R 371
2.0K-5-1/4	RESISTOR	6S124A56	_	R 435	2150-1-1/8	RESISTOR	06-10621C27	-	R 370
180-5-1/8	RESISTOR	6S185A31	-	R 434	51,1-1-1/8	RESISTOR	06-10621D60	-	R 369
68-5-1/8	RESISTOR	6S185A21	-	R 432	2.0K-5-1/4	RESISTOR	6S124A56	-	R 368
470-5-1/8	RESISTOR	6S185A41	-	R 431	10K-5-1/4	RESISTOR	6S124A73	- .	R 367
330-5-1/8	RESISTOR	6S185A37		R 430	10K-5-1/4	RESISTOR	6S124A73		R 366
330-5-1/8	RESISTOR	6S185A37		D 420	108-5-1/4	DESISTOD	65124473		202 1
330-5-1/8	RESISTOR	6S185A37		R 427	10K-5-1/4	RESISTOR	6S124A73		R 363
5.6K-5-1/8	HESISTON	6S185A67	-	R 426	100K-5-1/4	RESISTOR	6S124A97	-	R 362
4.7K-5-1/8	RESISTOR	6S185A65	-	R 425	27K-5-1/4	RESISTOR	6S124A83	-	R 361
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Part value	Nomenciature	Part Number	Ren Uty.	No	Part value	Nomenclature	Part No.	Bag	Find
Post Value	•		2]				2	1

SECTION 12

AUDIO SYNTHESIZER (A6)

12-1. General. Generation, processing, and control of modulation audio is the function of the Audio Synthesizer module. Three modulation signals, private line, digital private line, and a fixed 1 kHz, are generated on the board. Processing for external microphone and BNC jack audio inputs as well as sumation of all modulation sources to form a composite source is provided. Switching of the composite source to the appropriate modulator completes the function of the Audio Synthesizer. A block diagram of the Audio Synthesizer is shown in figure 12-1 with its schematic in figure 12-2.

12-2. Private Line Generator. Private line tones from 10 Hz to 10 kHz in 0.1 Hz increments are synthesized using a phase accumulative technique. Consider the 360 degrees in a cycle to be divided into 2" pieces. A 20 bit digital accumulator incrementing at some fixed rate could then at any instant represent a fixed point in the 360 cycle. That is, if the accumulator was half full it would represent the 180° point and if totally full would represent the 360° point.

12-3. The number of times per second that the accumulator goes through its complete cycle determines the output frequency. If the increment rate is fixed, the time required to accumulate 2²⁰ bits can be changed by changing the number of bits added at each increment time.

12-4. The PL synthesizer increments at a 104 857.6 Hz rate so that if only one bit were added each time, the time to complete one cycle would be 10 seconds. Processor loaded control latches determine the number of bits to be added at each increment time and thus the final output frequency. A 20 Bit Adder adds the control word to the current word in the 20 bit accumulator Latch. At the next increment time the Adder output is latched and becomes the next .input to the Adder.

12-5. Conversion of the linear digital output of the 20-Bit Latch accumulator into a sinusoidal digital output is the function of the Decode ROM. A Digital to Analog (D/A) converter following the ROM converts the sinusoidal information into a quantized sinewave having a period equal to the cycle time of the 20-Bit Latch accumulator.

12-6. A bandpass filter with a 10 Hz to 10 kHz passband filters the quantized waveform to a sinewave having less than 1% distortion. The level of the sinewave is processor controllable by a programmable attenuator having 0, 10, 20, and 30 dB settings. The output of the PL generator is switched with the output of the DPL generator to give the INT MOD signal.

12-7. DPL Generator. The 23 bit Digital Private Line (DPL) word is generated by the processor from the 3digit code. The 23-bit word is then transferred to a serial shift register and clocked out at a 133 Hz rate. Connecting the output of the shift register back to its input causes the 23-bit word to be continuously repeated.

12-8. A 133 Hz tone from the PL generator is the DPL clock input. For the DPL output mode the tone is gated to the clock input of the shift register by the Shift Register Control circuit. During the load mode the Shift Register Control gates a control latch to the shift register input. Twenty three data bits and clock pulses are then provided by the processor to load the DPL word. At the completion of the load mode, the Shift Register Control switches back to the output mode to cause the DPL word to be cycled through the shift register at the 133 Hz rate.

12-9. A bandpass filter following the shift register output removes the higher frequency components of the digital signal. The filtered DPL signal is then applied to the select switch. For the DPL off code (133 Hz tone), the processor switches the INT MOD line to the PL output so that a 133 Hz sinewave is output.

12-10. 1 kHz Tone. A filtered 1 kHz square wave provides the fixed 1 kHz modulation source. The SYNTH 1 kHz signal from the RF Synthesizer is filtered to less than 1% distortion by a bandpass filter. The filter output is the 1 kHz signal source.

12-11. External Modulation. A microphone and a front panel jack are the external modulation inputs. An Instaneous Deviation Control (IDC) circuit amplifies and limits the microphone signal (MIC IN) before sumation with the signal (EXT MOD IN) from the front panel jack. The sumation signal is the EXT MOD source.

12-12. Modulation Control. Level control of the three modulation sources is by either the front panel controls or the IEEE interface module. The level adjusted sources are then returned to the Audio Synthesizer module where they are summed together to form the composite modulation audio. The composite signal is then routed to the Scope/DVM Control module (MOD CAL AUDIO) for modulation determination, to a buffer amp which drives the front panel modulation output (MOD TO FP), and to a Modulation Select Switch which routes the signal to the desired modulator.

12-13. Modulation audio is switched to the speaker (VOL CNTL AUDIO) for any generate mode, to the DSBSC modulator (DSBSC MOD) for sideband modulation, to the RF Synthesizer for frequency modulation (FM MOD), to the offset oscillator for frequency modulation of the duplex output (OFFSET MOD), and to the RF output leveling loop for amplitude modulation. The signal for amplitude modulation is summed with a 5 VDC level and then routed to the variable RF level control on the front panel (5 VDC + AM MOD). At the RF level control the signal is attenuated according to the level setting to give the DC plus AM reference signal for the output leveling loop.

12-14. Module Control. Processor control of the Audio Synthesizer is via the AF control bus. The four bit address bus (AF ADD BUS 0-3) is decoded by the Address Decoder to determine which control latch is to be accessed. Control data is transferred to the accessed latch on the four bit data bus (AF DATA BUS 0-3). Synchronization of the data transfere is the function of the AF BUS EN1 signal line.



12-3, 12-4




(RTC-4011A)

C 054	C 053	C 052	C 051	C 050	C 049	C 048	C 047	C 046	C 045	C 044	C 043	0 042	0 04 1	C 041	C 040	C 039	C 038	C 037	C 036	C 035	C 034	C 032	C 031		C 029	C 028	C 027	C 026	C 025	C 024	C 023	C 022	C 021	C 020	C 019	C 01/	C 016	C 015	C 014	C 013	C 012	C 011	C 010	C 009	C 007	C 006	C 005	C 004	C 002	C 001	600	800	007	006	005	003	002	001	No.	Find	1 1 1
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23D84665F01	21D82187B14	21C82372C09	23D84665F01	23D84665F01	21084494830	23D84665F01	08D82096J08	21C82372C09	08D84326A48	23083441815	23003441013	21002072009	21082372000	21082372009	21D84494B07	21D84494B07	21C82372C09	23D84665F01	21C82372C09	21082372009	21022072000	21082372009	00004320740	21004494024	21084494816	2108449480/	210823/2009	210823/2009	21C82372C09	21C82372C09	21C82372C09	21C82372C09	21C82372C09	21D83596E32	21002372009 08D84326A27	21082372009	21C82372C09	21C82372C09	21C82372C09	21D84494B04	21R00863395	21C82372C09	21C82372C09	21C82372C09	21C82372C09	21C82372C09	21C82372C09	23D84665F01	23D84665F01	23D84665F01		MS35206-214	42C84284B01	5C84500B03	11-14167A01	MS204/UAU4-5	07-P00499N001	84-P00445N001		Part No.	1
CAPACITOR	CAPACITOR	CAPACITOR	CAPACITOR	CAPACITOR	CAPACITOH	CAPACITOR	CAPACITOR	CAPACITOR	CAPACITOR	CAPACITOR	CAPACITOR	CAPACITOR	CAPACITOR	CAPACITOR	CAPACITOR	CAPACITOR	CAPACITOR	CAPACITOR	CAPACITOR	CARACITOR	CAPACITOR	CAPACITOR	CARACITOR	CAPACITOR	CAPACITOR	CAPACITOR	CAPACITOR	CAPACITOR	CAPACITOR	CAPACITOR	CAPACITOR	CAPACITOR	CAPACITOR	CAPACITOR	CAPACITOR	CAPACITOR	CAPACITOR	CAPACITOR	CAPACITOR	CAPACITOR	CAPACITOR	CAPACITOR	CAPACITOR	ADHESIVE, SIL RUBBER	SCREW,PH	RETAINER	EYELET	INK		BRACKET, PWB MIG	PWB, AUDIO SYNTHESI		Nomenclature								
10UF-25V	1000PF-10-100	.1UF80-20-25	10UF-25V	10UF-25V	34PF-5-500	10UF-25V	.022UF-10-250	.10180-20-25	.02201-1-50	1.001-20-35	1.001-20-00	1 0UF-20-35	1UF80-20-25	.1UF80-20-25	150P.F-5-500	150PF-5-500	.1UF80-20-25	10UF-25V	1UF80-20-25	1UF80-20-25	1UF80-20-25	1UF80-20-25	02201-1-50	02211E-1-50	39PE-5-500	3300E-5-500	15005-5-500	111680-20-25	.1UF80-20-25	.1UF80-20-25	.1UF80-20-25	.1UF80-20-25	.1UF80-20-25	1100PF-5-200	.0557UF-2-50	11JF80-20-23	111580-20-25	.1UF80-20-25	.1UF80-20-25	100PF-5-500	3000PF-2-500	.1UF80-20-25	1UF80-20-25	1UF80-20-25	100PE-5-500	.1UF80-20-25	.1UF80-20-25	10UF-25V	10UF-25V	10UE-25V		4-40X.312			BLACK	1/87.312	1 /02 010	ZE		Part Value	
R 031	0 021	R 030	R 029	R 028	R 027	R 026	R 025	R 024	R 023	R 022	R 021	R 020	R 019	R 018	R 017	R 016	R 015	R 014	R 013	R 012	R 011	R 010	R 009	R 008	R 007	R 006	R 005	R 004	B 002		Q 005	Q 004	Q 003	Q 002	Q 001	L 004	L 003	1 001	- 001	800 L	J 007	J 006	J 005	J 004	2 003	J 001	CR009	CR008	CR007	CROOS	CR004	CR003	CR002	CR001	C 059	C 058	C 057	C 055	NO.	Find	
		.	-	_	-	-	-	-	-	-	-	-	1	1	-	-	-	-	_	-	-	-	_^)	_	<u> </u>	<u> </u>	_			• _			-	-	-	<u> </u>	. .	. -			-	-	-	-				-	<u> </u>				-	-	_	_ .	. .		neq.	Oty.	
06-10621E05	06 10631 850	06-10621D48	6S124A69	6S124A70	6S124A97	6S124A97	6S124B18	6S124A49	6S124A73	6S124A65	6S124A80	6S124A73	06-10621C43	06-10621C95	06-10621D56	06-10621D88	6S124A77	6S124A83	6S124A67	6S124A49	6S124A97	6S124B22	6S124A43	6S124A47	6S124A51	6S124A43	6S124A29	6S124A73	6S124R06	60124713	48R00869570	48R00869571	48R00869570	48R00869570	48R00869571	MS90539-03	MS90539-03	24-14198A55	M39024/11-01	48-80345A94	48-84463K02	48-84463K02	48-84463K02	40-00343401 48-84463K02	48-84463K02	48-80345A81	48-80345A80	21D82428B62	21D84494B34	21002372013 21084494B34	21C82372C09		Part No.								
RESISTOR	DECISTOR	RESISTOR	RESISTOR	RESISTOR	RESISTOR	RESISTOR	RESISTOR	RESISTOR	RESISTOR	RESISTOR	RESISTOR	RESISTOR	RESISTOR	RESISTOR	RESISTOR	RESISTOR	RESISTOR	RESISTOR	RESISTOR	RESISTOR	RESISTOR	RESISTOR	TRANSISTOR	TRANSISTOR	TRANSISTOR	TRANSISTOR	TRANSISTOR	INDUCTOR	INDUCTOR			TEST POINT	DIODE,ZENER	DIODE	DIODE	DIODE			DIODE,ZENER	DIODE,ZENER	CAPACITOR	CAPACITOR	CAPACITOR	CAPACITOR		Nomenclature															
348-1-1/8 147K-1-1/8	240 1 1/0	38.3K-1-1/8	6.8K-5-1/4	7.5K-5-1/4	100K-5-1/4	100K-5-1/4	680K-5-1/4	1K-5-1/4	10K-5-1/4	4.7K-5-1/4	20K-5-1/4	10K-5-1/4	3.16K-1-1/8	11K-1-1/8	46.4K-1-1/8	100K-1-1/8	15K-5-1/4	27K-5-1/4	5.6K-5-1/4	1K-5-1/4	100K-5-1/4	1M-5-1/4	560-5-1/4	820-5-1/4	1.2K-5-1/4	560-5-1/4	150-5-1/4	10K-5-1/4	220K-5-1/4	10K-5-1/4	106-5-1/4					330UH	330UH	470UH	4701 IH								10V				J.	9//	90	2.4V-55	.01UF80-20-200	68PF-5-500	68PF-5-500	.1UF80-20-25 .05UF-20-25		Part Value	
U 032	U 031	030	029	0 028	. 0 027	0 026	U 025	0 024	0 023	0.022	1 1 0 02 1	U 021	U 020	U 019	U 018	U 017	U 016	U 015	U 014	U 013	U 012	U 011	010		11 008	000		0 004	U 003	U 002	U 001	R 065	R 064	R 063	R 061	B UBU B	R 058	R 057	R 056	R 055	R 054	R 053	R 052	R 051	R 049	R 048	R 047	R 046	R 045	R 042	R 041	R 040	R 039	R 038	R 037	ASU A	R 034	R 033	NO.	Find	
	1	_	_	-			_				• -		-	_	-	-	-	-	-		.	-	• -	• -						_	-	-	_	_ .					_	-	-	<u> </u>		.		-1	_		. .		-	-	_	_			_	-	Heq.	Qty.	
51-84928F02	51-80345A01	51-84887K38	51-84887K38	51-84887K38	51-84887K38	51-84887K38	51R84320A80	51-84887K73	51-84887K73	51H8432UA80	51D0432UA00	51884320480	51_84887K77	51-84887K73	51-84887K30	51-84887K30	51-84887K30	51-84887K30	51-84887K30	51-84887K54	51-84887K54	51-80345A47	51-8488/K54	51H8432UA80	51-80346A59S	51-84118K31	51-84887K30	51-84887K30	51-80345A20	51-80345A04	51-80345A04	6S124A77	6S124A67	6S124A67	6S124473	6S124A73	6S124A66	6S124A49	6S124A25	6S124A25	6S124A73	6S124A73	6S124A29	6S124A25	6S124A73	6S124A73	6S124A25	6S124A25	6S124A91	6S124A66	6S124A91	6S124A73	6S124A99	6S124A43	65124A09 65124A53	6S124A65	6S124A53	6S124A62		Part No.	
INTEGRATED CIRCUIT	INTEGRATED CIRCUIT	INTEGRATED CIRCUIT	INTEGRATED CIRCUIT	INTEGRATED CIRCUIT	INTEGRATED CIRCUIT	INTEGRATED CIRCUIT	INTEGRATED CIRCUIT	INTEGRATED CIRCUIT	INTEGRATED CIRCUIT	INTEGRATED CIRCUIT					INTEGRATED CIRCUIT			INTEGRATED CIRCUIT	RESISTOR	RESISTOR	RESISTOR	RESISTOR	RESISTOR	RESISTOR	RESISTOR	RESISTOR	RESISTOR	RESISTOR	RESISTOR	RESISTOR	RESISTOR	RESISTOR	RESISTOR	RESISTOR	RESISTOR	RESISTOR	RESISTOR	RESISTOR	RESISTOR	RESISTOR	RESISTOR	RESISTOR	RESISTOR	RESISTOR	RESISTOR		Nomenclature														
																																15K-5-1/4	5 6K-5-1/4	4./M-5-1/4	10K-5-1/4	10K-5-1/4	5.1K-5-1/4	1K-5-1/4	100-5-1/4	100-5-1/4	10K-5-1/4	10K-5-1/4	150-5-1/4	100-5-1/4	10K-5-1/4	10K-5-1/4	100-5-1/4	100-5-1/4	56K-5-1/4	5.1K-5-1/4	56K-5-1/4	10K-5-1/4	120K-5-1/4	560-5-1/4	6.8K-5-1/4	4.7K-5-1/4	1.5K-5-1/4	3.6K-5-1/4		Part Value	
																																														Y 001	U 047	U 045	U 044	U 043	U 042	U 041	U 040	11 039	U 037	U 036	U 035	U 034	No.	Find	
																																														-				-	_	-		- -		-	-	-	Req.	Qty.	
																																														48-80346A07	51-80345A04	1-80/14864	51-84887K10	51-84887K10	51-84887K10	51-84887K10	51-84887K10	51-84887K10	51-84887K10	51-84928F02	51-80345A16	51-84928F02		Part No.	
																																														CRYSTAL	INTEGRATED CIRCUIT		INTEGRATED CIRCUIT	INTEGRATED CIRCUIT	INTEGRATED CIRCUIT	INTEGRATED CIRCUIT	INTEGRATED CIRCUIT	INTEGRATED CIRCUIT	INTEGRATED CIRCUIT	INTEGRATED CIRCUIT	INTEGRATED CIRCUIT	INTEGRATED CIRCUIT		Nomenclature	
																																														1.048576MHZ												-		Part Value	

Audio Synthesizer

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SECTION 13

PROCESSOR I/O MODULE (A7)

13-1. General. Frequency Counter and DVM functions with their processor interface as well as the processor interface for the two system control buses are contained on this module. Additionally, circuitry to complete the 10.245 MHz phase locked loop, and to zero beat the incoming carrier are also on this board. A block diagram of the processor I/O module is shown in figure 13-1 with its schematic shown in figure 13-2.

13-2. 10.245 MHz Phase Locked Loop. Only part of the circuitry for the second local oscillator loop is contained on this module. The 10.245 MHzVCO and the loop filter are on the received module. A sample of the 10.245 MHz second local oscillator is mixed with the SYNTH 10 MHz signal. A divide by fourty nine following the mixer divides the 245 kHz signal from the mixer to 5 kHz. A phase comparison between the 5 kHz from the divider and the SYNTH 5 kHz signal results in the 10.245 MHz VCO TV signal. The VCO TV signal is an error signal which is filtered by the loop filter on the receiver to correct the VCO frequency and maintain phase lock.

13-3. System Control Bus Interface. Interface between the processor buses and the system is through Peripheral Interface Adapters (PIA). The PIA is a single integrated circuit that provides 18 input/output latches which may either be read from or written into by the processor. Two additional inputs on the PIA provide for processor interrupt capability. The two system control buses utilize a single PIA.

13-4. Each system control bus consists of eight lines split into four data lines and four address lines. The address lines define the particular latch into which the data is to be stored, or the buffer from which data is to be obtained. One additional address line, the bus enable line, is required to enable the address decoding circuitry. Thus each control bus can have as many latches at one address as there are bus enable lines. The system utilizes one RF bus enable and two AF bus enables for a total control bus capability of 192 bits. The second bus enable for the AF control bus is on the processor card.

13-5. For internal timing ontonesequences.theprocessorisinterruptedevery10msec.When interrupted by the timing input the processor stops it current process, acknowledges the interrupt, increments its time counter and then combines as normal. The timing interrupt is the SYNTH 100 Hz input **to** the Bus PIA.

13-6. DVM. Inputs on the DVM to A/D signal line are digitized into a 10-bit digital word plus a sign bit and then input to the processor through the DVM PIA. An Absolute Value circuit converts the ± 1 volt bipolar input signal to a 0-1 volt unipolar positive level with a separate digital output to indicate the sign of the input. An Analog to Digital Converter (A/D) converts the unipolar input into a 10-bit word under processor command. A pulse on the START line from the processor starts the A/D. When conversion is complete the A/D signals the processor on the END line. The processor in turn enables the output drivers on the A/D, sets the DVM/CNTR Buffer to the DVM mode, and inputs the 10bit word plus the signal bit.

13-7. For AC measurements a filter is switched on in the Absolute Value circuit so that its output is a DC level proportional to the average value of the input sinewave. Conversion to RMS is made in the processor by multiplying the average level by 1.11 to obtain the RMS level.

13-8. Frequency Counter. Three possible signal sources are available to the frequency counter for frequency determination. For external inputs the EXT FREQ CTR line from the Front Panel Interface module provides the input. Determination of the duplex frequency is accomplished by measuring the frequency of the offset oscillator on the OFFSET FREQ line. Monitor frequency error is determined from the IF/BFO FREQ line by comparing that frequency to 455 kHz. The desired signal is selected to the counter control by the Select Switch under processor control.

13-9. The Counter Control circuitry responds to a START pulse from the processor to gate the output of the Select Switch to the Accumulator for a time period determined by the Gate Time Generator. When the gate time has ended, or if the accumulator overflows, the Counter Control signals the processor on the END line that the count is complete. The processor in turn disables the A/D output drivers, switches the DVM/CNTR Buffer to the counter mode, and inputs the 16-bit accumulator information.

13-10. Gate times from 0.001 sec to 10 sec are generated by the Gate Time Generator. The SYNTH 1 kHz signal is the reference input for the generator. Selection of the gate time is by processor control to give a five digit or 0.1 Hz resolution frequency display.

13-11. Zero Beat. A zero beat with the incoming carrier is obtained by successively mixing the 455 kHz IF/BFO FREQ with 500 kHz, 50 kHz, and 5 kHz. The beat signal that results from the mixing drives the ground return circuit for the signal presence indicator.

13-12. Module Control. Control of this module is from the processor on the AF control bus. A four bit address (AF ADRS BUS 0-3) is decoded by the Address Decode circuitry to determine which Control Latch the control data is to be stored. The four data bits (AF DATA BUS 0-3) are then stored into the selected Control-Latch by a pulse on the AF BUS EN 2 signal line.



13-3,13-4

Figure 13-3. Processor I/O Parts Locator





Schematic Diagram (Sheet 1 of 3) (RTC-4012A)

Figure 13-2. Processor I/O A7 Schematic Diagram (Sheet 2 of 3) (RTC-4012A)





ASSEMBLY PARTS LIST Processor I/O

INANSISION
R 056 R 057
1 1 6S1
65124A29 65124A29 65124A29 65124A14
RESISTOR RESISTOR RESISTOR RESISTOR
6.81K-1-1/8 150-5-1/4 150-5-1/4 36-5-1/4 36-5-1/4 36-5-1/4

Processor Module A7 RTC-4012A

SECTION 14

IEEE INTERFACE MODULE (A8)

14-1. **General.** Remote control of the system is possible using a IEEE-488 bus and the IEEE Interface Module. The Interface Module provides the interface for the 488 bus and provides for processor control of most of the functions normally controlled from the front panel. A block diagram of the IEEE Interface Module is shown in figure 14-1 with its schematic shown in figure 14-2. See section 22 for information on the use of the IEEE Bus for system control.

14-2. IEEE **Bus Interface.** Bus buffering and interface protocol as defined by the IEEE-488 specification is provided for by.the IEEE Bus Interface circuit. The system processor accesses the interface directly through its address', data, and control buses for reading from or writing to the IEEE bus.

14-3. **RF Level Control.** The RF Level Control circuitry selects between the 5 VDC + AM MOD or the AM MOD + DCREF(I) input for remote or local control respectively. For remote control the 5 VDC + AM MOD input is electronically attenuated to provide the requested RF output level. For local control the attenuator is programmed for unity gain so that the AM MOD + DC REF(I) signal from the front panel RF level potentiometer controls the RF output level.

14-4. For the IEEE control option, a electronically programmable RF step attenuator is installed in the system. Control of the attenuator is then from the processor through the Address Decode and Control Latch circuitry on the Interface Module.

14-5. Modulation Control. Each of the three modulation sources are individually controllable by the IEEE Bus Interface module. For remote control the respective modulation input (INT MOD (I), EXT MOD (I), and 1 kHz SINE) is switched to a programmable attenuator. The system processor selects the level of attenuation necessary to provide the requested level of modulation. For local control the attenuators are programmed for unity gain and the respective modulation signal from the front panel level control (INTMODRTN (I), EXT MOD RTN (I), and 1 kHz SINE RTN (I)) is selected to the attenuator to provide modulation level control.

14-6. Address Decode and Control Latches. The system processor has direct control over the programmable attenuators on the module with the Address Decode and Control Latch circuitry. Control data on the data bus (DO-D7) is latched at the Control Latch indicated by the address bus (AO-A15).



Figure 14-1. IEEE Interface Module A8 Block Diagram











NOTES: DESIGNATIONS ARE SNOWN. BOR COMPLETE DESIGNATION PREIN WITH AB OF RESERVE DRAWINGS REFER TO : IEEE Interface Module

ASSEMBLY PARTS LIST

U 011 U 012 U 013

56289 04713 71450 04713

206-6

51-80346A6C

INTEGRATED CIRCUIT

INTEGRATED CIRCUIT RESISTOR1/8 RESISTOR RESISTOR NETWORK RESISTOR NETWORK RESISTOR NETWORK RESISTOR NETWORK

U 015 U 014

04713 04713 51-80346A57

51-80346A5 51-80346A57

INTEGRATED CIRCUIT INTEGRATED CIRCUIT INTEGRATED CIRCUIT SWITCH, 6 STATION U 010 U 001 R 010

04713

51-80346A52 51-80346A63

06-10621D25

QUAD SIP 22.1K-1-1/8

22.1K-1-1/8

RESISTOR NETWORK

QUAD SIP

QUAD SIP

QUAD SIP

QUAD SIP

06-10621D25 784-3-R22K 784-3-R22K 784-3-R22K 784-3-R22K 784-3-R22K

C 005 C 006 C 007 C 010 C 010 C 011 C 012 C 012 C 014 C 015 C 015 C 016 C 017 C 016 C 017 C 016 C 017 C 016 C 017 C 005 C 052 C 063 C 0601 C 0602 C 0600 C 0600 C 0600 C 0600 C 0601 R 001 R 002 R 004 R 005 R 005 R 006 R 007 R 008 C 001 C 002 C 004 001 002 004 005 006 007 008 F00800 PZ.BLP Find No. C 024 C 027 C 029 C 021 C 023 C 051 C 050 C 030 C 020 C 019 C 018 Req TLS 800 Ŗ AR AR 01537 01537 30PZB 01537 82647 68035 48-84463K02 48-84463K02 48-84463K02 21D82187B49 5C84500B03 07-P00499N001 C9524-01 23D84665F01 21D82428B62 21D82428B62 21D82428B62 21D82428B62 21D82428B62 21D82428B62 21D82428B62 21D82428B62 21D82187B08 21D82187B06 21D82187B08 21D82187B06 21D82187B49 21D82187B49 21D82187B49 MS35206-214 42C84284B01 MS20470AD4-5 11-14167A01 SN63WRMAP3 84-P00204N001 80077 8009700000 784-1-R22K 784-1-1.0K 09-80313A09 48-84463K02 48-84463K02 23D84665F01 23D84665F01 23D84665F0 21D82428B62 21D82428B62 21D82428B62 21D82428B62 21D82428B62 21D82428B62 21D82428B62 21D82428B62 21D82428B62 Part No. CAPACITOR CAPACITOR CAPACITOR CAPACITOR CAPACITOR CAPACITOR CAPACITOR CAPACITOR SCREW WIRE RIVET Ŗ 0IBM OS/VS 370 DIODE25V DIODE25V CAPACITOR CAPACITOR CAPACITOR CAPACITOF CAPACITOF CAPACITOF CAPACITOF CAPACITOR CAPACITOR CAPACITOR CAPACITOF RETAINER EYELET BRACKET, PWB MTG SOLDER PWB, IEEE INTERFACE SOCKET CAPACITOR CAPACITOF CAPACITOF CAPACITOF CAPACITOF CAPACITOF CAPACITOR CAPACITOR CAPACITOR CAPACITOR RESISTOR NETWORK RESISTOR NETWORK SOCKET DIODE25V DIODE25V DIODE25V Nomenclature 14 PIN .01UF80-20-200 .01UF80-20-200 BLACK 150PF-10-500 150PF-10-500 HEX SIP 24 PIN 10UF-25V 4-40X.312 1/8X.312 10UF-25V .01UF80-20-200 .01UF80-20-200 .01UF80-20-200 .01UF80-20-200 .01UF80-20-200 .01UF80-20-200 .01UF80-20-200 .01UF80-20-200 .01UF80-20-20 220PF-10-500 220PF-10-500 220PF-10-500 220PF-10-500 150PF-10-500 HEX SIP 10UF-25V 10UF-25V 01UF80-20-200 01UF80-20-200 01UF80-20-200 .01UF80-20-200 01UF80-20-200 01UF80-20-200 50PF-10-500 Part Value No. U 066 U 052 U 052 U 045 U 046 U 047 U 048 U 034 U 031 U 027 U 026 U 025 U 024 U 021 U 016 U 017 U 018 U 067 U 064 U 051 U 049 U 044 U 037 U 036 U 035 Req. 04713 04713 04713 04713 04713 04713 04713 04713 04713 04713 04713 04713 04713 18714 18714 18714 18714 04713 04713 04713 04713 04713 04713 32293 32293 32293 32293 3 51-80345A98 3 51-80345A98 3 51-80346A51 3 51-80346A51 51-80345A04 51-80345A04 51-80345A04 51-80346A55 51-5467G01 51-80346A58 51-80346A56 51-80345A98 51-80346A50 51-80346A51 Part No. 51-80345A04 51-80346A59 51-5467G0 51-80345A98 INTEGRATED CIRCUIT Nomenclature Part Value Find Req. NNNAA N N 30-P04125T001 30-P04126T001 4545-440-A-9 67-P04122T001 51-80345A12 30-P04146T001 30-P04147T001 C9524-01 MS35338-40 MS27183-3 01-80304A54 57-30240 5610-6-31 MS35206-213 40-P04127T00 42-P06849R002 03-15013G11 Part No. MS27183-2 MS35338-39 WASHER,FLAT CABLE ASSEMBLY,FRONT CABLE ASSEMBLY, REAR STRAP, CABLE CLAMP CABLE ASSEMBLY, IEEE CABLE ASSEMBLY WAFER SWITCH, 14 POS CONNECTOR SCREW CABLE ASSEMBLY, IEEE BLOWER MODIFICATION K STANDOFF SCREW,PH SOCKET INTEGRATED CIRCUIT WASHER, PLAIN WASHER, FLAT WASHER,LOCK WASHER,LOCK Nomenclature 24 PIN 14 PIN NO.2 4-40X1.18 NO.4 NO.4 4-40X.250 16PIN-101N-END 2-56X5/16 Part Value

IEEE Interface Module A8

RTC-4013A

SECTION 15

PROCESSOR MODULE (A9)

15-1 General. Primary control and data manipulation requirements of the system are performed by the microprocessor on the Processor Module. Input and output information is carried along the processor's address, data, and control buses, or displayed on the CRT through the character generator circuits on the Processor Module. A block diagram of the Processor Module is shown in figure 15-1 with its schematic shown in figure 15-2.

15-2. Processor. A Motorola MC6802 microprocessor is the central part of the processor. The microprocessor then accesses and controls the rest of the processor via three signal buses. The sixteen bit address bus (AO-A15) determines which device on the bus the microprocessor will access. Data that will be read from or written to the accessed device is contained on the seven bit data bus (DO-D7). Synchronization of the data transfer and several specialized processor functions are provided by the control bus (HALT, NMI, RESET, IRQ, VMA, E, and R/W).

15-3. The series of commands that direct the microprocessor's actions (software) is contained in the 16Kx 8 ROM (Read Only Memory). A standard unit has 12K x 8 ROM for the main program. With the IEEE option an additional 4K x 8 ROM is added to provide the extra program required.

15-4. Temporary storage of microprocessor data is provided by 1Kx8 RAM (Random Access Memory). RAM may be written into or read from by the microprocessor, and is used to store data generated by one part of the program for use in another part of the program. Half of the RAM is reserved for storage of the data to be displayed on the CRT.

15-5. A Peripheral Interface Adapter (PIA) on the microprocessor bus provides input and output latches for thefront panel keyboard input (KYBD COL 0-3 and KYBD ROW 0-4), the IEEE option detector input (IEEE OPT DET), the AF BUS EN1 signal line, and the nonvolatile memory (NVM). Data that is to be held during power off is stored by the microprocessor in the NVM. Then when power is turned on the microprocessor reads the contents of the NVM to obtain its start up mode, the RF and tone memory presets, and the rest of the preset data. If the operator changes a preset, the microprocessor changes the data in the NVM so that the new preset will be remembered. The NVM has a 42 x 8 or fourty two eight bit words capability.

15-6. Character Generator. The Character Generator sequentially accesses that half of RAM where character information is stored and causes the respective characters to be displayed on the screen. Since both the character generator and the microprocessor share the same memory, the two must be synchronized so that they access that memory during alternate half cycles of the master clock. A 4 MHz oscillator is divided by four within the 6802 to give the 1 MHz (E) master timing signal. The E signal from the microprocessor is then used to synchronize the 2 MHz DOT CLOCK and input to the Character Clock Generator.

15-7. A raster scan technique is used to generate the character display. The CRT beam is scanned horizontally and vertically across the screen with the beam being modulated by a sequence of pulses. Each pulse is a dot on the screen whose position is determined by its time of occurrence from the horizontal and vertical sync pulses. During the horizontal sync the beam is reset to the left edge of the screen and during the vertical sync it is reset to the top of the screen. Each character field is eight dots wide and eight dots high. There are 32 character fields across the width of the CRT and 16 character field vertically. Since two horizontal character fields are used for horizontal sync blanking and one vertical field for vertical sync blanking the total display area is 30 by 15 characters or 450 characters.

15-8. Seven bit words representing one of 128 possible characters are stored in RAM for each character location on the CRT. The character generator sequentially accesses each RAM location in synchronization with the raster scan and creates a pulse modulation sequence in response to the character data that results in the character being displayed on the CRT. When the processor is not accessing RAM, the Address/Data Buffer from the processor is disabled and the Address Buffer from the character generator is enabled. The CHAR ADRS signal from the Character Clock Generator addresses the RAM location corresponding to the location being currently scanned on the CRT. The seven bits of data representing the character to be displayed are latched by the CHAR LATCH signal. A Character ROM decodes the seven bits plus a three bit ROW ADRS to determine the dot pattern for the current dot row scan position for the character to be displayed. The dot pattern is then parallel loaded into a Shift Register and clocked out serially to give the CHAR GEN Z-AXIS pulse modulation sequence. It should be noted that each character line on the CRT is scanned eight times, once for each dot row. Thus each character must be accessed eight times from RAM before the total character is displayed.

15-9. To maintain synchronization between the CHAR GEN Z-AXIS signal and the raster scan, the Clock Generator outputs horizontal and vertical character sync signals. These sync signals coordinated the sweep generators on the Scope/DVM Control module with the character generator. For the dual display mode, explained in paragraph 9-6, a LINE 1 output and a CHAR GEN RST input is provided to the clock generator.



Figure 15-1. Microprocessor Module A9 Block Diagram Figure 15-2. Microprocessor/Character Generator A9 Schematic Diagram (Sheet 1 of 4) (RTC-4014A)



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Figure 15-2. Microprocessor/Character Generator A9 Schematic Diagram (Sheet 3 of 4) (RTC-4014A)





Schematic Diagram (Sheet 4 of 4) (RTC-4014A)



Microprocessor/Character Generator A9 RTC-4014A

ASSEMBLY PARTS LIST

1		Dast Number	Non-nolation	David Valua	1		Davi Number	Nomenclatura	Dart Value
No.	Req.				No.	Req.			
					R 013	-	6S124A80	RESISTOR	20K-5-1/4
001	-	84-P00443N001	PWB, MICRO PROC CHAR		R 015	-	6S124A61	RESISTOR	3.3K-5-1/4
003	AR	SN63WRMAP3	SOLDER		R 017		6S124A73 6S124A73	RESISTOR	10K-5-1/4
004	- AH	11-14167A01	INK	BLACK	R 018	-	6S185A49	RESISTOR	1K-5-1/8
005	4 1	07-P00499N001	BRACKET,PWB MTG	CFC 201 +	U 001	- ,	51-82807K41	INTEGRATED CIRCUIT	
007	24	5C84500B03	FYFI ET	1/07.312	U 002	-	51-84118K71	INTEGRATED CIRCCUIT	
008	2	42C84284B01	RETAINER		U 003		51-84118K79	INTEGRATED CIRCUIT	
600	22	MS35206-214	SCREW, PH	4-40X.312			51-84118K/9	INTEGRATED CIRCUIT	
010	-ω	C9524-01	SOCKET	24 PIN	006	<u>.</u>	51-80345A37	INTEGRATED CIRCUIT	
012	4 4	3-582118-5	JACK, HP	WHITE	U 007	-	51-80345A38	INTEGRATED CIRCUIT	
C 001	-	21D82428B62	CAPACITOR	.01UF80-20-200	008 U	-	51-80345A39	INTEGRATED CIRCUIT	
C 002	-	21D82428B62	CAPACITOR	.01UF80-20-200	009		51-82807K03	INTEGRATED CIRCUIT	
C 003	-	21D82428B62	CAPACITOR	.01UF80-20-200	U 011		51-84621K24		
C 004		21D82428B62	CAPACITOR	.01UF80-20-200	U 012	_ _	51-80345A29	INTEGRATED CIRCUIT	
0.006		21082428862	CAPACITOR	.010F80-20-200	U 013	-	51-80345A29	INTEGRATED CIRCUIT	
C 007	<u> </u>	21D82428B62	CAPACITOR	.01UF80-20-200	U 014	-1	51-80345A31	INTEGRATED CIRCUIT	
C 008	-	21D82428B62	CAPACITOR	.01UF80-20-200	U 015		51-80345A31	INTEGRATED CIRCUIT	
C 009	-	21D82428B62	CAPACITOR	.01UF80-20-200	U 017	.	51-80345A31	INTEGRATED CIRCUIT	
C 010		21D82428B62	CAPACITOR	.01UF80-20-200	U 018	-	51-80345A11	INTEGRATED CIRCUIT	
C 012	<u> </u>	21D82428B62	CAPACITOR	.01UF80-20-200	U 019	-	51-80345A11	INTEGRATED CIRCUIT	
C 013	-	23D84665F01	CAPACITOR	10UF-25V	U 020		51-84118K76	INTEGRATED CIRCUIT	
C 014	-	23D84665F01	CAPACITOR	10UF-25V	U 022		51-84118K25		
C 016	. .	23D84665E01	CAPACITOR	10UF-25V	U 023	-	51-84118K25	INTEGRATED CIRCUIT	
C 017	-	23D84665F01	CAPACITOR	10UF-25V	U 024	-	51-80345A12	INTEGRATED CIRCUIT	
C 018	-	23D83441B15	CAPACITOR	1.0UF-20-35	U 025		51-84118K32	INTEGRATED CIRCUIT	
000	•	23D83441B15	CAPACITOR	1.0UF-20-35	U 027	-	51-80346A59S	INTEGRATED CIRCUIT	
C 021		21082372009	CAPACITOR	1UF80-20-25	U 028	-	51-84118K77	INTEGRATED CIRCUIT	
C 022	-	21D82428B62	CAPACITOR	.01UF80-20-200	U 029		51-84118K63	INTEGRATED CIRCUIT	
C 023	-	21D82187B14	CAPACITOR	1000PF-10-100	U 031		51-803464595		
C 024		0K16-104	CAPACITOR	.1UF-20-16	U 032	_	51-84118K17	INTEGRATED CIRCUIT	
C 026		21D84494B40	CAPACITOR	21PE-5-500	U 033	1	51-84118K13	INTEGRATED CIRCUIT	
C 027	-	21D84494B40	CAPACITOR	21PF-5-500	U 034	• -	51-5467G01	INTEGRATED CIRCUIT	
C 028		21D84494B12	CAPACITOR	220PF-5-500	36	- - -	1-80346A62	INTEGRATED CIRCUIT	
CR002		48-86850C47 48-86850C47	DIODE		VR001	1	48-84302A09	DIODE, ZENER	6.2V-54
CR003	-	48-84463K02	DIODE		VR002		48-84302A09	DIODE, ZENER	6.2V-54
L 001	-1	MS90538-08	COIL	68UH	100 A	-4	48-80346A06	CRYSTAL	4.0MHZ
L 002	-	MS90538-08	COIL	HU89					
		48R00869571	TRANSISTOR						
R 001	-	6S124A85	RESISTOR	33K-5-1/4					
R 002	-1	6S124A61	RESISTOR	3.3K-5-1/4					
R 003		6S124A61	RESISTOR	3.3K-5-1/4					
R 004	• _	65124A/3	RESISTOR	10K-5-1/4					
R 006		6S124A57	RESISTOR	2.2K-5-1/4					
R 007	-	6S124A67	RESISTOR	5.6K-5-1/4					
R 008		6S124A67	RESISTOR	5.6K-5-1/4					
R 012		6S124A73	RESISTOR	2.2K-5-1/4 10K-5-1/4					

Microprocessor/Character Generator A9 RTC-4014A

SECTION 16

HIGH VOLTAGE POWER SUPPLY (A10)

16-1. General. Bias and drive voltages for the CRT are supplied by the High Voltage Supply. The supply converts a nominal 8 VDC input level to output voltages of +4 kV and -2 kV. Circuits for low voltage control of the intensity and focus grids is also provided. A block diagram of the High Voltage Power Supply is shown in figure 16-1 and its schematic in figure 16-2.

16-2. High Voltage Supply. A nominal 8 VDC level at the center tap on the high voltage transformer is switched through the primary winding by the chopper at a 20 kHz rate. Q4/Q6 BASE DRIVE signals from the Low Voltage Supply drive the Chopper circuitry. The secondary of the transformer is a 1 KV winding that is quadrupled to a nominal +4 kV and is doubled to a nominal -2 kV. A separate 6.3 V winding provide the CRT heater drive.

16-3. The-2 kV is regulated by comparing a sample of that voltage to the 7.9VREF signal. The resultant error signal (HV CONTROL) controls the level of the DC input to the high voltage transformer.

16-4. A Bias Divider circuit on the primary center tap provides the operating potential for the Q4/Q6 drive winding in the low voltage power supply.

16-5. Intensity and Focus Control. An 87V zener diode and resistive divider provide the intensity and focus voltages. The modulator circuits provide variable output voltages within their bias ranges under the control of the low voltage INTENSITY/FOCUS TV inputs.

16-6. DC control loops are utilized to stabilize the gird and focus voltages. For the intensity circuit the INTENSITY SAMPLE and HV REF signals are compared, on the Scope Amplifier Module, with the control input. The result of the comparison is the INTENSITY TV drive signal to the modulator. The focus voltage is controled in the same way, except the HV REF signal is not used in the comparison.



Figure 16-1. High Voltage Power Supply A10 Block Diagram High Voltage Power Supply A10 Parts Location

High Voltage Power & Regulator A10A1



l







SCOPE-ZAxis/Focus/Int Control A10A3



P 001 1 P 002 1 P 003 1 P 004 1 P 005 1	P 001 1 P 002 1 P 003 1 P 004 1	P 001 1 P 002 1 P 003 1	P 001 1	P 001 1	B 001	0 011	C 041 1	C 040 1	C 024 1	C 023 1	020 1		0.015 1	046	045 2		044 2	043 1	240		041 A	040 A	039 A	038 A	03/ A	000	360	035	034 A	033 A	032	031	620	020	120	020	026	025	024	023	022	021	020	018	017	010	016	015	013	012	011	010	600	800	4 007	4 900	cun	004	004	003	002	001	No. R	Find					
30-P06844R00		640440-5	1-640440-0	2-6076	DE-8b	2499-003X3W	2400 003VEW	2400-002XEM	2499-003X5W	2499-003X5W	2499-003X5W	2499-0U3X5W		29-14155A02	MS3367-4-9	10100000-00	MS35333-36	MS51848-45	8104-A-0440-	MI20033/9-10-	M03053/5-10/	G-649	LL LL	R M23053/5-103	R M23053/5-103			E01 A070	ŭ	U.	MS77068-1	NAS1746-3	SW-4	14-15141A01	MSJ3549-262	NO27 103-0	MS27183-5	MS35338-41	MS35206-230	MS35206-236	MS35489-6	42-14060A01	F1-10-106-12	7	5 MS35206-213	4503-440-A-9	40-1004414	A3 1505409-1	MC2EADD 1	MS35640-040	MS97183_3	MS35338-40	MS35206-216	29-15106A51	R 11-14167A01	R SN63WRMAR	15-80335A29	12MC0C00-12		DTD 4004A	RTP-4002A	RTP-4003A	ġ	ty. Part No.		п	Hign voltag		
1 LEAD ASSY,HV		CONNECTOR,5 PIN	CONNECTOR, 10 PIN	CONNECTOR, CRT	CONNECTOR	JUZAA CAPACITOR	MARA CAPACITOR		10944 CAPACITOR	502AA CAPACITOR	502AA CAPACITOR	JUZAA CAPACITOR		TERMINAL	STRAP	WASHER	WASHED	WASHER	10A SPACER	-9 INSULATING SLEEVING			TAPE	-9 INSULATION SLEEVING	INSULATION SLEEVING	INSULATION TAPE, MY	WIRE, HIGH VULTAGE		WIRF	CABLE, TWISTED PAIR	TERMINAL, LUG	FERRULE,SOLDER	WASHER, SHOULDER	INSULATOR, PLATE	NUT, HEX	WASHER, FLA		WASHEDLOOK	SCREW	SCREW	GROMMET	CLAMP ASSEMBLY, CO	INSERT SHEET EDGE	WIRE	SCREW, PH	SPACER, HEX-M/F		GHUMMEI		NIT HEY	WASHED ELAT	WASHEDLOOK	SCREW PH	TERMINAL	INK	3 SOLDER	COVER, HV SUPPLY	CHASSIS HV SUPPLY			HV SI IPPI V-PWB ASSI	HIGH VOLTAGE REGU		Nomenclature		TP-1001A	e Power Supply AT)	
						5000PF-GMV-500	5000PF-GMV-500	DOG-AWEHADDOG	EDOODE CHINA 200	5000PF-GMV-500	5000PF-GMV-500	5000PF-GMV-500	002	256	NATURAL	NU.4	NO.	NO.4	4-40X.375	3 .125 WHT			NATIIRAI	3 .093 WHT	G CL1.093 ID	LAR 1IN YELLOW		11144 42	24 WILT	24	4				6-32	NO.6	NO.0	0-027.000	6-32X 500	6-32X1.500		ONNEMALE-SCREWLOCK	6-32	22 WHT	4-40X.250		4-4UX.3/5		4-40	NO.4	NO.4		4-40X 438		BLACK				VIN			LATO		Part Value			C		
																																							R 174	R 173	R 169	R 115	R 114	R 113	R 111	R 110	H 109	H 108	R 106	C 118	C 117	0116	C 115		C 114	C 111	005	004	003	002	100		No.	Find					
																																					-	• -	. .	-	-	-	-	-	-	-	-		-		-1			• -	-	-	AR	AR	AR	AR			Req.	Qty.			I		
	a second s	h.		12																																	51-80345A02	06-10621056	DE TOPPIDED	66104479	CGH-1	6S124A01	6S126A25	6S124B30	06-10621E85	06-10621E85	06-10621E17	18D83452F32	06-10621E85	21D82187B14	30GA-027	21D83596E19	21082372009		CM04ED560 In2	21D83596E19	F01A070		11-14167A01	SN63WRMAP3	84-P00378N001			Part No.		RTP	ah Voltage Re		
	States I have been and the second sec	6			うのう		Start of the	4				1				X	1												-13								INTEGRATED CIRCUIT	RESISTOR4	HESISTOH	RESIGIOR		BESISTOR	RESISTOR	RESISTOR	RESISTOR	RESISTOR/8	RESISTOR	RESISTOR, VARIABLE	RESISTOR0-100	CAPACITOR	CAPACITOR	CAPACITOR	CAPACITOR			CAPACITOR	WIRE, HIGH VOLTAGE	WIRE, BUS	INK	SOLDER	PWB, HIGH VOLTAGE FR			Nomenclature		-4003A	gulator Card A10A		
					/	A Particular of the	and and a second	1 1										5											A2 (HV PWB ASSY)									46.4K-1-1/8	10K-5-1/4	20M-1-1	10-0-1/4	10 E 1/4	100-5-1	2.2M-5-1/4	1M-1-1/8	1M-1-1/8	196K-1-1/8	10K	1M-1-1/8	1000PF-10-100	27PF-10-3KV	.01UF80-20-3KV	.1UF80-20-25	00-1-2-200		011 JE80-20-3KV		24	BLACK		EG			Part Value			-		
			Ī		/																					\ \		1 1	04 A1 (F									R 217	R 216	R 212	CH216		CE212	CR211	CR210	CR209	CR208	C 213	C 212	C 209	C 205	C 204	C 203	C 202	000	006	005	004	003	002	001		No.	Find					
のための時間になったいとの方をする		-			P	11/1	11	1	1	1					100		No.					The second		1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1		L			ATOR ASSYN MO											-					. .	-	-	1	-1	-1	-	-	1	-	1		AR	AR	AR	AR	-		Req.	Qtv.					
					4				(NT N											1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1	ドーノノ							A3 (2-AXIS)III ATOR ASSY)									6S125B22	6S124A73	6S124A73	48-80345A63	48-80345A63	40-000404000	48-80345463	48-80345463	48-80345A63	48-80345A63	21D83596E19	21D83596E19	60GA-D47	21D83596E19	21D83596E19	21D83596E19	21D83596E19	GE4004	DEJOOL		F01A070	11-14167A01	SN63WRMAP3	84-P00448N001			Part No.		ra remover nem	High Voltage		
																																						RESISTOR	RESISTOR	RESISTOR	DIODE, HIGH VOLTAGE	DIODE, HIGH VOLTAGE					DIODE HIGH VOLTAGE	CAPACITOR	CAPACITOR	CAPACITOR	CAPACITOR	CAPACITOR	CAPACITOR	CAPACITOR	TRIMER		COMPOSIND	WIRE HIGH VOI TAGE	INK	SOLDER	PWB, HIGH VOLTAGE SL			Nomenclature	1 400274		Sunnly PWR A10A		
																																						1M-5-1/2	10K-5-1/4	10K-5-1/4								01UF80-20-3KV	.01UF80-20-3KV	.0047-6000	.01UF80-20-3KV	.01UF80-20-3KV	.01UF80-20-3KV	.01UF80-20-3KV					BLACK		P			Part Value		1	U		
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																				Con Inter	14. Y 19. 1	CNYING	6S124A73	06-10621052		CGH-1	6S124B22	6S124A97	6S124B16	6S124A97	164471 CO	65124407	6S124A29	6S124A29	6S124B22	6S124A97	CGH-1	6S124B22	6S124B22	6S124A89	6S124B18	00120024	BC 105B04	6S125R22	6S125B24	48-80341A46	48-80341A45			48-80345AR7	48-80345	21D83596E19	21D82428B24	21082428862			RTV3145		11-14167A01	SN63WRMAP3	84-P00380N001			Part No			7 Avia/Eagura		
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																							10K-5-1/4	42.2K-1-1/"	20191-1-1	20M-1-1	1M-5-1/4	100K-5-1/4	560K-5-1/4	100K-5-1/4	100K-5-1/4	100/ 5 1/4	150-5-1/4	150-5-1/4	1M-5-1/4	100K-5-1/4	20M-1-1	1M-5-1/4	1M-5-1/4	47K-5-1/4	680K-5-1/-	1.2M-5-1/2	IM-9-1/2	11.611-0-1/2	1 04-5-1/0					150V-55.	87V-55	01UF80-20 7K	.01UF80-20-50	.01UF80-20	1 HW 22				BLACK					Dort V			1000		

Figure 16-4. High Voltage Power Supply

640440-5 30-P06844R001 48R869627 48R869627 24-P00325N001

CONNECTOR,5 PIN LEAD ASSY.HV TRANSISTOR TRANSISTOR TRANSFORMER,HV

P3 C15 8521-54

C C41

6 PS CABLE ASSY

2888

80-1142

Figure 15-3. High Voltage Power Supply Parts

Locator

SECTION 17

RF INPUT MODULE (AH)

17-1. General. The RF Input Module is subdivided into three isolate circuits; input protection and power meter, wideband amplifier and frequency converter and duplex generator. A block diagram of the RF Input Module is shown in figure 17-1 with its schematic shown in figure 17-2.

17-2. Input Protection and Power Meter. RF power to and from the system pass through this section to a common input/output RF connector (RF In/Out) attached to the module. In the generate or monitor operating modes the input protection relay is switched so that a low-loss 50-ohm path exists through the module. When the power monitor mode is selected, the WATT MTR EN line switches the relay so that the input is connected to a 50 ohm power termination. A detector across a portion of the load provides a DC level proportional to the input RF level. This level isamplified and made available to the system processor for the determination of input power. A thermal sensor monitors the load temperature and signals the processor when safe operating limits are exceeded. The processor in turn warns the operator that the RF input to the unit must be removed to prevent permanent damage.

17-3. If power in excess of 200 mW is applied to the system while operating in the 50 ohm load, protecting the system. A signal line (INPUT PROTECT ACT) to the processor results in an audiable and visual warning to the operator that the unit is in a protected mode. The warning ceases and normal operation resumes if the RF input is removed or if the power monitor mode is selected.

17-4. Wideband Amplifier and Frequency Converter. The wideband amplifier provides a leveled RF output from-3dBm to+13dBm in the generate mode and a +7dBm LO drive in the monitor modes over the 10 KHz to I GHz frequency range. Primary components of the leveling loop are; the input VCA (Voltage Controlled Attenuator), the output level detector, and the level comparator. A level control voltage, proportional to the desired output level, is compared to the actual output level as determined by the level detector. The result of the comparison steers the VCA maintaining the detected output level equal to the requested output level. In the generate mode the control voltage is obtained from the front panel RF level control (AM Mod + DC REF). For generate AM, the modulation signal is summed with the DC control level, causing the RF output level to follow the modulation signal. Also, in the generate mode the signal from the output level detector(CARRIER + MOD LVL) is made available for the determination of RF output power and percent of AM. A fixed reference voltage is switched to the level control input in the monitor modes giving a leveled +7dBm local oscillator drive.

17-5. The VCA on the wideband amplifier board covers the frequency range from 1 MHz to 1 GHz. For frequencies below 1 MHz, the VCA select circuit clamps the VCA in the minimum attenuation position and enables a low frequency VCA in the RF Synthesizer. Coincident with the enabling of the low frequency VCA, the time constant of the output RF level detector is increased assuring proper operation down to 10 kHz.

17-6. The wideband amplifier output is relay switched between the local oscillator port of the input mixer for the monitor and generate DSBSC modes, and the RF attenuator for the generate mode. An RF sample from the mixer local oscillator output terminal, at a nominal level of -20dBm, is provided to the duplex generator.

17-7. The frequency converter section consists of the input mixer, the first IF amplifier, and IF filters. In the monitor mode the desired signal is converted to 10.7 MHz by the input mixer. A two-pole input filter, IF amplifier, and a four-pole output filter select the 10.7 MHz component at the mixer output. The 10.7 MHz IF **output of** the converter is applied to the receiver module.

17-8. For DSBSC generation the modulation audio is applied to the IF port of the input mixer through an isolation network. With the output of the wideband amplifier switched to the local oscillator port, a DSBSC signal is present at the RF port. Switching the Step Attenuator to the RF output port makes the DSBSC signal available at the RF output.

17-9. Duplex Generator. The Duplex Generator output is a frequency component that is offset from the system monitor frequency by 0 to 10 MHz or by 45 MHz. The offset is obtained by mixing the -20dBm local oscillator signal from the wideband amp, which is already offset by 10.7 MHz, with a signal frequency from 10.7 MHz to 0.7 MHz or 34.3 MHz.

17-10. For the 34.3 MHz mixing signal, a single VCO is used. Tuning of the VCO is with the OFFSET FINE TUNE line from the front panel. Frequency modulation of the VCO is implemented by suming the OFFSET MOD signal with the tuning voltage.

17-11. For the 0.7 MHz to 10.7 MHz mixing signal a VCO with a frequency range from 35 MHz to 45 MHz is mixed with the 34.3 MHz VCO. The 35-45 MHz VCO is tuned by the OFFSET COARSE TUNE line from the front panel.

17-12. A sample of the offset frequency is made available to the frequency counter on the OFFSET FREQ line. The processor uses the frequency information to calculate and display the actual duplex frequency.

Figure 17-1. RF Input Module A11 Block Diagram





Figure 17-2. RF Input Module A11 Schematic Diagram (RTC-1003A)







Figure 17-3. RF Input Module Parts Locator

Power Meter Protection A11A1










Figure 17-4. Wideband Amplifier Card A11A2 Schematic Diagram (RTC-4015A)



Figure 17-5. Offset Generator Card A11A3 Schematic Diagram (RTC-4016A)



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VOTES :

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				2499-003X5W50	2499-003X5W50	2499-003X5U10	2499-003X5W50	2499-003X5W50	2499-003X5W50	2499-003X5W50	2499-003X5W50	2499-003X5W50	58-P00312N001	58-80335 447	RTC-4016A	RTC-4015A	RTC-4061A	03-15013G09	5610-21-31	MS24693-S3	64-P04136T001	12-15013000	SN62WRMAP3	M23053/5-104-9	32-P06853R001	26-P00346N001		813		M23053/5-103-9	MS35206-215	G-642	29-15122A05	42-P06849R001	43-P06840R001	29-14070A91	07-P00318N001	8-2	MS35338-41	NAS620C6	MS35206-227	MS24693-S1	2053-440-SS-20	07-P00208N001		30-84421F13	11-14167A01	NAS620C2	MS35338-39	MS27183-3	MS35338-40	0541008380	3-134212	MS35206-213	15-80335A33	15-80335A32	27-80335A30		Part No.	R
				2AA CAPACITOR	2AA CAPACITOR	2M CAPACITOR	2AA CAPACITOR	TERMINATION, RF	ATTENHATOR	OFFSET GENERATOR	RF CONV/WB AMPL P	RF PROTECTION & PV	SCREW	WASHER	SCREW	COVER.STIFFENER		SOLDER	INSULATION SLEEVIN	EMI GASKET	SHIELD, AT2 CHEM MII	INSULATION SLEEVIN	WIRE BUS	TAPE	INSULATION SLEEVIN	SCREW	COMPOUND THERMAL	TERMINAL,LUG	STRAP, CLAMP, CABLE	SPACER, CONNECTOR	TERMINAL	BRACKET CHOKE MNT	CABLE ASSEMBLY	WASHER,LOCK	WASHER, FLAT	SCREW	SCREW,FH	SPACER	BRACKET, FRONT ATT	WIRE	CABLE, RF	INK	WASHER, FLAT	WASHER,LOCK	WASHER, FLAT	WASHER,LOCK	SCREW, FH SWAGEFOH	SCREW, THD FORMING	SCREW,PH	COVER, MODULE-RF		HOUSING, RF FRONTEN		Nomenclature	FC-1003A					
				5000PF-GMV-500	5000PF-GMV-500	1000PF-20-500	5000PF-GMV-500	5000PF-GMV-500	5000PF-GMV-500	5000PF-GMV-500	5000PF-GMV-500	5000PF-GMV-500	500HM			WB A	VR M	2-56X.187	N0.4	4-40X5/16	F 0007-101	2-56Y 187		G .125 WHT		F	G 22 WHT	24	NATURAL	G .093 WHT	4-40X.375						6		NO.6	NO.6	6-32X.312	4-40X3/16			22 WHT	WHITE	BLACK	NO.2	NO.2	NO.4	NO.4	M 4-40X.250	4-40X.312	4-40X.250	PROTECTION-WATTMETI	WIDE-BAND AMPL.			Part Value	
		C 121	C 120	-0 119	C 118	C 115		C 113	C 112	C 111	C 110	C 109	C 108	C 106	C 104	C 103	C 102	C 101	007	006	005	004	003	001		NO.	Find					5 013	RT001	P 002	P 001	L 011	L 009	L 008	L 007	L 006	L 005	L 003	L 001	J 007	J 005	J 002	J 001	FL014	FL013	FL012	FL011	FL008	FL007	FL006	ER C 107	C 022	C 021	C 020	Find No.	
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		21D82187B14	VJ0805A220JF	V.IDR05A220.IF	M30014/01-1321	VJU8U5A2ZUJF	MIDDOFA 200 IE	M39014/01-1321	VJ0805A220JF	23D84762H14	VJ0805A220JF	VJ0805X222MF	VJ0805A220JF	M39014/01-1321	21C82372C09	21C82372C09	21D83596E32	21D83596E32	26-P0034/N001		11-14167A01	SN62WRMAP3	SN63WHMAP3	84-P00459N001			Part No.			RTC-4	ut/Wattmeter F	40-170033014001	06C83600K05	01-80304A54	01-80304A53	MS75083-3	MS75083-3	MS75083-3	MS75083-3	MS75083-3	MS75083-5	25-83127G01	WS00530-07	9C84135B02	9C84135B02	2051-1201	91-00340A12 M39012/04-0002	91-80346A12	91-80346A12	91-80346A12	91-80346A12	91-80346A12	91-80346A12	91-80346A12	VJ0805X222MF	2499-003X5W502	2499-003X5W502	2499-003X5W502	Part Number	
		CAPACITOR	CAPACITOR	CAPACITOR	CAPACITOR	CAPACITOR	CARACITOR	CAPACITOR	CAPACITOR	CAPACITOR	CAPACITOR	CAPACITOR	CAPACITOR	CAPACITOR	CAPACITOR	CAPACITOR	CAPACITOR	CAPACITOR	WIDE	INSULATION SLEEVIN	INK	SOLDER	SOLDER	PWB, RF PROT & PWI			Nomenclature)61A	rotect PWB A11		THERMISTOR	CABLE ASSEMBLY	CABLE ASSEMBLY	COIL		COL	COIL	COIL	COIL	CHOKE		JACK, PHONO	JACK, PHONO	CONNECTOR, RF	CONNECTOR, RF	FILTER	FILTER	FILTER	FILTER	FILTER	FILTER	FILTER	CAPACITOR	AA CAPACITOR	AA CAPACITOR	AA CAPACITOR	Nomenclature	>
		1000PF-10-100	22PF-5-100	20PE-5-100	1085-10-200	22PF-5-100	10FF-10-200	10PE-10-200	22PF-5-100	.47UF-20-50	22PF-5-100	2200PF-20-100	22PF-5-100	10PF-10-200	1UF80-20-25	1UF80-20-25	1100PF-5-200	1100PE-5-200	1EM-	VG 24 WHT	BLACK			A WE			Part Value				A1			16PIN-101N-END	24PIN-181N-SIDE	.15UH-10	1511H-10	.15UH-10	.15UH-10	.15UH-10	.22UH		470UH				TYPE N								2200PF-20-100	5000PF-GMV-500	5000PF-GMV-500	5000PF-GMV-500	Part Value	
	H 143	R 142	R 141	R 140	R 139	R 138	R 137	R 135	R 134	R 133	R 132	R 129	R 128	R 126	R 125	R 124	R 123	R 122	ج 121 F	÷ ۱20 ج	119	H 114	R 113	R 112	- R 111	R 110	R 109	- R 107	R 106	R 106	H 106	R 106	R 105	R 105	R 105	R 103	R 102	R 101	Q 106	Q 105	0 104	0 103	L 101	K 102	K 101	CR114	CR112	CR111	CR110	CR109	CR107	CR105	CR104	CR103	C 125	C 124	C 123	C 122	Find No.	
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	18D83452F09	06D83175C51	6S124A73	6S124A49	6S124A41	6S124A41	6S124A49	6S124A73	6S124A65	6S124A51	6S125A83	06D83175C03	06083175003	06D83175C03	06-10621C19	06-10621C19	06D83175C90	18D83452F09	6S124A71	6S124A87	6S124A75	6S124A/5	6S124A89	6S124A89	6S124A73	6S124A65	6S124A53	6S124B06	6S124A25	6S124A24	6S124A21	6S124A23	6S124A87	6S124A83	6S124A85	6S124B22	6S124A66	6S124A81	48-6123A22	48-80345A51	48R00869570	48-80345A51	24C83961B01	01-80304A41	01-80304A41	48-84463K02	48-80345A65 48-84463K02	48-80345A65	48-80345A65	48-80345A64	48-80345A64	48-80345A64	48-84463K02	48-84463K02	23U84/62H14 MRD702	21D82187B14	21-P14473A127.	VJ0805A220JF	Part No.	
	RESISTOR, VARIABLE	RESISTOR	RESISTOR	RESISTOR	RESISTOR	RESISTOR	RESISTOR	RESISTOR	RESISTOR	RESISTOR	RESISTOR	RESISTOR	RESISTOR	RESISTOR	RESISTOR	RESISTOR	RESISTOR	RESISTOR, VARIABLE	RESISTOR	RESISTOR	RESISTOR	BEGISTOR	RESISTOR	RESISTOR	RESISTOR	RESISTOR	RESISTOR	RESISTOR	RESISTOR	RESISTOR	RESISTOR	RESISTOR	RESISTOR	RESISTOR	RESISTOR	RESISTOR	RESISTOR	RESISTOR	TRANSISTOR	TRANSISTOR	TRANSISTOR	TRANSISTOR	CHOKE	RELAY ASSEMBLY	RELAY ASSEMBLY	DIODE	DIODE	DIODE	DIODE	DIODE		DIODE	DIODE	DIODE	DIODE	CAPACITOR	CAPACITOR	CAPACITOR	Nomenclature	
	IK	37.4K-1-1/4	10K-5-1/4	1K-5-1/4	470-5-1/4	470-5-1/4	1K-5-1/4	10K-5-1/4	4.7K-5-1/4	1.2K-5-1/4	27K-5-1/2	10K-1-1/4	10K-1-1/4	10K-1-1/4	1780-1-1/8	1780-1-1/8	8.06K-1-1/4	1K	8.2K-5-1/4	39K-5-1/4	19K-5-1/4	10-5-1/4	47K-5-1/4	47K-5-1/4	10K-5-1/4	4.7K-5-1/4	1.5K-5-1/4	220K-5-1/4	100-5-1/4	91-5-1/4	75-5-1/4	82-5-1/4 NOMINAL	39K-5-1/4	27K-5-1/4	33K-5-1/4 NOMINA	5. IX-5-1/4 1M-5-1/4	5.1K-5-1/4	22K-5-1/4																	.4/UF-20-50	1000PF-10-100	.01UF-20-50	22PF-5-100	Part Value	
				C 222	C 221	C 220	C 219	C 218	C 217	C 216	C 215	C 214	C 213	C 212	C 211	C 210	C 208	C 207	C 206	C 205	C 204	C 203	C 201	014	014	013	012	011	009	008	007	06	004	003	.L .910	002	001	NO.	Find				RF		VR104	VR102	VB101	U 102	U 101	RT101	R 152	R 151	R 150	R 149	R 147	R 146	R 145	R 144	Find No.	
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(Sheet 1)	RF Input N	ASSEMBLY Pr		54-803-004-102P	VJ0805X681MF	VJ0805X271MF	VJ0805X222MF	VJ0805X681MF	VJ0805X222MF	VJ0805X271MF	VJ0805X681MF	VJ0805X222MF	23D83441B15	23D83441B15	23D83441B15	23D83441B15	23D83441815 V 10805Y222ME	23D83441B15	23D83441B15	VJ0805X681MF	VJ0805X222MF	VJ0805X391MF	V.IORO5X222MF	26-P04143T001				10123033/3-103-9 30-84421F13	26-P06855H001	RTV3145	26-P00235N001	26-P00234N001	SN63WBMAP3	11-14167A01		SN62WRMAP3	84-P00453N001		Part No.	•		RTC-40	ter/Wideband ,		48-82556C15A	48-82556C15A	48-80345A84	51-80345A01	51R84320A80	6C83600K05	6S124A73	6S124A17	6S124A49	6S124A73	6S124A49	6S124A41	6S124A53	6S124A89	Part Number	
	Aodule	ARTS LIST		CAPACITOR	CAPACITOR	CAPACITOR	CAPACITOR	CAPACITOR	CAPACITOR	CAPACITOR	CAPACITOR	CAPACITOR	CAPACITOR	CAPACITOR	CAPACITOR	CAPACITOR	CAPACITOR	CAPACITOR	CAPACITOR	CAPACITOR	CAPACITOR	CAPACITOR	CAPACITOR	SHIELD	INSULATION SLEEVING	INSULATION SLEEVING	WIRE, BUS	CABLE RF		ADHESIVE, SILICONE	SHIELD, FILTER	SHIELD,CAN	SOLDER	INK		SOLDER	PWB, RF CONV/WB AMPLI		Nomenclature			15A	Amplifier PWB A1		DIODE, ZENER	DIODE ZENER	DIODE ZENER	INTEGRATED CIRCUIT	INTEGRATED CIRCUIT	THERMISTOR	RESISTOR	RESISTOR	RESISTOR	RESISTOR	RESISTOR	RESISTOR	RESISTOR	RESISTOR	Nomenclature	
				1000PF100-0-50	680PF-20-100	270PF-20-100	2200PF-20-100	680PF-20-100	2200PF-20-100	270PF-20-100	680PF-20-100	2200PF-20-100	1.0UF-20-35	1.0UF-20-35	1.0UF-20-35	1.0UF-20-35	22000E-20-35	1.004-20-35	1.0UF-20-35	680PF-20-100	2200PF-20-100	390PF-20-100	2200PE-20-100		24 WHT	20 WHT	22	WHITE	197 W/LIT					BLACK			ת		Part Value				1A2		5.1V-54	5.1V-54					10K-5-1/4	47-5-1/4	1K-5-1/4	10K-5-1/4	1K-5-1/4	470-5-1/4	1.5K-5-1/4	47K-5-1/4	Part Value	

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				L 208	L 207	L 206	L 204	L 203	L 202	L 201	K 202	K 201	J 004	J 003	FL202	CH226	CR225	CR224	CR223	CH221	CR217	CR211	CR210	CR209	CR208	CR206	CR205	CR204	CROUZ	CR201	C 253	C 252	C 251	C 249	C 248	C 247	C 245	C 244	C 243	C 242	C 240	C 239	C 238	C 236	C 235	C 234	C 232	C 231	C 230	C 229	C 227	C 226	C 225	C 223	NO.	Find
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Sheet 2)	RF Input Module			24C83961B01	24C83961B01	24C83961B01	24C83961B01	24C83961B01	24C83961B01	24C83961B01	228-4-1A	228-4-1A	2004-7188	2004-7188	SFE-10 7MA-5A	48-84463K02	48-84463K02	48-84463K02	40-04403NU2 48-84463K02	48-80310A74	48-80310A74	48-84463K02	48-84463K02	48-84463K02	48-80345A62	48-80345A62	48-84463K02	48-84463K02	48-84463K02	48-84463K02	23D83441B15	VJ0805A221GF	23D83441B15	23D84665F02	VJ0805X222MF	23D84665F01	21C82372C09 23D84665F01	21D82428B36	21C82372C09	21C82372C09	21D82428B62	23D83441B15	21C82372C09	21D84494B06	21D84494B24	21D82428B62	CV31D350	21D82428B62	21D82187B08	21D84494B40	21D84494B42	21D82187B08	21C82372C09	23D83441815		Part No.
	Ū		01	CHOKE, RF	CHOKE, RF	CHOKE, HF	CHOKE, RF	CHOKE, RF	CHOKE, RF	CHOKE RE	RELAY	RELAY	CONNECTOR	CONNECTOR	FILTER	DIODE	DIODE	DIODE	DIODE	DIODE	DIODE	DIODE	DIODE	DIODE	DIODE	DIODE	DIODE	DIODE	DIODE	DIODE	CAPACITOR	CAPACITOR	CAPACITOR	CAPACITOR	CAPACITOR	CAPACITOR	CAPACITOR	CAPACITOR	CAPACITOR	CAPACITOR	CAPACITOR	CAPACITOR	CAPACITOR	CAPACITOR	CAPACITOR	CAPACITOR	CAPACITOR	CAPACITOR	CAPACITOR	CAPACITOR	CAPACITOR	CAPACITOR	CAPACITOR	CAPACITOR		Nomenclature
																															1.0UF-20-35	220PF-2-100	1.0UF-20-35	15UF-25V	2200PF-20-100	10UF-25V	10UF-25-25	2000PF-10-200	1UF80-20-25	.1UF80-20-25	.01UF80-20-200	1.0UF-20-35	.1UF80-20-25	120PF-5-500	39PF-5-500	21PF-5-500 .01UF80-20-200	9 TO 35PF-200	.01UF80-20-200	220PF-10-500	330PF-5-300	27PF-5-500	220PF-10-500	.1UF80-20-25	1.0UF-20-35		Part Value
				R 266	R 265	R 262	R 260	- R 259	R 258	R 257	H 252	R 251	R 250	R 245	R 243	H 239	R 233	R 232	R 231	H 226	R 225	R 224	R 223	R 222	R 221	R 219	R 218	R 217	R 216	H 214	R 213	R 212	R 211	R 209 R 210	R 208	R 206	R 205	R 203	R 202	R 201	Q 212 O 213	Q 211	Q 201	L 222	L 221	L 220	L 218	L 217	L 216	L 214	L 213	L 212	L 211	L 209	NO.	Find
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				6S124A67	6S124A25	6S124A25	6S124A61	6S124B22	18-80346A20	6S124B10	18D83452F15	6S124A89	6S124B14	6S185A49	6S185445	6S124A81	6S124A03	6S124A03	6S124A97	6S124A67	6S124A49	6S124A49	6S124A09	6S124A25	6S124A73	6S124A97	6S124A89	6S124B06	6S124A72	6S124B22	6S185A25	6S124A61	6S124B02	6S124A73 6S124A43	6S124A85	6S124A37	6S124A25	6S124A03	6S124A55	6S124A01	48R00869570 48-80345A42	48R00869571	48R00869570	MS75084-4	DD-1.00-5	24C83961B01	24-P00255N001	24-P00255N001	MS90541-03	24C83961B01 MS90541-03	MS90541-03	24E83879G10	MS75084-10	24C83961B01		Part No.
				RESISTOR	RESISTOR	RESISTOR	RESISTOR	RESISTOR	RESISTOR, VARIABLE	RESISTOR	RESISTOR, VARIABLE	RESISTOR	RESISTOR	RESISTOR	RESISTOR	RESISTOR	RESISTOR	RESISTOR	RESISTOR	RESISTOR	RESISTOR	RESISTOR	RESISTOR	RESISTOR	RESISTOR	RESISTOR	RESISTOR	RESISTOR	RESISTOR	RESISTOR	RESISTOR	RESISTOR	RESISTOR	RESISTOR	RESISTOR	RESISTOR	RESISTOR	RESISTOR	RESISTOR	RESISTOR	TRANSISTOR	TRANSISTOR	TRANSISTOR	COIL	COIL	CHOKE	CHOKE	CHOKE	COIL	CHUKE,RF	COIL	CHOKE, VARIABLE	COIL, RF	CHOKE, RF		Nomenclature
				5.6K-5-1/4	100-5-1/4	4 7K-5-1/4	3.3K-5-1/4	1M-5-1/4	5K	3306-5-1/4	20K	47K-5-1/4	470K-5-1/4	1K-5-1/8	8.2K-5-1/4	22K-5-1/4	12-5-1/4	12-5-1/4	100K-5-1/4 22-5-1/4	5.6K-5-1/4	1K-5-1/4	1K-5-1/4	22-5-1/4	100-5-1/4	10K-5-1/4	100K-5-1/4	47K-5-1/4	220K-5-1/4	9.1K-5-1/4	1M-5-1/4	100-5-1/8	3.3K-5-1/4	150K-5-1/4	10K-5-1/4	33K-5-1/4	330-5-1/4	470-5-1/4	12-5-1/4	1.8K-5-1/4	10-5-1/4				2.2UH	1.0UH-5	.000	5000UH	5000UH	4700UH	4700UH	4700UH		6.8UH			Part Value
C 326	C 324 C 325	C 323	C 321	C 320	C 319	C 318	C 318	C 317	C 316	C 315	C 313	C 312	C 311	C 310	C 309	C 307	C 306	C 305	C 304	C 303	007	006	005	004	003	001		No.	Find					VR204	VR202	VR201	U 209	U 208	U 206	U 205	U 204	U 202	U 201	R 281	H 2/9	R 278	R 277	R 276	R 274	R 273	R 272	R 269	R 268	R 268 R 266	NO.	Find
-					1	S01	S01	_	_	-• -			-	-			-	_		. –	AR	AR	<u>ь</u>	AR	AR	3 -		Req.	Qty.					-			1	- -	• _•		- - .		-	<u>-</u>		-	-	- - -		-					neų.	Qty.
21D82428B62	21C82372C09 21D84494B37	21C82372C09	21C82372C09	21D82187B14	CV31A080	21D84494B06	21D84494B34	21D82428B62	21D82428B62	21082372009	21082372009	21D84494B37	21D82187B14	21D84494B37	2 1D84494830 CV31A080	21K840811	CV31A080	21D82428B62	21C82372C09	21002372009	30-84421F13	M23053/5-105-	26-P00345N00	SN63WRP3	3N63WHMAP3	84-P00458N00			Part No.		RT	Uttset Gene	04	48-83624E52	48-80345A92A	48-83461E13	51R84320A80	51-84887K73	51-80345A04	51-80345A35	51-80345A35	51-80345A34	51-80345A34	6S185A01	6S185A35	6S185A35	6S185A07	05124A05 18D83452F09	6S124A45	06-10621C63	06-10621878	6S124B16	6S124A89	6S124A89 6S124A67		Part No.
CAPACITOR	CAPACITOR	CAPACITOR	CAPACITOR	CAPACITOR	CAPACITOR, VARIABLE	CAPACITOR	CAPACITOR	CAPACITOR	CAPACITOR	CAPACITOR	CAPACITOR	CAPACITOR	CAPACITOR	CAPACITOR	CAPACITOR, VARIABLE	CAPACITOR	CAPACITOR, VARIABLE	CAPACITOR	CAPACITOR	CAPACITOR	CABLE, RF	9 INSULATING SLEEVING	SHIELD	SOLDER	INK	PWB OFFSET GENERAT			Nomenclature		C-4016A	rator PWB A11A3		DIODE,ZENER	DIODE, ZENER	DIODE,ZENER	INTEGRATED CIRCUIT	INTEGRATED CIRCUIT	INTEGRATED CIRCUIT	INTEGRATED CIRCUIT	INTEGRATED CIRCUIT	INTEGRATED CIRCUIT	INTEGRATED CIRCUIT	RESISTOR	RESISTOR	RESISTOR	RESISTOR	RESISTOR VARIABLE	RESISTOR	RESISTOR	RESISTOR	RESISTOR	RESISTOR	RESISTOR		Nomenclature
.01UF80-20-200	.10F80-20-25 11PF-5-500	.1UF80-20-25	.1UF80-20-25	1000PF-10-100	2 T0 8PF-350	120PF-5-500	68PF-5-500	.01UF80-20-200	.01UF80-20-200	1UF80-20-25	0111E80-20-25	11PF-5-500	1000PF-10-100	11PF-5-500	2 TO 8PF-350	10PF-N4/0	2 TO 8PF-350	.01UF80-20-200	1UF80-20-25	011 JEB0-20-20	WHITE ALLER OF SE	.187 WHT			BLACK	OR			Part Value															10-5-1/8	270-5-1/8	270-5-1/8	18-5-1/8	4.7K-5-1/4 1K	680-5-1/4	5.11K-1-1/8	681-1-1/8	560K-5-1/4	47K-5-1/4	47K-5-1/4 5.6K-5-1/4		Part Value
							Q 314	Q 313	Q 312	Q 311	0 340	Q 308	Q 307	Q 306	Q 305	Q 302	Q 301	L 308	L 307	- 306	L 304	L 303	L 302	L 301	L 2006	D 302	D 301	CR311	CR310	CR300	CR307	CR306	CR305	CR304	CR301	C 351	C 350	C 348	C 347	C 346	C 344	C 343	C 342	C 341	C 339	C 338	C 337	C 335	C 334	C 333	C 331	C 330	C 329	C 327 C 328	N.	Find
						-	• 1	-	-	. .			_	-			_	-		•		1	-	<u> </u>			-	-			· _	-	-			-	<u> </u>		-	-			-		•				-	-			-		ved.	Oty.
						48H008695/1	48R00869571	48R00869570	48-80345A53	48R00869571	48R00869570	48R00869570	48R00869571	48R00869570	48R00869570	48-80345A42	48-80345A42	MS90538-20	24C83961B01	MS/5084-13	MS75085-03	MS75085-03	MS75083-10	MS75083-13	48-80345A72 2004-7188	48-80345A72	48-80345A74	48-84463K02	48-84463K02	48-84463K02	48-84463K02	48-84463K02	48-84463K02	48-84463K02 48-84463K02	48-84463K02	21D82187B14	21D82187B14	23D84665F02	23D84665F01	21D82428B62	21C82372C09 21C82372C09	21D82428B62	21C82372C09	21082428862 21082372009	21D82428B62	21C82372C09	21082372009	21C82372C09	21C82372C09	21082372009	21C82372C09	21C82372C09	21D84494B42	21D82428B62 21D82428B09		Part No.
						TRANSISTOR	TRANSISTOR	TRANSISTOR	TRANSISTOR	TRANSISTOR	TRANSISTOR	TRANSISTOR	TRANSISTOR	TRANSISTOR	TRANSISTOR	TRANSISTOR	TRANSISTOR	COIL	CHOKERF	COIL	COIL	COIL			VARACTOR	VARACTOR	VARACTOR	DIODE		DIODE	DIODE	DIODE	DIODE	DIODE	DIODE	CAPACITOR	CAPACITOR	CAPACITOR	CAPACITOR	CAPACITOR	CAPACITOR	CAPACITOR	CAPACITOR	CAPACITOR	CAPACITOR	CAPACITOR	CAPACITOR	CAPACITOR	CAPACITOR	CAPACITOR	CAPACITOR	CAPACITOR	CAPACITOR	CAPACITOR		Nomenclature
																		220UH	39UH	12UH	47UH	47UH	.56UH	1 H												1000PF-10-)	1000PF-10-100	15UF-25V	10UF-25V	.01UF80-20-200	1UF80-20-	.01UF80-2C 0	.1UF80-20-25	01UF80-20_0	.01UF80-2C IO	.1UF80-20-25	111E80-20-	.1UF80-20-	.1UF80-20-^-	.1UF80-20-25	1UF80-20-	.1UF80-20-	27PF-5-500	.01UF80-20 0		Part Value

Find No.	Qty. Req.	Part No.	Nomenclature	Part Value	Find No.	Qty. Req.		Nomenclature	Part Va
Q 316	1	48R00869570	TRANSISTOR		R 370	1	6S124A63	RESISTOR	3 9K-5-1/4
R 301	1	6S124A73	RESISTOR	10K-5-1/4	R 371	1	6S124A97	RESISTOR	100K-5-1/4
R 302	1	6S124A89	RESISTOR	47K-5-1/4	U 301	1	51-06472A26	INTEGRATED CIRCUIT	1001(-3-1/4
R 303	1	6S124A97	RESISTOR	100K-5-1/4	U 302	1	51-80345A23	INTEGRATED CIRCUIT	
R 304	1	6S124A73	RESISTOR	10K-5-1/4	U 303	1	51-80346A04	MIXER	
R 305	1	18D83452F10	RESISTOR, VARIABLE	1K	VR001	1	48-84302A09	DIODE.ZENER	6 21/-5- 4
R 306	1	6S124A97	RESISTOR	100K-5-1/4					0.20-04
R 307	1	6S124A73	RESISTOR	10K-5-1/4					
R 309	1	6S124A89	RESISTOR	47K-5-1/4					
R 311	1	6S124A45	RESISTOR	680-5-1/4					
R 313	1	6S124A53	RESISTOR	1.5K-5-1/4					
R 314	1	6S124A51	RESISTOR	1.2K-5-1/4					
R 315	1	6S124A41	RESISTOR	470-5-1/4					
B 316	1	6S124A73	RESISTOR	10K-5-1/4					
B 317	1	65124449	RESISTOR	1K-5-1/4					
B 318	1	65124497	BESISTOR	100K-5-1/4					
B 310		65124473	RESISTOR	10K-5-1/4					
R 321	1	6S124A89	RESISTOR	47K-5-1/4					
B 322	1	6S124A65	RESISTOR	4 7K-5-1/4					
B 323	1	65124445	RESISTOR	680-5-1/4					
B 324	1	65124497	RESISTOR	100K-5-1/4					
B 325	1	65124497	RESISTOR	100K-5-1/4					
B 326		65124481	RESISTOR	100K-5-1/4					
B 327		691244.01	RESISTOR	22R=3=1/4					
D 328		66104441	REGISTOR	18-3-1/4					
B 200		60124841	RESISTOR	470-5-1/4					
F 220		05124A47	RESISTOR	820-5-1/4					
R 330		65124A51	RESISTOR	1.2K-5-1/4					
R 332	1	6S124A41	RESISTOR	470-5-1/4					
R 333		65124A/1	RESISTOR	8.2K-5-1/4					
R 334	1	6S124A41	RESISTOR	470-5-1/4					
H 335	1	6S124A57	RESISTOR	2.2K-5-1/4					
H 336	1	6S124A57	RESISTOR	2.2K-5-1/4					
H 337	1	6S124A57	RESISTOR	2.2K-5-1/4					
R 338	1	6S124A73	RESISTOR	10K-5-1/4					
R 339	1	6S124A49	RESISTOR	1K-5-1/4					
R 340	1	6S124A18	RESISTOR	51-5-1/4					
R 341	1	6S124A73	RESISTOR	10K-5-1/4					
R 342	1	6S124A73	RESISTOR	10K-5-1/4					
R 344	1	6S124A57	RESISTOR	2200-5-1/4					
R 345	1	6S124A67	RESISTOR	5.6K-5-1/4					
R 346	1	6S124A53	RESISTOR	1.5K-5-1/4					
R 347	1	6S124A29	RESISTOR	150-5-1/4					
R 348	1	6S124A42	RESISTOR	510-5-1/4					
R 349	1	6S124A49	RESISTOR	1K-5-1/4					
R 350	1	6S124A49	RESISTOR	1K-5-1/4					
R 351	1	6S124A49	RESISTOR	1K-5-1/4					
R 352	1	6S124A49	RESISTOR	1K-5-1/4					
R 353	1	6S124A49	RESISTOR	1K-5-1/4					
R 354	1	6S124A61	RESISTOR	3.3K-5-1/4					
R 355	1	6S124A49	RESISTOR	1K-5-1/4					
R 356	1	6S124A33	RESISTOR	220-5-1/4					
R 358	1	6S124A21	RESISTOR	68-5-1/4					
R 359	1	6S124A29	RESISTOR	150-5-1/4					
R 360	1	6S124A15	RESISTOR	39-5-1/4					
R 361	1	6S124A29	RESISTOR	150-5-1/4					
R 362	1	6S124A45	RESISTOR	680-5-1/4					
R 363	1	6S124A57	RESISTOR	2.2K-5-1/4					
R 364	1	6S124A57	RESISTOR	2.2K-5-1/4					
R 365	1	6S124A57	RESISTOR	2 2K-5-1/4			V660		т
B 366	1	6S124A73	RESISTOR	10K-5-1/4			A995	WIDLT FARTS LIS	1.
B 367	1	65124A49	RESISTOR	116-5-1/4					
B 368	1	65124457	RESISTOR	2 2K-5-1/4			RI	 Input Module 	
B 369	1	6S124A73	RESISTOR	104-5-1/4			(9	heet 3)	
		00124010	neolo I On	101-0-1/4			,0		

Part Value

P/O A11A1

SECTION 18

FRONT PANEL INTERFACE MODULE (A12)

18-1. General. Input buffers and output latches for front panel control and display interface to the processor are contained on the Front Panel Interface Module. Buffering and ranging circuits for the external scope vertical, SINAD, DVM, Frequency Counter, and external scope horizontal inputs is also contained on this module. A block diagram of the Front Panel Interface Module is shown in figure 18-1 with its schematic shown in figure 18-2.

18-2. Input Coupling and Ranging. Scope inputs to the Range Attenuator are from the front panel jack (EXT IN) or from the internal modulation sources (INT SCOPE TO RNG SW). An INT/EXT relay selects the input path. The external path may be AC or DC coupled and is also the path for external DVM, Frequency Counter, and SINAD inputs.

18-3. Four decades of attenuation from 1.0 to 0.001 are provided by the Range Attenuator. The input impedance of the attenuator is 1.0 megohm compensated for a bandwidth of 1 MHz. A unity gain buffer amp following the attenuator provides the drive for the DVM, Frequency Counter, and Scope Vertical Preamp circuits.

18-4. DVM Buffer. For DC measurements the DVM Buffer provides a 2-pole low pass filter with a minimum of 30 dB attenuation at 50 Hz. For AC measurements the bandwidth of the buffer is switched so that the attenuation at 10 kHz is less than 0.5 dB.

18-5. Frequency Counter Preamp. The Frequency Counter Preamp has sufficient gain for 30 mV rms sensitivity and provides hystersis for noise immunity.

18-6. Scope Vertical Preamp. A calibrated gain of 50 or a variable gain from 5 to 50 is provided by the Vertical Preamp. The gain is control led from the front panel. For vertical scope positioning the DC bias point of the preamp is controlled by the front panel position control. Deflection sensitivity at the VERT FROM RNG SW output is 0.5 volt per division.

18-7. Scope Horizontal Preamp. A fixed gain of 5 in the Horizontal Preamp gives a horizontal input sensitivity of 0.1 volt per division. Horizontal vernier gain is implemented on the front panel, and horizontal positioning on the Scope Amplifier module. Deflection sensitivity at the HORIZ TO SCOPE AMPL is 0.5 volt per division.

18-8. Control and Display Interface. Front panel control information is input to the processor in 4-bit groups through the AF control bus. Priority encoders convert the multiposition switches, scope horizontal, frequency scan, and RF step attenuator, to 4-bit codes. The processor sequentially addresses (AF ADRS BUS 0-3) each input buffer through the Address Decoder. Data in the selected buffer is then transfered to the processor on the AF DATA BUS 0-3 lines while the AF BUS EN 2 signal is low.

18-9. A three or four bit code for each LED display group is transfered from the processor to the display latch. The latched data is decoded and the indicated LED driver is enabled.

18-10. Two additional latches provide the processor control interface for the Range Attenuator, input switching, and DVM Buffer control.





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Figure 18-2. Front Panel Interface Module A12 Schematic Diagram (Sheet 1 of 2) (RTL-4045A)





Schematic Diagram (Sheet 2 of 2)

(RTL-4045A)

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	K 003	CR014	CR013	CR012	CR011	CR010	CROOM	CHOOS	CR005	CR004	CR003	CR002	CR001	C 036	C 035	C 034	C 033	C 039	C 029	C 028	C 027	C 026	C 025	C 024	C 023	C 021	C 020	C 019	C 018	C 017	C 016	C 014	C 013	C 012	C 011	C 010	C 007	C 006	C 005	C 004	C 003		006	005	004	003	001	NO.	Find	1			
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	80-803466401	40-04403702	48-84463602	48-84463K02	48-84463602	48-84463KU2	48-84463K02	23D83441B15	23D84665F01	23D84665F01	21D82187B28	21084494834	21D82187B07	23D84762H18	21C82372C09	21D84494B29	21C82372C10	21D82187B14	21082428836	210823/2009	21084494830	21D82428B62	21C82372C09	21C82372C05	21C82372C09	21082372009	21C82372C09	23D82397D23	23D82397D23	21082372009	CM06FD472J03	21D84494B19	21D82187B14	CM04ED330J03	21082428619 9300	21800401029	M23053/5-107-9		11-14167A01	SN63WRMAP3	84-P00496N001		Part No.				Front Par						
						DIODE	CAPACITOR	CAPACITOR	CAPACITOR	CAPACITOR	CAPACITOR	CAPACITOR	CAPACITOR	CAPACITOR	CAPACITOR	CAPACITOR	CAPACITOR	CAPACITOR	CAPACITOR	CAPACITOR	CAPACITOR	CAPACITOR	CAPACITOR	CAPACITOR	CAPACITOR	CAPACITOR	CAPACITOR	INSULATION SLEEVING	WIRE	INK	SOLDER	PWB, FRONT PANEL IN		Nomenciature			RTL-4045A	nel Intertace Modu															
	14-5000													1.0UF-20-35	10UF-25	10UF-25	390PF-10-500	68PF-5-500	470PF-10-500	47UF-20-10	.1UF80-20-25	10PF-5-500	.05UF-20-25	1000PE-10-100	.1UF80-20-25	1UF80-20-25	34PF-5-500	.01UF80-20-20C	.1UF80-20-25	.2UF80-20-25	1UF80-20-25	.1UF80-20-25	.1UF80-20-25	6.8UF-20-20	6.8UF-20-20	111580-20-200	4700PF-5-500	470PF-5-500	1000PF-10-100	33PF-5-500	1 7-6PE-250V	.02UF80-20-600	.375 WHT	22 WHT	BLACK		3		Part value				IE AIZ
R 037	B 035	H 034	D 034			H 030	R 029	R 028	R 027	R 026	R 025	R 024	R 023	R 022	R 021	R 020	R 019	R 017	R 016	R 015	R 014	R 013	R 012	R 011	R 010	R UU8	R 007	R 006	R 005	R 002	R 001	0 014	Q 013	Q 012	Q 011	0 010	0 008	Q 007	Q 006	Q 005	Q 004	0 002	Q 001	L 001	K 008	K 007	K 006	K 005	K 004		NO.	Find	
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6S124A65	6S124A65	6S124A65	05124A/3	65124A/3	051244/3	6S124A73	6S124A73	6S124A73	BS124473	6S124A73	6S124A73	6S124A73	6S124A73	6S124A73	40-000000A79 6S124A73	48-80335A79	48R00869570	48R00869570	48R00869571	48R00869570	48R00869570	48-80345A40	48R00869570	48R00869570	48R00869570	48R00869570	48R00869570	MS90540-08	80D84157B01	80D84157B01	80D84157B01	80-80346A01	00 00046 004			Part No.																	
RESISTOR	RESISTOR	RESISTOR	RESISTOR	RESISTOR	RESISTOR	RESISTOR	RESISTOR	RESISTOR	RESISTOR	RESISTOR	RESISTOR	RESISTOR	RESISTOR	RESISTOR	RESISTOR	RESISTOR	RESISTOR	RESISTOR	RESISTOR	RESISTOR	RESISTOR	RESISTOR	RESISTOR	RESISTOR	RESISTOR	RESISTOR	RESISTOR	RESISTOR	RESISTOR	RESISTOR	RESISTOR	TRANSISTOR	TRANSISTOR	TRANSISTOR	TRANSISTOR	TRANSISTOR	TRANSISTOR	TRANSISTOR	TRANSISTOR	TRANSISTOR	TRANSISTOR	TRANSISTOR	TRANSISTOR	COIL	REED RELAY	REED RELAY	REED RELAY	RELAY,REED				Nomenclature	
4.7K-5-1/4	4.7K-5-1/4	4.7K-5-1/4	10K-5-1/4	10K-5-1/4	10K-5-1/4	10K-5-1/4	10K-5-1/4	10K-5-1/4	10K-5-1/4	10K-5-1/4	10K-5-1/4	10K-5-1/4													2200UH	1A	1A	1A-300V	1A-500V				Part Value																				
																		R 073	R 072	R 071	R 070	R 069	R DRR	R 067	R 065	R 064	R 063	R 062	R 061	R 060	R 058	R 057	R 056	R 055	R 054	R 052	R 051	R 050	R 049	R 048	R 046	R 045	R 044	R 043	R 042	R 041	R 040	R 038			No.	Find	
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																		6S124A67	6S124A67	06-10621C11	6S124A18	6S124A18	06-10621031	6S124B14	6S124B14	18D83452F18	6S124B07	6S124B06	6S124A85	6S124A41	6S124A41	6S124A41	6S124A41	6S124A41	6S124A41	6S124A41	RT22C2X101	6S124A13	6S124A15	65104418	6S124A97	6S124A73	6S124A97	6S124A97	CMF1/10-1020-	CMF1/10-9090-	CMF1/4-909K	6S124A65				Part No.	
																		RESISTOR	RESISTOR	RESISTOR	RESISTOR	RESISTOR	DESISTOR	RESISTOR	RESISTOR	RESISTOR, VARIABLE	RESISTOR	RESISTOR	RESISTOR	RESISTOR	RESISTOR	RESISTOR	RESISTOR	RESISTOR	RESISTOR	RESISTOR	RESISTOR, VARIABLE	RESISTOR	RESISTOR	DESISTOD	RESISTOR	RESISTOR	RESISTOR	RESISTOR	5-T1 RESISTOR	5-T1 RESISTOR	5T1 DESISTOR	RESISTOR				Nomenclature	
																		5.6K-5-1/4	5.6K-5-1/4	1.47K-1-1/8	51-5-1/4	51-5-1/4	3.83K-1-1/8	470K-5-1/4	470K-5-1/4	50K	240K-5-1/4	220K-5-1/4	33K-5-1/4	470-5-1/4	470-5-1/4	470-5-1/4	470-5-1/4	470-5-1/4	470-5-1/4 270-5-1/4	470-5-1/4	100-5-3/4	33-5-1/4	39-5-1/4 39-5-1/4	390K-5-1/4	100K-5-1/4	10K-5-1/4	100K-5-1/4	100K-5-1/4	10205-1/4	9090- 5-1/4	909K5-1W-500V	4.7K-5-1/4				Part Value	
U 029	U 028	U 027	U 026	U 025	U 024	U 022	U 021	U 020	U 019	U 018	U 017	U 016	11 015	11014	210.01		U 010	600 N	008 U	U 007	U 006	1 004	003	U 002	U 001	TP001	R 107	R 106	R 104	R 102	R 101	R 100	R 099	R 098	R 096	R 095	R 094	R 093	R 092		R 088	R 087	R 086	R 085	R 084	R DR3	R 081	R 080			No.	Find	
-	-	1	4	-	1	-	-	-	-	-	.	<u> </u>	•		-	-	-	-	_ .	•			-	-	1	 .	. .			-	-	<u> </u>			-	-1		• -		-	-	_	<u> </u>	. .		-			Req.	Qty.	
51-80345A07-8.0	51-80345A08	51-80345A04	51-84887K73	51-80345A26	51-80323A60	51-84887K63	51-84887K63	51-84887K63	51-84887K63	51-84887K63	51-84887K79	51-84887K79	51-84887K71	51-84887K71	51-84887K71	51-8488/K11	51-84887K10	51-84887K10	51-84887K72	51-84887K72	51-84887K72	51-80345421	51-80345A21	51-80345A21	51-80345A21	3-582118-5	RT22C2X101	6S124A41	65124A61	6S124A00	6S124A47	6S124A41	6S124B22	6S124A29	6S124A73	6S124A73	6S124A73	6S124A73	6S124A73	65124A/3	6S124A73	6S124A73	6S124A73	6S124B38	6S124B38	18D83452FU/	6S124A65	6S124A65				Part No.	
3 INTEGRATED CIRCUIT	TRANSISTOR ARRAY	TRANSISTOR ARRAY	TRANSISTOR ARRAY	JACK, TIP	RESISTOR, VARIABLE	RESISTOR	RESISTOR	RESISTOR	RESISTOR	RESISTOR	RESISTOR	RESISTOR	RESISTOR	RESISTOR	RESISTOR	RESISTOR	RESISTOR	RESISTOR	RESISTOR	RESISTOR	RESISTOR	RESISTOR	RESISTOR	RESISTOR VARIABLE	RESISTOR	RESISTOR				Nomenclature																							
																											100-5-3/4	470-5-1/4	3.3K-5-1/4	4./K-5-1/4	820-5-1/4	470-5-1/4	1M-5-1/4	150-5-1/4	10K-5-1/4	10K-5-1/4	10K-5-1/4	10K-5-1/4	10K-5-1/4	10K-5-1/4	10K-5-1/4	10K-5-1/4	10K-5-1/4	4.7M-5-1/4	4.7M-5-1/4	34	4.7K-5-1/4	4.7K-5-1/4				Part Value	

Front Panel Interface Module ASSEMBLY PARTS LIST

SECTION 19

10 MHz FREQUENCY STANDARD MODULE (A13)

19-1. General. The frequency Standard Module provides a stable 10 MHz source and the interface for an external 10 MHz input. A block diagram of the Frequency Standard Module is shown in figure 19-1 with its schematic shown in figure 19-2.

19-2. 10 MHz Oscillator and Control. The internal 10 MHz source is either a temperature compensated crystal oscillator (TCXO) or an optional ovenized crystal oscillator (OVXO). A voltage regulator on the module supplies the voltage to the oscillator and monitors the supply current. For the ovenized option, at power on the oven draws high current. As the oven warms up the current decreases, reaching some low valve when the operating temperature has been reached. A current detector illuminates the oven ready indicator when the current has decreased to the stabilized valve. The indicator is continuously illuminated with the TCXO.

19-3. Internal/External Switchover. With no signal at the external 10MHz input jack, the internal oscillator is gated to the SYNTH 10 MHz and the external 10 MHz OUT signal paths. When an external 10 MHz input is applied the switchover circuitry detects its presence, removes the power from the internal oscillator, and gates the external input to the SYNTH 10 MHz and external 10 MHz OUT signal paths. The oven ready indicator is extinguished when the system is operating from an external standard.





J17 > EXT 10 MHz IN







Figure 19-3. Frequency Standard Module Parts Locator



Schematic Diagram. (RTL-1004A) ASSEMBLY PARTS LIST Frequency Standard Module

> THIS OSCILLATOR USED WITH HIGH STABILITY OPTION A13 MODULE RTL-1007A No. 0003 004 0006 0009 011 012 012 013 014 014 015 016 017 .A005 A 001 A 002 J 017 J 018 Req 7 7 2 1 2 AR AR AR AR ω Frequency Standard Module A13 MS27183-3 30-15068A34 Part Number 07-P00228N001 MS35338-40 MS35206-214 SN63WRMAP3 RTL-1006A M39012/21-0002 M39012/21-0002 01-80307A98 RTL-4046A NAS620C4L 14-15140A08 11-14167A01 29-15122A17 64-P06839R001 1107-4-A-7 RTL-1004A WASHER,LOCK WASHER,FLAT CABLE,RF Nomenclature WIRE SCREW,PH OSC, H1 STABILITY CONNECTOR, RF CONNECTOR, RF REF OSC, 10 MHz, TXCO NK SOLDER WIRE PLATE, CONNECTOR MTG SPACER BRACKET,OSC 10MHZ STANDARD INTERF WASHER INSULATOR, MICA TERMINAL,LUG NO.4 WHITE 4-40X.312 NO.4 22 WHT BLACK 24 WHT NO.4 Part Value Find No. C C 001 C C 002 C C 002 C C 005 C C 00 10MHZ STANDARD INTERFACE PWB Req ₽ AR Å A 23D83441B15 21D82428B62 21D82428B62 21D82428B62 21D82428B62 Part Number MS14046-2 MS90539-15 21D82187B14 23D84665F01 21C82372C09 21D82428B62 21D84494B42 21C82372C09 21D82428B62 SN96WRMAP3 11-14167A01 SN63WRMAP3 9-84231B02 48-84302A09 48-84463K02 48-84463K02 23D84665F02 23D84665F02 23D84665F02 23D84665F02 23D84665F02 23D84665F01 84-P00376N001 RTL-4046A CAPACITOR CAPACITOR CAPACITOR CAPACITOR CAPACITOR CAPACITOR CAPACITOR CAPACITOR CAPACITOR SOLDER CHOKE, RF COIL CONNECTOR, PHONE JACK DIODE, ZENER00 DIODEF-10-100 DIODEF-10-100 CAPACITOR CAPACITOR CAPACITOR CAPACITOR CAPACITOR CAPACITOR WIRE ĪĶ SOLDER Nomenclature PWB, 10 MHZ INTERFACE .01UF80-20-200 .01UF80-20-200 .01UF80-20-200 10UF-25V .1UF80-20-25 15UF-25V 15UF-25V 15UF-25V .1UF80-20-25 27PF-5-500 1000UH 6.8UH 6.2V-5-.4 1000PF-10-100 15UF-25V BLACK Part Value 10UF-25V 1.0UF-20-35 01UF80-20-200 .01UF80-20-200 R 010 R 011 R 0112 R 013 R 013 R 014 R 015 R 016 R 017 R 016 R 017 R 016 R 017 R 021 R 021 R 022 R 022 R 022 R 022 R 002 R 003 R 004 R 005 R 006 R 006 R 009 Find No. U 001 U 002 R 001 Q 002 0 001 Req. 6S124A92 18D83452F14 6S124A68 6S124A49 6S124A42 6S124A81 6S124A35 6S125B70 6S124A81 6S124A81 6S124A59 6S124A42 6S124A42 6S124A42 6S124A49 6S124A83 6S124A89 6S124A42 Part Number 51R84320A80 51-80323A60 6S124A59 6S124A81 6S125B70 6S124A59 6S124A90 6S124A42 6S124A42 6S124A21 48R0086957 48-80321A06 RESISTOR RESISTOR RESISTOR RESISTOR RESISTOR,VARIABLE RESISTOR RESISTOR RESISTOR RESISTOR RESISTOR RESISTOR RESISTOR RESISTOR INTEGRATED CIRCUIT INTEGRATED CIRCUIT RESISTOR RESISTOR RESISTOR RESISTOR RESISTOR RESISTOR RESISTOR RESISTOR RESISTOR TRANSISTOF FRANSISTOF Nomenclature 510-5-1/4 510-5-1/4 510-5-1/4 47K-5-1/4 27K-5-1/4 510-5-1/4 510-5-1/4 510-5-1/4 6.2K-5-1/4 1K-5-1/4 1K-5-1/4 1K-5-1/4 1K-5-1/4 1-5-1/2 22K-5-1/4 22K-5-1/4 270-5-1/4 51K-5-1/4 22K-5-1/4 22K-5-1/4 1-5-1/2 2.7K-5-1/4 68-5-1/4 2.7K-5-1/4 Part Value









03+5V-06 92 SB

-> SCAN UP SOKHE/SEC



(01-80304A42)

Schematic Diagram (Sheet 2 of 3)















Figure 20-1. Front Panel A14 (01-80304A42) Schematic Diagram (Sheet 3 of 3)

OVEN LED CATHODE (79 OVEN LED ANODE (78 DC LED ANODE (15

R

LED 100 Ty.

SCOPE DC IND (25

12 PAREA CTR NO 12 PAR

+50

SCOPE AC IND

SWP .01-1MHZ IND (37

MOD IND (35

34

SWP I-IOMHZ IND 38 CW ND (40

OUR MEN IZO SPECT ANAL IND

DPLX GEL ND. AT VIEW ZO

FRED CIR IND EXT WATT NO

DVN ZD TT ZD

Schematic Diagram (01-80304A43)



3. UNLESS OTHERWISE SPECIFIED: ALL RESISTORS ARE IN UF. ALL VOLTAGES ARE IN DC.

PL/DPL INV IND

6

DS5 054

PL/DPL IND

1

ס

3

MV50758

\$0P

MV50758





Figure 20-2. Display Board A14A1 Schematic Diagram (01-80304A43)







, , ,	042	041	340	339	038	037	J36)35	934	032)30	729	028	-027	126)25	024	023	22	-19	018	017	16	15	014	013	12	11	010	, (70 70	90°	005	4	ы	۲,		5		1		
8	-	-	з	-	1	-	-	2	-	8	-	1	-	-	-	-	-	-	-	-	-	-		-1	-	-	-	-	.	4 4		4 7	8	7	8	7	-		·		Ren .	Đ.	
36-80335A79	4-7699	2-482070	04-14154B12	84-P00439N001	84-P00440N001	30-P00213N001	004-9011	MP-0100-40-DW-	64-P00339N001	35500-43	35500-80	35500-79	35500-78	35500-77	35500-76	35500-75	35500-74	35500-73	35500-72	35500-71	13-80335A04	18-80346A18	18-80346A18	18-80346A13	18-80346A19	18-80346A13	40-80335A76	40-80335A79	40-80335A78	3-134212 18-80346A17	00-00046700	3S135676	1914-5A	1920-2A	65T-2	45T-3	64-80335A02					Part Number	Front Panel 01-80
KNOBER,COMP	WASHER,COMP	NUT,COMP	INSULATOR	PWB,FLEX RIGHTSIDE.S	PWB,FLEX LEFTSIDE,DIS	CABLE ASSEMBLY, OFFS	HOLDER,LED	-4H CONNECTOR	PLATE, SWITCH CLAMP	PUSHBUTTON,SWITCH,I	PUSHBUTTON, SWITCH	PUSHBUTTON, SWITCH-	PUSHBUTTON, SWITCH-	PUSHBUTTON,SWITCH-	FR PANEL OVERLAY	RESISTOR, VARIABLE/SV	RESISTOR, VARIABLE/SV	RESISTOR, VAR, DUAL, FC	RESISTOR, VAR, DUAL, OF	RESISTOR, VAR, DUAL, SC	SWITCH/DUAL RESISTO	SWITCH, RESISTOR VARI	SWITCH RESISTOR VARI	BESISTOR VARIABLE/SW	SCREW THD FORMING	SCREW, PHILLIPS HEA	WASHER,LOCK	WASHER, LOCK	NUT,COMPONENT	NUT, COMPONENT	PANEL, FRONT					Nomenclature	Assembly A14)304A42						
1/8 SHAFT			NO.4	õ	0	SET FRONT PANEL/A11		80 PIN W/O EARS		DEL	-9	6	-7	- 6	5	4	ώ	-2	-	0		VI EXT LEVEL	VI 1KHZ LEVEL	ŏ	i	ŏ	R, SCOPE HORIZ	A SCOPE VERT.	A SCOPE TRIG	A BFO	4-40X 312	4-40X.250	1/4	3/8	1/4-32	3/8-32					Fait Value	Part Value	
	R 008	R 007	R 006	R 005	R 004	R 003	R 002	R 001	P 001	J 027	J 025	J 024	J 022	J 021	J 005	J 001	D 003	A 001	078	077	076	075	074	070	069	068	064	060	059	058	057	056	055	053	052	051	050	049	048	047	045		Find No.
	-	-	-	-	-	-	-			-	-	_	_	-	1	1	-	1	1	AR	-	AR	2	AR	1	AR	4	1	AR	AR	-		- 44	AR	AR	-	AR	AR	AR	AR	ω		Qty. Req.
	06-	18-80346A14	18-80346A14	18-80346A16	06-	06-	06-	18-80346A15	01-80304A54	UJ2B	M39012/21-0002	M39012/21-0002	M39012/21-0002	M39012/21-0002	M39012/21-0002	9-830418	48D84404E03	01-80304A43	61-80335A53	RTV3145	61-P01314V001		36-80346A23	20-42127	61-P04142T001	1245X.5W	5610-21-31	29-15122A17	SN63WRP3		36-80335A86	13-P06819R001	75-80335450	M23053/5-103-9	M23053/5-105-9	NAS1745-3	30-15068A34		11-14167A01	SN63WRMAP3	36-80335A87		Part Number
	RESISTOR	RESISTOR, VAR, SQUELC	RESISTOR, VAR, AUDIO L	RESISTOR, VAR, INT, MOD	RESISTOR	RESISTOR	RESISTOR	RESISTOR, VAR, DISPR/S	CABLE ASSEMBLYE JAC	CONNECTOR, PHONE JA	CONNECTOR, RF	CONNECTOR, MIC	LED	DISPLAY BOARD ASSY	CRT GRATICLE	ADHESIVE	WINDOW,CRT	TAPE	KNOB GASKETING	MESH GASKETING	WINDOW,EMI	TAPE, SHIELDING	WASHER	TERMINAL,LUG	SOLDER	WIRE	KNOB.DUAL 1/8-1/4 SHA	BEZEL	ISOLATOR CET EBONTA	INSULATION SLEEVING	INSULATING SLEEVING	FERRULE,SOLDER	CABLE, RF	WIRE	INK	SOLDER	KNOB, DUAL 1/8-1/4 SHA		Nomenclature				
	PART OF F/N 13	Ϋ́SK	LE 5K	0, 5K	PART OF F/N 18	PART OF F/N 16	PART OF F/N 16	W 10K-10-1/4	CK 16PIN-101N-END	ICK.												NATURAL	DUAL 1/8-1/4 SHAF				N0.4			24 WHT	-	i	ĥ	.093 WHT	.187 WHT		WHITE	22 WHT	BLACK		1		Part Value
					VR002	VR001	S 019	S 018	S 017	S 016	S 015	S 014	S 012	S 011	S 010	800 S	S 008	S 007	S 006	S 005	S 004	S 003	T S 002	S 001	R 028	R 027	R 026	R 023	R 022	R 021	R 020	R 019	H U1/	R 016	R 015	R 014	R 013	R 012	R 011	R 010	R 009		Find No.
					-	1	-	1	-4			-			-	-	-	-	-	_	-	-		-	-	-	1		-	_	-			.	_	-		-	-	-	-		Qty. Req.
					48-80345A92	48-80345A96	40-P04127T001	40-	40-80335A80	40-80335A80	40-	40-80335A81	40-	40-	40-	40-	40-80335A75	40-80335A66	40-80335A66	40-80335A65	40-80335A64	40-80335A83	40-80335A81	40-80335A82	6S125A69	6S124A49	6S124A44	6S124A43	6S124A57	6S124A57	6S124A57	6S124A57	06-	06-	06-	06-	06-	18-80346A14	06-	06-	06-		Part Numbe
					DIODE04	DIODE SWITCH, 14 POS	WAFER SWITCH, 14 POS	SWITCH	SWITCH, TOGGLE	SWITCH, TOGGLE7	SWITCH	SWITCH, TOGGLE2	SWITCH	SWITCH	SWITCH	SWITCH	SWITCH, ROTARY	SWITCH, PUSHBUTTON	SWITCH, PUSHBUTTON	SWITCH, PUSHBUTTON	SWITCH, PUSHBUTTON	SWITCH, TOGGLE	SWITCH, TOGGLE	SWITCH, TOGGLE	RESISTOR	RESISTOR	RESISTOR	RESISTOR	RESISTOR	RESISTOR	RESISTOR	RESISTOR	RESISTOR	RESISTOR	RESISTOR	RESISTOR	RESISTOR	RESISTOR, VAR, RF LEVE	RESISTOR	RESISTOR	RESISTOR		Nomenclature
					6.8V-104 A13	15V-204		PART OF F/N 10	SP5T, IMAGE	SP5T,WB/NB	PART OF F/N 17	SP3T, OFFSET	PART OF F/N 12	PART OF F/N 11	PART OF F/N 13	PART OF F/N 18	1 POL, 11 POS, RF SC	2 POSITION	2 POSITION	2 POSITION	12 POSITION	DP3T,PWR	SP3T, MON GEN	SP3T,CSSG	6.8K-5-1/2	1K-5-1/4	620-5-1/4	560-5-1/4	2 2K-5-1/4	2.2K-5-1/4	2 2 2 2 2 2 2 2 2 2 2 2 2 2 2 2 2 2 2 2	2 2K-5-1/4	PART OF F/N 15	PART OF F/N 15	PART OF F/N 14	PART OF F/N 14	PART OF F/N 17	FF 5K	PART OF F/N 12	PART OF F/N 11	PART OF F/N 13		Part Value
					10-,4 R 003	R 002	R 001	DS030	DS029	DS028	DS027	DS026	DS025	DS024	DS023	DS022	AN DS021	DS020	DS019	DS018	DS017	DS016	DS015	DS014	DS013	DS012	DS011	DS010	DS009	DS008	DS007	DSUD	DS004	005	005	004	003	002	001		No	F	
					1	-	-	-	-	-	-	1	-	1	-	1	-	1	-	1	-	1	-	-	1	-	-	 .	-		. .			AR	AR	31	AR	AR	-		Req	d Otv.	
					6S124A29	6S124A29	6S124A29	48D84404E03	48D84404E03	48D84404E03	48D84404E03	48D84404E03	48D84404E03	48D84404E03	48D84404E03	48D84404E03	48D84404E03	48D84404E03	48D84404E03	48D84404E03	48D84404E03	48D84404E03	48D84404E03	48D84404E03	48D84404E03	48D84404E03	48D84404E03	48D84404E03	48D84404E03	48D84404E03	48084404E03	48D84404E03	48D84404E03	AG1.5WS	AG1.5WS	75491-004	11-14167A01	MULTICORE-TL	84-P00207N001			Part Numi	Display Bo 01-803
					RESISTOR	RESISTOR	RESISTOR	LED	LED	LED	LED	LED	LED	LED	LED	LED	LED	LED	LED	LED	LED	LED	LED	LED	LED	LED	LED		5		5		LED	SOLDER	SOLDER	CONTACT	INK	CWRAP3SOLDER	PWB, DISPLAY			oer Nomenclature	ard A14A1 04A43
					. 150-5-1/4	150-5-1/4	150-5-1/4																													BERG, TRI PIN	BLACK	LOW TEMP.293 F				Part Valu	

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ASSEMBLY PARTS LIST Front Panel

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SECTION 21

BLOWER ASSEMBLY (A15)

21-1. For continuous high ambient temperature operation or with the IEEE control option, the blower option provides additional cooling for the system. The blower as received from the factory will run whenever the unit is connected to the AC line. However, a jumper shorting out a thermostat in the blower assembly may be removed so that the fan only operates when high temperature conditions exist. A schematic of the Blower Assembly is shown in figure 21-1.

Figure 21-2. Blower Assembly Parts Location







010	001 002 003 004 005	Find No.
1		Qty. Req.
206429-1	MU2A1 15-P00257N001 3450-87-239 477712 32-P06859R001	Part Number
CONNECTOR	FAN CASE, HOUSING THERMOSTAT FILTER ASSY GASKET, BLOWER	Nomenclature
÷		Pa

SECTION 22

IEEE — 488 BUS CONTROL

22-1. INTRODUCTION

22.2. The IEEE Interface Option enables the use of the Communications System Analyzer as a programmable measurement instrument. When combined with a suitable programmable controller and applications software, the major functions of the analyzer can be controlled or monitored via an IEEE-488 standard digital interface. Thus, repetitive test routines can be performed and the data recorded quickly and accurately with little operator interaction.

22-3. The interface characteristics conform to the specifications of the IEEE Standard Digital Interface for Programmable Instrumentation (IEEE Standard 488) which defines both the electrical and the mechanical interface. Control protocol is also defined by the specification. Control commands which are unique to the analyzer are described in detail in the following paragraphs of this section.

22-4. The controller for this application should be capable of reading and writing ASCII and control characters from and to the bus in accordance with the 488 specification. Application software is the user's responsibility as dictated by the controller selected, although interface and application assistance is available from Motorola.

22-5. The IEEE option package consists of an IEEE Interface module (A8) with a rear panel connector, an electrically programmable RF attenuator in place of the step attenuator on the RF Input Module (AH), a fourteen position rotary switch on the front panel in place of the step attenuator shaft, and one additional ROM memory 1C on the Processor module (A9).

22-6. While in the local mode the IEEE-488 equiped system operates and performs the same as a standard system, except the maximum RF output level is reduced to +11 dBm from +13 dBm. However, when the Remote Enable (PEN) line on the IEEE Bus is activated many of the front panel controls are disabled and their functions placed under bus control. Refer to table 22-1 for a listing of those functions which can be controlled or monitored via the 488 Bus.

Control/Measurement	Comment
Function Switch	Generate/Power Monitor/ Monitor
Modulation Control	Continuous/OFF/BURST
Wideband/Narrowband Switch	
Image High/Low Switch	
Duplex Oscillator Switch	0-10 MHz/OFF/45 MHz
Keyboard	Numeric Entries 0-9. Can be transmitted to the bus.
Display Mode	Generate/Monitor Metering (Note: 1) Modulation Spectrum Analyzer Duplex Generator RF Memory Tone Memory Tone Memory Frequency Counter DVM External Wattmeter IF Scope AC Scope DC Remote Terminal Mode Unit can also display a subset of ASCII characters (numerals 0-9, upper case alpha letters A-Z, plus other symbols—ASCII characters 20 thru 5F Hexa- decimal) enables display of operator messages on CRT display in a transparent ter- minal mode.
Function Mode	FM (Note: 1) CW (Note: 1) AM SSB/DSB SWP 1-10 MHz SWP 0.01-1 MHz
Code Synthesizer Mode	PL/DPL PL/DPL Invert Tone A Tone B A/B Tone Remote

Table 22-1.	IEEE-488	Interface	Controllable	Functions
-------------	----------	-----------	--------------	-----------

Control/Measurement	Comment
RF Frequency PL Frequency DPL Code Tone A Frequency Tone B Frequency	Frequency entry to be supplied by program. Frequencies not available from memory table.
Time Sequence Select	Sequences 1 through 5 only (Note: 2)
Wattmeter Element Select	4
External Modulation	Modulation settable to any measurable level
Code Synthesizer Modulation	(0-20 KHz deviation in 10 Hz steps) (Note: 3) (0-90% AM in 0.1% steps)
RF level	.RF level settable to any displayable level (-140 to +11 dBm in 0.1 dBm steps (Note: 3) (Note: 4)
Offset Oscillator Adjust	Duplex Generator Frequency Settable from fo to fo ±10 MHz in 1 KHz steps (Switch) placed in 0-10 MHz position) (Note: 3)
Scope Vertical Step Attenuator	0.01, 0.1, 1, 10 volts
Horizontal Scope Sweep	1, 10, 100 milliseconds 1, 10, 100 microseconds External
Input Power Meter Frequency Error Deviation + or - % AM + or - SINAD External DVM (AC or DC) External Frequency Count External Frequency Count	Reading returned as displayed on screen (Note: 3)
Deviation + or - % AM + or - % AM + or - SINAD External DVM (AC or DC) External Frequency Count External Power Meter FWD/REV Notes: (1) May be affected by other (2) Sequence 5 timing is pro- (3) As reading is displayed. U display and function mod	Reading returned as displayed on screen (Note: 3) controls (see below). grammable under IEEE bus control. ED corresponding to appropriate e will illuminate.

NON-CONTROLLABLE FUNCTIONS

Since control and monitor functions of the inter- not implemented in the interface due to their face are implemented to obtain remote measure- local operator orientation. A list of these operment capability, certain front panel controls are ator oriented controls are as follows:

Power On/Off Power Mode Indicators **Display Focus Display Intensity** Dispersion/Sweep Scope/DVM Vertical Vernier Scope Trigger Level Scope Trigger Slope Scope Horizontal Sweep Vernier Scope Vertical Position Scope Horizontal Position **Receiver Squelch** Receiver Volume Zero Beat Indicator RF Scan **RF Memory Table** Tone Sequences, 6, 7, and 8 Entries

Deviation Limit Battery Voltage Reading Deviation Limit Alarm (Disabled Under Remote Control) Attenuator 0 Indicator Battery Below Limit Warning **BFO Frequency Adjust**

8521-66

22-7. IEEE-488 BUS STRUCTURE

22-8. The following discussion briefly describes the 488 Bus operation. It is not a complete definition of the total bus structure or capability. For complete information a copy of IEEE Standard 488 should be obtained.

22-9. Bus Signals. The IEEE-488 Bus consists of 16 parallel lines. The lines are divided into three groups. Lines DI01-DI08, Data Input Output, form the 8-bit data bus for the bidirectional transfer of control and ASC II characters. Three handshake lines, Data Valid (DAV), Not Ready for Data (NRFD), and Not Data Accepted (NDAC), control the transfer of data on the data bus. The remaining five lines can be termed the bus management lines with functions as follows:

Attention (ATN)	 When true the data bus carries an address or
	a comand when false it carries data.
Interface Clear (IFC)	 When true all devices on the bus are placed
	in a known quiescent state.
Service Request (SRQ)	 Indicates a device on the bus needs service.
Remote Enable (REN)	 Enables the remote control feature of the devices on the bus.
End or Indentify (EOI)	 Indicates the end of a multiple byte transfire.

22-10. Data Transfer. Each byte of data that is transferred across the data bus is synchronized with a handshaking procedure. This procedure allows devices with different data transfer rates to share the same bus. The handshake cycle starts when the source device which has data totransfer checks for a false condition on the NRFD line. When NRFD is false, all devices on the bus are ready to accept data. The source then puts the data onto the data bus and sets the DAV to its true state. The acceptor devices input the data, set the NRFD line to its true state, and when ready sets the NDAC line to its false state. Because the NRFD and NDAC lines are wire-ORed the line will not go to the false state until all devices on the bus have released the line. Thus the slowest device on the bus determines the transfer rate. When the NDAC line goes false the source devices sets the DAV false which in turn causes the acceptor devices to set the NDAC line true. When the acceptor devices have completed processing the data byte just received they allow the NRFD line to go to the false state completing the handshake. As the data transfer continues the cycle repeats for each data byte.

22-11. Bus Address. Each device on the bus is assigned a four bit address by the programer. The address assigned to the device is set by an address switch within the device. On the analyzer the address switch is on the IEEE Interface Module. Only the top four switches are used to set the address. The fifth switch is unused. To set the address use the binary equivalent of the address number and set the switches to the ON position for a logic 1. The least significant bit is the top switch.

22-12. Programing

22-13. Programing the system analyzer consists of first addressing the unit as a listener, transferring the control commands to the unit, and then sending a command termination sequence. To obtain data from the system, the pertinent control commands are first transferred to the unit and then the unit is addressed as a talker. As a talker the system outputs onto the bus the data requested by the control commands.

22-14. The bus controller is the central part of the automatic system. The program, consisting of sequences of analyzer control commands and sequences of controller instructions for handling the return data, is contained within the controller. The user must initially write the program so that the desired test sequences and data outputs will be obtained. The following paragraphs define the instruction set and data formats that can be used to control or will be returned from the system analyzer. The user must insure that the controller is compatible with the IEEE-488 Standard bus and that its program is correct for the instruments on the bus.

22-15. Command Structure. Each command consists of a two letter definition prefix followed by a numeric data field. The data field will vary in length and structure according to the definition prefix as shown in table 22-3. Spaces may be inserted anywhere in the command but are not required. Each letter or number of a command is transferred from the bus controller to the^nalyzer in ASCII format. ASCII defines a 7-bit digital code for each letter, number, and symbol commonly used in computer programming.

22-16. The first letter of the two letter prefix identifies a command category with the second letter identifying a particular command within that category. A listing of the command categories and the corresponding first letter is provided in table 22-2. A complete list of commands is shown in table 22-3.

Table 22-2. Command Categories

Α	Audio Synthesizer
С	Control
F	Frequency Counter
G	Generate/Monitor Control
К	Keyboard
Μ	Modulation
0	Oscilliscope
R	Receiver
V	Voltmeter
W	Wattmeter

22-17. The data field is comprised of five sub-fields as shown:

± n.n E ± n DATA SIGN / EXPONENT \ EXPONENT MAGNITUDE DATA VALUE EXPONENT SIGN

Data limits and accompanying units are given in table 22-3. The data field is optional or not allowed for certain commands.

22-18. Data Sign. The data sign is a single'+'or'-'character indicating the sign of the data value. The sign may be omitted for positive value data.

22-19. Data Value. The data value field is restricted to the numbers '0' through '9' and'.'. A maximum of five digits to the right and to the left of the decimal point are allowed. The decimal point can be omitted for integer values. If the value field is omitted, it is assumed to be zero.

22-20. Exponent. The presence of the "E" character in the exponent field indicates that the data value is to be multiplied by 10 raised to the power following the "E" character. If the "E" is omitted the exponent is assumed to be 10° or 1.

22-21. Exponent Sign. The exponent sign is a single + or - character and can be omitted for positive exponent values.

22-22. Exponent Magnitude. The exponent magnitude is a single character 0 through 9. If the exponent magnitude is omitted, it is assumed to be zero.

-	-	-	-		~			7		1	0	0 0				0							Q			P.	A I	AV	AS	P	AB			Prefi
K4 0	K3 0	K2 0	K1 0		ې +	GF 0		0			¥ 	≊ ດິ ∣				ří P							0 0			-0	0-9	× 0-9	0-5	0-0	0-9	_	-	×
)-127)-127)-127)-127		130.0 to 13.0	-999.9999		1-35000			ï					'n							12			9.99	9.99	9.99	5 11	999.9	6.666 6			Data
1	I	I	I		DBM	MHz		KHz			I	1 1				ī							I			SEC	SEC	SEC	Î I	ΗZ	HZ HZ			Units
σ	D	U	D		0	Ū		0			0	იი	,			n)						ი			C	0 0	00	00	סס	00		»	Туре
Function Down Key Data	Function Up Key Data	Display Down Key Data	Display Up Key Data	KEYBOARD	Generate RF Level	Generate/Monitor Frequency	GENERATE/MONITOR	External freq count			Power Monitor Mode	Generate Mode Monitor Mode	5 SWP 0.01-1 MHz	3 SSB/DSBSC 4 SWP 1-10 MHz	1 CW 2 AM	0 FM	12 Terminal	10 Scope AC 11 Scope DC	8 Ext Wattmeter 9 IF	5 Tone Mem 6 Freq Counter 7 DVM	3 Duplex Gen 4 RF Mem	1 Modulation 2 Spect Analyzer	Display Select 0 Gen-Mon Mtr		CONTROL	B OFF, User Seq. (AS≡5)	B ON, User Seq. (AS=5)	A ON, User Seq. (AS=5) A OFF, User Seq. (AS=5)	Audio Sequence Select	PL Frequency	Tone A Frequency Tone B Frequency		UDIO GENERATOR	Function
					GEN			2			PWR	GEN				_						~	_							2				Changes To Display Function Mode
i	R				RA	RW	RN	Ҏ	모			(Ş				Ŷ		MS		MK	S T	i				MM	MO	MC	MB		K6	K5	Prefix
	0-100				0-13	I	I	I	1			ć	0-3				0-6		0-99.9		0-99.9	0-99.9					0-5	1	1	I		0-127	0-127	Data
	1				10's d	1	1	I					1			-	I		kHz (Fi % (AN	(FM) %(AN	%(AN	(FM)					I	I	I	I		I	1	Units
)	0	-			б С	o	ი	ი	0			0	ິ				0.		<u>33</u> 0	3	ი ი	c.)				0	0	0	0		D	D	Туре
	Receive frequency error	1 10 dB	1 10 dB	0 0 dB	Receive Mode Step	Wide band	Narrow band	Low Image	High Image	RECEIVER	2 0.1 V/div 3 0.01 V/div	0 10 V/div 1 1 V/div	Vertical Gain Select	4 10 milli sec/div 5 100 milli sec/div 6 External	3 1 milli sec/div	1 10 micro sec/div	Horizontal Sweep Select 0 1 micro sec/div	OSCILLOSCOPE	Code Synthesizer Mod Level		I kHz Mod Level	External Mod Level	5 Tone Remote	4 A/B	2 Tone A	0 PL/DPL 1 PL/DPL div	Modulation Mode	Modulation Off	Modulation Continuous	Modulation Burst	MODULATION	Mode Down Key Data	Mode Up Key Data	Function
1	4																	-											<u>.</u>					Displa
																																		Change y Fund
	2																		3		3	ů C	, ,											s To ction N
-	02												- Z-						ž		Ĕ	Ï				1								ode
										6. FM i 7 Fxte	4. Gen, 5. DVM	2. Exte 3. FM if	otes:	WR		N I	MI C					NE 1		vs i	5	A O		л o	я 0	₽ •	٩ 0		0 PF	refix
										f in DSBSC or t	/Mon Mtr Display	rnal Frequency f not AM)-1000	0-1000	-1000)-132.0					9		-40.0	-300	-300		-99.99	-99.99	-99.99	-99.99		7	Data
									Curden 2	SWEEP	ły	y the data Counter E	-	WATTS		WATTS	WATTS					I		dB .		VOLTS		%	%	kHz	кНz		T	Units
												Jisplay	-	0	(- -	0					D		0	о (0	ļ	0	2	0	0		0	Туре
														Reverse External Vattmeter Reading	Nattmeter Reading	Forward External	Internal Wattmeter	9 1000 W	6 100 W 7 250 W	5 50 ¥	1 2.5 W 2 5 W	Vattmeter element number	WATTMETER	linad Reading	DVM DC	DVM AC	VOLTMETER	lus % AM	Ainus % AM	¹ lus Deviation	finus Deviation	1 Signal present	Signal Presence Indication	Function
																						_	L			1	- 1							
													_	7		7	4							4	ט רט	5		4	4	4	4			Display
													-	7		7	4							4 6	ט רט	5		4 AM	4 AM	4 FM	4 FM			Changes 1 Display Functio

22-23. The following are examples of correct data fields for the value 12.34:

0.1234 E+2	+0.1234 E2	1234 E-2	1234. E-2
+12.34	12.34 E	+1234 E-2	12.34 EO

22-24. Command Strings. A command string consists of either a single command or multiple commands in succession with or without embedded spaces. A command string must be terminated with a carriage return and a line feed character.

22-25. Command Types. Eachcommand isoneofthreebasictypes,controlselects(C),dataentry(D),and output requests (0). Type information for each command is listed in table 22-3.

22-26. Control Selects. Control select commands select front panel switch settings. Some of these commands do not require accompanying data, such as toggle switch commands.

22-27. Data Entry. Data entry commands replace manual entry of data through the keyboard. All of these commands require data in the data field.

22-28. Output Requests. Output request commands allow data that is normally displayed on the CRT to be transferred to the controller. Accompanying data is not required with output requests. The data limits and units listed in table 22-3 for these commands refer to the return data. Output request commands cause the analyzer to go to the proper display, function, and mode to acquire the designated reading. These states are listed in table 22-3. The measurement however, is not made until a trigger command 'T' has been sent from the controller. The trigger command causes the measurement to be made and the data held for transmission to the controller. Then when the controller addresses the analyzer as a talker the data is output to the controller. A reading can be retaken for any number of triggers without repeating the output request. The request is lost however, when any command changing the display, function, or mode is sent.

22-29. Trigger Command. The trigger command is the exception to the two character command prefix. This command is simply the letter T' usually sent immediately following the output request command. If no output request is pending, the trigger command is ignored.

22-30. Return Data. The data returned from the analyzer is formatted similar to the control data as shown:

± n E -n

DATA SIGN DATA VALUE EXPONENT SIGN EXPONENT MAGNITUDE

The data is always returned in this format with a single exception. Data for the "RP", signal present, command is returned as a single digit having a value of "0" or "1".

22-31. Data Sign. A + or - character indicates the sign of the return data.

22-32. Data Value. The data value is 1 to 5 digits in length with leading zero supression and no decimal point.

22-33. Exponent and Exponent Sign. The letter 'E' followed by a '-' character is always transmitted with return data.

22-34. Exponent Magnitude. The exponent magnitude is a single digit with a value from 0 to 9. The digit indicates the negative power of ten that is to be multiplied with the data value to obtain the units listed in table 22-3.

22-35. Programming Commands. Table 22-3 lists the programming commands available for the system analyzer. The table identifies the category and type of command, the data limits and units, the command function, and any display, function, or mode change that would occur.

22-36. Terminal Mode. When the command 'CD12' is used, the system terminal mode is enabled. The terminal mode allows the analyzer's CRT display and keyboard to perform as a limited function I/O terminal.
Possible uses for the terminal mode would be to provide test instructions to a test operator at an auto test station.

22-37. Display Format. Once the 'CD12' command has been sent the terminal mode has been entered. All further ASCI I valid characters sent from the controller will appear on the CRT display. The total display area on the CRT is 15 lines of 30 characters each. Character entry on the CRT is on the bottom line. Each linefeed character causes the bottom line to move up one place. If more than 30 lines are entered, the top lines are lost off the top of the display. A list of valid ASCII characters for the display is provided in table 22-4. All invalid characters are ignored in the terminal mode.

22-38. Keyboard Entry. In the terminal mode the keyboards on the analyzer may be used to input data to the bus controller. The ten numeric keys and the left cursor key have predefined ASC 11 characters. The character corresponds to the number on the key for the numeric keys. For the left cursor key, carriage return and line feed characters are sent. The down cursor key causes a bus service request to be generated regardless of the operating mode. Thus this key could be used to halt an automatic test sequence.

22-39. The remaining pushbuttons are defined, prior to entering the terminal mode, with the use of the keyboard control commands listed in table 22-3. Each key is assigned an ASCII character by following the Kn command prefix with the decimal equivalent of the binary ASCII code for that character. A list of valid ASCII characters and their binary and decimal equivalents are listed in table 22-4.

22-40. Data that is entered from the keyboard is stored in a 9 character buffer until addressed by the bus controller. If more than 9 keypresses occur before the controller accesses the analyzer, the excess inputs are lost. Once the controller has addressed the analyzer, the analyzer transmits the character data to the controller. The analyzer will continue to transmit, or hold up the bus handshake if no keys have been pressed, until the left cursor key is pressed. Thus every data string entry from the keyboard must terminate with the left cursor key. As the data is transmitted to the controller it is also entered onto the CRT display.

22-41. Terminal Mode Exit. An ASCII end of transmission character (EOT) sent from the controller will terminate the terminal mode. When the mode is terminated the analyzer returns to the Gen/Mon Mtr display, and is ready to accept new command inputs.

22-42. Error Messages. Error messages are generated by the analyzer to help the programmer troubleshoot his program. As control commands are received by the analyzer, they are decoded to determine the command sent. If the analyzer is unable to decode the command it generates an error message and ignores all succeeding commands. To clear the error condition the bus controller must address the analyzer as atalker so that the error message will be transferred to the controller.

Equivalent		Equivalent					
ASCII			10	ASCII			
Char.	Binary	Hex	Dec	Char.	Binary	Hex	Dec
SP	00100000	20	32	@	01000000	40	64
1	00100001	21	33	А	01000001	41	65
"	00100010	22	34	В	01000010	42	66
#	00100011	23	35	С	01000011	43	67
\$	00100100	24	36	D	01000100	44	68
%	00100101	25	37	E	01000101	45	69
&	. 00100110	26	38	F	01000110	46	70
'	00100111	27	39	G	01000111	47	71
(00101000	28	40	н	01001000	48	72
)	00101001	29	41	1	01001001	49	73
·	00101010	2A	42	J	01001010	4A	74
+	00101011	2B	43	к	01001011	4B	75
,	00101100	2C	44	L	01001100	4C	76
-	00101101	2D	45	M	01001101	4D	77
	00101110	2E	46	N	01001110	4E	78
1	00101111	2F	47	0	01001111	4F	79
0	00110000	30	48	Р	01010000	50	80
1	00110001	31	49	Q	01010001	51	81
2	00110010	32	50	R	01010010	52	82
3	00110011	33	51	S	01010011	53	83
4	00110100	34	52	т	01010100	54	84
5	00110101	35	53	U	01010101	55	85
6	00110110	36	54	V	01010110	56	86
7	00110111	37	55	w	01010111	57	87
8	00111000	38	56	х	01011000	58	88
9	00111001	39	57	Y	01011001	59	89
:	00111010	ЗA	58	z	01011010	5A	90
;	00111011	3B	59]	01011011	5B	91
	00111100	3C	60		01011100	5C	92
=	00111101	3D	61	1	01011101	5D	93
	00111110	3E	62		01011110	5E	94
?	00111111	3F	63	-	01011111	5F	95
		NO	N-PRINTING	G CHARACT	ERS		
	-			Equ	ivalent		
ASC	CII					_	_
Cha	ar	Bi	nary		Hex	[Jec
EOI	1	000	00100		04		4
BEL	L .	000			07		<i>'</i>
BSP	,	000	01000		08		8
		000	01010				10
CR		000	101101		UD		13
*causes exit from terminal mode							

Table 22-4. Terminal Mode ASC II Characters Printable Characters

ERROR nn (CR)(LF)

The two digit number nn defines the error condition as listed in table 22-5. The carriage return (CR) and line feed (LF) characters are the termination sequence used by analyzer whenever it transmitts information. All characters are ASC II coded.

Table 22-5. Error Messages

Error Code	Condition
00	Data requested without trigger
01	Involid mnemonic prefix
02	One character mnemonic (not T
03	Invalid mnemonic suffix
04	Exponent overflow
05	Data underflow
06	Data overflow
07	Data transmitted, not allowed
08	Invalid data
09	RF input power exceeded
10	Level or mod control error

22-44. To effectively utilize the error message capability of the analyzer it is necessary to address the unit as a talker after the transmission of each command string. The bus controller must then be programmed to recognize the error message and to decode the error number. A successful data transmission will send back an error code 00 when addressed as a talker. The controller should be programmed to ignore error 00 and to display any other error to the operator. Of course if a valid output command followed by the trigger command was sent, the talker address will result in the requested data being output to the controller.

22-45. Service Requests. There are only two conditions that will cause the analyzer to generate a service request (SRQ) on the bus. IfaSRQ is generated it must be cleared by a serial poll of the analyzer. The serial poll is a bus command which results in a data byte being sent to the controller from the analyzer. The data byte indicates the cause of the SRQ. Table 22-6 lists the SRQ causes and the corresponding serial poll data.

	Return Data			
Condition	Binary	HEX	DEC	
Depressing Cursor Down Key	01000001	41	65	
RF load over Temperature	01000010	42	66	

Table	22-6.	SRO	Data
raore	22 0.	DICY	Duttu

22-46. Programming Considerations. The flexibility of the IEEE-488 option is reflected in the number of programming commands. To use these effectively and efficiently, certain programming practices should be followed. The following paragraphs present the major considerations for effective programming.

22-47. Generate Mode. For accurate level control it is best to specify the generate frequency priortotheRF output level. For example, the command string:

CGGFIOOGL5

sets the generate mode, a frequency of 100 MHz and an output level of +5 dBm.

22-48. Code Synthesizer. Before enabling the output of the code synthesizer with an MS, ME, or MK command, all the necessary parameters must first be defined. Table 22-7 lists the modes and their controlled parameters that need to be defined. It should be noted that these parameters do not need to be defined each time a mode is selected, only when they are to be changed for that mode.

Table 22-7. Code Synthesizer Programming Considerations

Output	Command String	Effect
DPL Code	CFOAD131MMOMS3	FM, DPL Code 131, 3 kHz FM
DPL Inverted Code	CFAD313MM1MS5	FM, DPL Code 313, 5 kHz FM
PL Code	CF2AP60.5MMMS30	AM, PL-60.5 Hz, 30% AM
Tone A	CFAA2E3MM2MS3	FM, 2000 Hz, 3 kHz FM
Tone B	CFAB2000MM3MS3	FM, 2000 Hz, 3 kHz FM
Tone Remote	CFAA1.5E3AB300MM5MS3	FM, A = 1500 Hz, B = 300 Hz, 3 kHz FM
A/B Standard Sequence	CFAS4AA1E3AB2E3MM4MS3	FM, Sequence 4, A = 1 kHz, B = 2 kHz, 3 kHz FM
A/B User Sequence	CFAS5AA1E3AB2E3AW1	FM, Sequence 5, A = 1 kHz, B = 2 kHz
	AX1AY1AZ1MM4MS3	1 sec on/off times, 3 kHz FM

22-49. Modulation. The system analyzer is capable of modulating with three simultaneous sources. The commands ME, MK, and MS only affect their individual portion of the total output. Thus to avoid inadvertently having an unwanted modulation source enabled it is recommended that all three source values be defined together. For example;

CFMKMSME20

selects the FM mode, disables the 1 kHz and code synthesizer modulation, and set 20 kHz deviation from the external input. The external input must be applied to the analyzer prior to sending this command.

22-50. For the generate AM mode the frequency and output level must be defined prior to selecting the modulation level. The following command string is of the proper sequence to obtain 30% AM at 100 Mz with a level of-100 dBm:

CGGF100GL-100MEMSMK30

22-51. The bandwidth control commands, RN and RW, range the generate FM modulator sensitivity. For greater resolution and faster set up time for deviations less than 20 kHz use the narrowband 'RN' command. Above 20 kHz deviation the wideband 'RW command must be used.

22-52. Measurements. To obtain correct monitor mode data it is necessary to first set the frequency, bandwidth, and image prior to making the reading. Thus, it is a good practice to always place the request for a reading as the last command in the string. For example the command string:

CMRNRHGF95.5RET

selects the monitor mode, narrowband, high image, and 95,5 MHz center frequency. The'RET' command asks for a frequency error reading and triggers the analyzer so that the reading will be made.

22-53. General. Overall, programming the analyzer involves the same steps as are involved when using it. manually. A program can be fairly easily obtained by first performing the desired test sequence manually noting each time a setting is changed and a reading made. The program is then simply a duplication of the ' manual steps with control commands substituted.

22-54. R2002A Analyzer Configuration

The R2002A analyzer differs in configuration from the standard R2001A in the following manner:

A11 Module: The manual attenuator AT1 is replaced with a programmable version (P/N RTL-4064A). A new ribbon cable assembly connected to the A8 module provides control signals for the attenuator. The module is reidentified for ordering purposes as RTC-1003A.

A9 Module: Additional memory for the IEEE program is added by adding U36 (E-PROM TMS 25L32).

Front Panel Assembly A14: Rotary switch S19 is added for control of the RF I nput/Output level. The switch P/N is40-P04127T001.

Module AS is added to the analyzer (see Section 14 for details). Ribbon cable assembly 30-P04147T001 is added from the A8 module to the rear panel of the analyzer to provide I/O signals.

The R2002A also contains blower assembly A15 for additional cooling (see Section 21).


MOTOROLA INC.

Communications Group

MOTOROLA BATTERY PACK

MODEL RTP-1002A

1. DESCRIPTION

The RTP-1002A is a battery pack and charger designed to be mounted to the back of the R-2001A Communications System Analyzer. The unit contains battery capacity to operate the R-2001A for approximately one hour. A constant current charging system is capable of recharging the batteries in 16 hours.

2. OPERATION

2.1 The RTP-1002A Battery Pack is automatically engaged when no ac power is present, and the power switch is either in the ON or STANDBY positions. When ac power is applied, the R-2001A automatically switches the RTP-1002A Battery Pack out of the circuit and draws its power from the ac power source. 2.2 When the power switch is in the OFF or STAND-

BY position and ac power is applied to the R-2001A, the RTP-1002A Battery Pack draws dc current from the R-2001A to activate the charging circuit. The charging circuit delivers approximately 750 mA of current until the battery voltage reaches 14 volts. As the battery voltage reaches 14 volts, the current drops to approximately 25 mA and the high-charge indicator LED extinguishes.

2.3 When the R-2001A systems analyzer is used with

the RTP-1002A Battery Pack, it is recommended to keep the power switch in the STANDBY position whenever possible. This extends the time the battery is able to operate the R-2001A Communications System Analyzer. The low trickle charge rate enables the batteries to be left on charge indefinitely without damage due to overcharging.



RTP-1002A Battery Pack Mounting Detail

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68P81122E59-A 5/30/80-SK







RTP-1002A Battery Pack Schematic Diagram, Circuit Board Detail, Parts Location Detail, and Parts List Motorola No. PEPS-29554-O (Sheet 1 of 2) 3/24/80-SK



RTP-1002A Battery Pack Parts Location Detail

parts list

11P-1002A Battery Pack		PL-6816-0	-		
REFERENCE SYMBOL	MOTOROLA PART NO.	DESCRIPTION	REFERENCE SYMBOL	MOTOROLA PART NO.	DESCRIPTION
		battery, 12 V:	non-referenced items		
BT1,2	60-80340A88	6-cell		1-80304A71	BATTERY CASE includes:
		eeneeltee flue t		27-80335A41	CASE, battery
C1	9 92006 149	capacitor, fixed:		3-80340A89	SCREW, captive: 6-32 x 21/32"; 2 used
C2	21 92506510	.1 UF ± 10%; 250 V		41-80342A53	SPRING, clip
02	21-03090E10	220 pF ± 20%; 500 V		15-80340A92	COVER, battery case
		dla day (a.e 4)		1-80304A72	CIRCUIT BOARD ASSEMBLY includes:
CP1	49 99595001	diode: (see note)		42-82690A01	CLIP, fuseholder: 2 used
CHI	40-02020001	silicon		43-865080	STANDOFF, threaded: 4 used
				1-80304A73	LEAD ASSEMBLY, battery (red) includes:
DS1	10 000101 05	light emitting diode:		30-10310A26	WIRE, No. 16 stranded; 4-1/2" used
	48-82019L05	LED		29-859118	CONTACT, receptacle
				1-80304A74	LEAD ASSEMBLY, battery (black) includes:
F1 Q1, 2	05 00 1000	tuse:		10-134301	WIRE, No. 16 stranded: 4-1/2" used
	65-804906	15A slow blow		29-859118	CONTACT, receptacle
		And the set of the second second second		3-120938	SCREW, machine: 4-40 x 5/16": 4 used
		transistor: (see note)		4-7667	WASHER, look: No. 4 external tooth: 4 used
	48-869302	NPN; type M9302		64-80342A54	PLATE, heatsink
				1-80303A91	CABLE ASSEMBLY includes:
		resistor, fixed: ± 10%; 1/4 W:		15-10811A08	HOUSING connector 4-nin
		unless otherwise stated		9-83741F01	CONTACT, receptacle: 4 used
R1	17-80344A60	10; 10 W		42-80340A90	CLAMP cable
R2	6-124C37	330		2-2888	NUT, hex: 5/8-24
R3	6-126C45	680; 1 W		2-7005	NUT hex: 6-32: 4 used
R4, 5	6-125B70	1 ± 5%; 1/2 W		4-7666	WASHER lock: No 6 external tooth: 4 used
R6	17-80344A70	0.82; 2 W		14.80340491	INSULATOR BOARD
R7	6-124A73	10k ± 5%		75.82566B01	FOOT rubber: 4 used
R8	18-80342A10	variable: 2k ± 20%; 1/2 W		3-80342446	SCREW machine: 6.32 x 1/2": 4 used
R9	6-124A73	10k ±5%		3.136774	SCREW machine: 4.40 x 1/4": 5 used
				3.132840	SCREW machine: 8-32 x 5/8": 2 used
		integrated circuit: (see note)		4.7667	WASHER lock: No 4 external tooth: 5 used
U1	51-84320A42	MC1723CL		42.850025	CI AMD

note: For optimum performance, replacement diodes, transistors and integrated circuits must be ordered by Motorola part numbers.

> RTP-1002A Battery Pack Schematic Diagram, Circuit Board Detail, Parts Location Detail, and Parts List Motorola No. PEPS-29554-0 (Sheet 2 of 2) 3/24/80-SK

