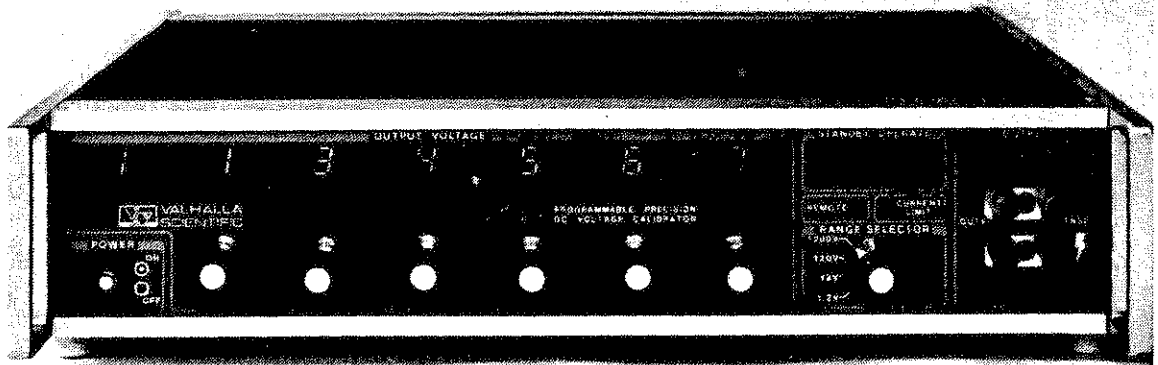


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2701 B

Programmable Precision DC Voltage Calibrator

Operating and Maintenance Manual



CERTIFICATION

Valhalla Scientific, Inc. certifies that this instrument was thoroughly tested and inspected and found to meet its published specifications when it was shipped from the factory. Valhalla Scientific, Inc. further certifies that its calibration measurements are traceable to the National Bureau of Standards to the extent allowed by NBS's calibration facility.

WARRANTY

The warranty period for this instrument is stated on your invoice and packing list. Please refer to these to determine appropriate warranty dates. We will repair or replace the instrument during the warranty period provided it is returned to Valhalla Scientific, Inc. freight prepaid. No other warranty is expressed or implied. We are not liable for consequential damages. Permission and a return authorization number must be obtained directly from the factory for warranty repair returns. No liability will be accepted if returned without such permission.

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SECTION I - GENERAL INFORMATION

1.1 DESCRIPTION

1.2 Valhalla Scientific's Model 2701B Programmable Precision DC Calibrator is a highly stable precision DC voltage source. Front panel switches permit selection of output voltages from 1 microvolt through 1,222.221 volts in four ranges, with a resolution of one part in one million.

1.3 The instrument uses a precision reference for output control. The voltage level of this reference is dependent on the duty cycle of a digital pulse generator which, in turn, is controlled by the front panel voltage selection switches. Since this technique eliminates the shortcomings of conventional resistive voltage dividers, the accuracy and stability of the instrument are independent of switch control resistance. Digital control of the pulse generator greatly simplifies remote voltage selection through an optional IEEE-488 interface.

1.5 OPTIONAL EQUIPMENT

1.6 A number of optional equipment items are available to increase the utility of the Model 2701B Programmable Precision DC Calibrator. These are described in the following paragraphs.

1.7 OPTION "TL-3" IEEE-488

1.8 Option IEEE-488 permits control of the Model 2701B through the IEEE-488 General Purpose Interface Bus. The bus coupler is mounted inside the instrument and a 24-pin IEEE-488 connector and address switch are mounted on the rear panel. This option may be installed at the time the instrument is purchased or may be incorporated as a factory retrofit. Installation and operating instructions are contained in Section VI.

1.9 OPTION C

1.10 Option C is a 48 inch shielded cable with a dual banana plug on one end, and alligator clips on the other.

1.11 OPTION BBC

1.12 Option BBC is a 48 inch shielded cable with dual banana plugs on both ends.

1.13 OPTION PRM

1.14 Option PRM enables the output polarity of the Model 2701B to be reversed via the IEEE-488 interface.

1.15 MODEL 2704

1.16 The Model 2704 is a precision 100:1 resistive voltage divider. When used with the Model 2704, the 2701B will provide 0.12 volts full scale with 0.1 microvolt resolution on its 12 volt range, and 12 millivolt full-scale with 10 nanovolt resolution on its 1.2 volt range.

SECTION II - INSTALLATION

2.1 INTRODUCTION

2.2 This section of the manual contains information for receiving inspection and installation of the Model 2701B Programmable Precision DC Voltage Calibrator.

2.3 INITIAL INSPECTION

2.4 If the shipping container shows evidence of intransit damage, such damage should be immediately brought to the attention of the carrier and the nature of the damage noted on the bill of lading.

2.5 Unpack the instrument and retain the shipping container until the instrument has been inspected for possible damage in shipment. If in-shipment damage is observed, notify the carrier and obtain his authorization for repairs before returning the instrument to the factory. If the shipping container shows evidence of damage in transit, but the instrument does not, it may be advisable to perform the calibration procedure of Section V to determine that the instrument has not sustained internal damage.

2.6 POWER REQUIREMENTS

2.7 The instrument requires 115 or 230 volts AC, 50-60 Hz, at 40 watts. A detachable line cord with appropriate connector is provided.

2.8 INSTALLATION

2.9 Installation requires only that the line cord be plugged into a power receptacle. The instrument output appears on two banana jacks on the front panel. Two additional banana jacks serve as sensing inputs. These should be connected directly across the load terminals when the resistance of the leads between instrument output and the load will produce a significant voltage drop. The sensing inputs cause the instrument to maintain the voltage across the load at that selected with the front panel switches. The sensing jacks are, otherwise, connected to their respective voltage jacks, using the straps provided. If the optional IEEE-488 interface is installed, the instrument may be connected to a computer or controller through the IEEE-488 24-pin connector on the rear panel.

2.10 The Model 2701B is intended to be operated in environments where the temperature does not exceed 50°C (122°F). If higher temperatures are anticipated, external means must be provided to maintain the temperature around the Model 2701B at or below 50°C.

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SPECIFICATIONS

270 HB

DC VOLTAGE RANGES	1.2V 12V 120V 1200V
RESOLUTION	1 μ V 10 μ V 100 μ V 1mV
ACCURACY (25°C \pm3°C)	\pm 10ppm of setting \pm 4 ppm of range \pm 2 μ V
TEMPERATURE COEFFICIENT	1 ppm/ $^{\circ}$ C.
STABILITY	5 ppm/30 days.
RIPPLE AND NOISE	4 ppm of range \pm 2 μ V pk to pk (.1Hz to 10Hz)
LOAD REGULATION	10 ppm from no load to full load.
CURRENT	20mA
SETTLING TIME	3 ms/V of change ascending (and descending for changes < 100V) plus 300ms. 10ms/V for descending changes greater than 100V.
WARM UP TIME	Within 30 ppm of final value within 15 seconds, 30 minutes to rated accuracy
ISOLATION	Optical: Output may float up to 500 volts from chassis potential.
SHORT CIRCUIT PROTECTION	Current limits at 25 milliamperes and automatically switches to standby after 100 milliseconds overload.
OPERATIONAL ENVIRONMENT	0 to 50 $^{\circ}$ C, 0 to 80% relative humidity, to 10,000 ft.
STORAGE ENVIRONMENT	-20 to +85 $^{\circ}$ C, 0 to 95% relative humidity, 30,000 ft.
FULL SCALE VOLTAGE	1222221 (Decimal point ignored).
POWER REQUIREMENTS	40 watts at 115 or 230 VAC, \pm 10%, 50 to 60 Hz.
WEIGHT	18 pounds net; 25 pounds shipping weight.
SIZE	17 inches W, 3-1/2 inches H, 14 inches deep.

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SECTION III - OPERATION

3.1 INTRODUCTION

3.2 This section of the manual contains complete operating instructions for the Model 2701B Programmable Precision DC Voltage Calibrator.

3.3 FRONT PANEL CONTROLS AND INDICATORS

3.4 A push-on, push-off switch at the left of the front panel connects the external power source (115/230 VAC at 50/60 Hz) to the instrument's internal supply circuits. Six twelve-position, rotary switches are located to the right of the power switch. Each controls a single decade (plus overrange and carry) of the output voltage selection. The switches are arranged in descending order, with the control for the least significant decade on the right. The switch knobs have a transparent skirt on which position numbers are silk screened. A white area, silk screened on the panel, highlights the number at the top of the skirt in each switch position.

3.5 A seven-segment LED display device is located directly above each switch to indicate the value selected in that decade. The seventh LED, located above the power switch, displays a "1" if the most significant decade switch is set at or above "10". The displays indicate the voltage selected by the switches when the instrument is in the operate mode. The knob skirt numbers indicate the selected output value when the instrument is in the standby mode, since the displays then show only zeros.

3.6 A four-position rotary range selection switch is located to the right of the voltage selection switches. Full scale ranges of 1.2 volts, 12 volts, 120 volts, and 1200 volts are selectable and the selected range is indicated by illumination of one of four LED's indicating decimal point position.

3.7 Two pushbutton switches are located above the range switch to select standby and operate modes. One of two LED's will be illuminated to indicate the mode of operation; a third LED (REMOTE) indicates when the instrument is being remotely controlled. The CURRENT LIMIT LED is illuminated when excess current is flowing in the output.

3.8 REAR PANEL FEATURES

3.9 The line cord connector and the AC power fuseholder are located on the rear panel of the instrument. If the IEEE-488 Interface is installed, a 24-pin interface connector and an address switch will also be located on the rear panel. Consult the IEEE-488 interface option for instructions on setting the device address switch.

3.10 OPERATING PROCEDURE

3.11 Turn on power, wait five seconds then press the OPERATE pushbutton. Select the desired range. Note that the decimal point will move from one digit display to another as the range switch is moved from one position to another. Set the six voltage selection switches so that the desired voltage is indicated by the seven-segment displays. Press the STANDBY button to remove the output voltage before making any connections at the output terminals.

CAUTION

The voltage at the front panel output and sense terminals, when operating on the 120 and 1200 volt ranges, is dangerous to life. Contact with these terminals while the instrument is in the operate mode may result in serious injury or death. The user should exercise extreme caution to insure that the instrument is in the stand-by mode before connecting or disconnecting leads to the output or sense terminals.

3.12 Each rotary decade switch has positions from 0 to 11. To prevent display ambiguity when a switch is placed in position 10 or 11, a carry is automatically transferred to the next most significant decade, adding one to that decade.

3.13 In the event of an overload, where the current is in excess of 20 milliamperes, but less than 25 milliamperes, the CURRENT LIMIT LED will be illuminated. If current is increased above 25 milliamperes, the instrument will automatically switch to the STANDBY mode after 100 milliseconds. The STANDBY indicator will be illuminated and the voltage, and the selected voltage display, will be reduced to zero. Operation may be resumed by reducing the current requirements of the load and pressing the OPERATE button.

3.14 In some applications, the resistance of the leads between the instrument and the load may cause the voltage across the load to be significantly less than that appearing at the instrument output terminals. To maintain the voltage across the load at the value selected, with the Model 2701B front panel switches, external sense terminals have been provided so that true four-wire regulation may be used. To utilize this feature, disconnect the straps between the output and sense terminals. Connect the positive sense terminal to the positive terminal of the load. Connect the negative sense lead to the negative terminal of the load. Since current flow in the sense

leads is negligible, the voltage at the sense inputs is that appearing across the load. The instrument then automatically adjusts its output voltage to compensate for the voltage drop in the output leads and to maintain the selected output voltage at the load terminals.

3.15 Instructions for remote operation through an IEEE-488 General Purpose Interface Bus are provided in the IEEE-488 Interface Option manual. Note that for safety reasons, the front panel STANDBY and OPERATE pushbuttons continue to operate in the remote mode.

SECTION IV - THEORY OF OPERATION

4.1 GENERAL

4.2 This section of the manual provides functional and detailed descriptions of the circuits of the Model 2701B Programmable Precision DC Calibrator. The functional descriptions are referenced to the block diagram of Figure 4-1, and are intended to assist the user in gaining a general understanding of the instrument prior to study of the detailed circuit descriptions. The material of this section, together with that of Section V, will provide the information necessary for maintenance of the instrument.

4.3 FUNCTIONAL DESCRIPTION

4.4 The Model 2701B comprises a coarsely regulated switching power supply feeding a series regulator. The output of the regulator is controlled by a precision variable reference voltage. A fixed voltage is precisely divided to produce the precision reference voltage. The division of the fixed voltage is accomplished, using digital techniques, by switching the reference on for a precise percentage of the time, determined by the setting of the front panel voltage selection switches. This produces a series of pulses with an average DC level precisely related to the pulse width. An oven-stabilized voltage reference provides the fixed voltage. The accuracy of the instrument is primarily determined by the stability of this voltage and is in the order of 0.0015%. The following circuit descriptions are arranged under headings corresponding to the designators in Figure 4-1.

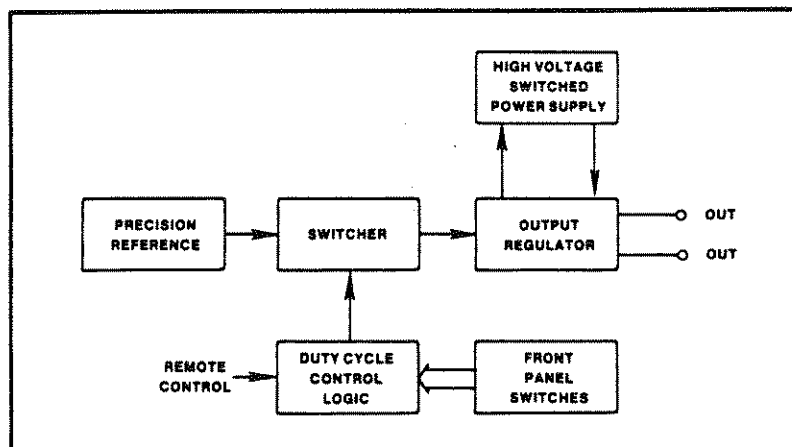


Figure 4-1. Block Diagram, Model 2701B Precision DC Voltage Calibrator.

4.5 SWITCHING POWER SUPPLY

4.6 The primary power source for the series regulator is a 1500 volt, full-wave, filtered power supply. The circuit is conventional except that primary power to the power transformer is electronically switched. By interrupting the power input, the switching circuit provides coarse regulation of the power supply output at approximately 100 volts above the desired instrument output voltage. This feature minimizes the power dissipated in the series regulator. The circuit is designed to switch only at the time of zero crossing of the AC sine wave input.

4.7 SERIES REGULATOR

4.8 The series regulator consists of a group of transistors connected in series between the output of the switching power supply and the output terminals. An operational amplifier provides the control that determines the voltage drop across the transistors and, thus, the voltage appearing at the output terminals. When the sensing terminals are connected directly to the output terminals, the feedback to the operational amplifier causes it to compare the voltage at the output terminals with the reference voltage. The output of the operational amplifier forces the voltage at the output terminals to the level of the reference. If the sense inputs are connected across the load terminals, the comparison is between the voltage across the load terminals and the reference voltage. The selected output voltage is then maintained across the load and the voltage at the instrument output terminals will rise to whatever level is necessary to compensate for the drop in the leads between the output terminals and the load terminals.

4.9 DIGITALLY CONTROLLED VARIABLE REFERENCE

4.10 The variable reference consists of an ultraprecise, variable duty cycle pulse generator driving an electronic switch of single pole, double throw configuration. The output of the switch alternates between a fixed, precise voltage level and ground. The pulse duty cycle is precisely determined by the duty cycle of the pulses driving the switch and the amplitude is determined by the precise voltage input level. Thus the average voltage level of the pulse train, after filtering, is precisely proportional to the pulse duty cycle. The duty cycle of the pulse generator is determined by BCD counter circuits, described in the following paragraph, which are controlled by the front panel switches or, alternately, by a remote controller through the optional IEEE-488 Interface.

4.11 CONTROL LOGIC

4.12 In response to signal inputs from front panel push-buttons, the control logic selects standby or operate modes, and automatically selects standby when an overload occurs. A

remote enable input from the optional IEEE-488 Interface will disable the front panel voltage selection switches and transfer control to an external controller.

4.13 DETAILED CIRCUIT DESCRIPTION

4.14 This section provides a detailed description of each circuit of the 2701B DC Calibrator. Unless otherwise specified, the reference designators appearing in this section are those of the schematic diagrams, Figures 5-3 and 5-4. Where multiple circuit IC's are involved, the individual circuits are identified by their output terminal numbers. For example, IC44-10 identifies that circuit of quad NOR gate IC44 that has its output connected to pin 10.

4.15 To facilitate an understanding of circuit operation, a simplified diagram of the high-voltage power supply and output regulator circuits is included as Figure 4-2. Its purpose is to provide an overall view of the circuit interconnections. It is not intended to replace the complete schematics, although the reader may wish to refer to this diagram as well as those of Figures 5-3 and 5-4. Note that all circuit components are not included in Figure 4-2 and, also, that some that are may not be identified.

4.16 SWITCHING POWER SUPPLY

4.17 The switching power supply provides the power for the instrument's series regulator which provides the output voltage. Primary power is applied to transformer T1 through a diode bridge consisting of diodes CR42 through CR45. Transistor Q20, connected across the DC output terminals of the diode bridge, is driven by Q19. Q19 is driven, through optical isolator IC6, from comparator IC3 and zero crossing detector IC5. The bridge acts as an open circuit unless Q20 is conducting. When Q20 is turned on, the bridge conducts in both directions, permitting the primary AC to pass to the transformer. With Q20 conducting continuously, the bridge consisting of CR50 through CR53, connected to the secondary of T2, rectifies the AC, providing approximately 1500 volts DC to filter capacitors C9 through C12. Lower voltages are produced when Q20 does not conduct continuously. Resistors R128-R145 equalize the voltage across the filter capacitors. Note, as shown in Figure 4-2, that the high voltage power supply has a resistance path from positive to negative which is tapped to provide a ground connection. The drop across the resistance from positive to ground will be maintained at approximately 100 volts by the circuit of IC3 and IC5. IC2 utilizes the drop across the resistance connected from ground to the negative terminal as a sense input to the summing junction at its non-inverting input.

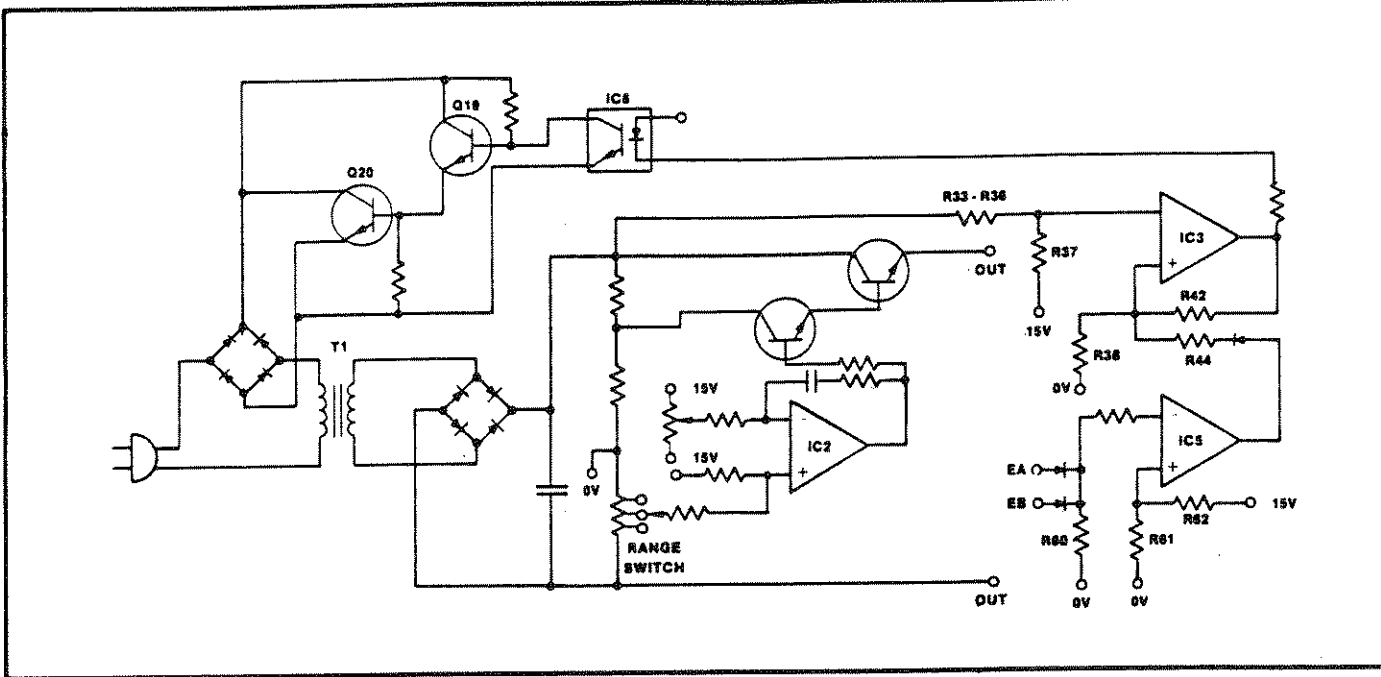


Figure 4-2. Simplified Schematic Diagram, Model 2701B Precision DC Voltage Calibrator.

4.18 Power supply switching is controlled by a gated comparator circuit comprised of operational amplifier IC3 and associated components. The output of the comparator can change state only during the zero crossings of the AC input sine wave.

4.19 CR11 and CR12 are connected to opposite ends of a secondary winding of T1. Thus, during a narrow "window" that extends to either side of the zero crossing point of the AC input, the inverting input of IC5 will be high, forcing its output high. This will forward bias CR8 and apply a fixed bias of approximately +2.5 volts at the inverting input of IC3 to which the voltage at the junction of R36 and R37 is compared. If the junction voltage is lower than that at the non-inverting input, the output of IC3 will be forced high, turning off the LED in optical isolator IC4 which will place (or maintain) Q20 in conduction. If the junction voltage is higher than that at the non-inverting input, the output of IC3 will go low, turning on the LED in IC4 which will turn off Q20. When the AC input to CR11 or CR12 moves outside the "window", the output of IC5 goes low, back biasing CR8 and removing the bias from the non-inverting input of IC3. This allows the positive feedback, through R42, to hold the output of IC3 at the high or low limit to which it was driven as a result of the input comparison. Thus, between zero-crossings of the AC input, the output of IC3 remains at one limit or the other.

4.20 The resistor values of the voltage divider are chosen so that whenever the voltage drop across the total resistance exceeds 100 volts, the output of IC3 will go low, turning on the LED in IC6. When the voltage drop falls below 100 volts, the output of IC3 goes high turning off the LED in IC6 which causes Q20 to conduct. The switching power supply output is thus maintained such that the drop across the series regulator transistor bank is maintained at approximately 100 volts.

4.21 When the operator selects a higher output voltage, the switching power supply can respond rapidly by allowing Q20 to conduct continuously until the selected voltage is reached. Zener diode CR7 prevents the voltage applied to the inverting input of IC3 from exceeding 6.2 volts. A selected decrease in voltage can only be accomplished by discharging the filter capacitors. To provide a quick discharge path, a winding on T2 is rectified by CR54 and filtered by C14, producing approximately 500 volts DC. The output of this supply is connected to the negative output of the switching power supply through R132. The result is that even at moderate output voltages, the filter capacitors can be discharged quickly with this reverse current. The current flow is from ground through CR55, the transformer secondary, C14 and R132.

4.22 SERIES REGULATOR

4.23 The series regulator reduces the output of the switching power supply to the precise voltage selected with the front

panel switches. It also limits the output current to 25 milliamperes if a short circuit is applied to the output terminals. If the overload exists for approximately 100 milliseconds, it provides an output to the control logic to place the instrument in the standby mode. The series regulator consists of transistors Q7 through Q18, operational amplifiers IC2 and IC4, and optical isolator IC7. IC2 is a precision instrumentation amplifier.

4.24 The odd numbered transistors drive the bases of the even numbered transistors which pass the instrument's output current. Voltage regulation is provided by Q17 and Q18. Under steady-state conditions, the drop across Q7 through Q18 is held to approximately 100 volts by the switching power supply as described in 4-17. Therefore, the voltage at the collector of Q18 is at approximately 17 volts. For a brief period after a sudden decrease in output voltage selection, the drop across the series regulator will be much greater than 100 volts, as discussed in paragraph 4-20. During such periods, the entire string of transistors will safely share the large voltage drop and the power dissipation.

4.25 IC2 serves as a comparator controlling Q17 and Q18. The output of the variable reference is compared with a voltage that is proportional to the instrument's output voltage. Contacts of relays K1 and K2 select the appropriate combination of R39, R40, R41, R45 and R46 for each of the instrument's voltage ranges so that the voltage at the junction of R39 and R40 is within the input range of IC2. The contacts of K1 are closed for the 12 volt range, the contacts of K2 are closed for the 1200 volt range and the contacts of both relays are open for the 120 volt range.

4.26 The current from the series regulator flows through R14. When the current reaches approximately 25 milliamperes, the voltage drop across R14 is sufficient to forward bias CR2 and DS8, pulling Q17 and Q18 toward cutoff. This portion of the circuit limits the output current if a short circuit occurs. DS8 is the front panel OVERLOAD indicator and is illuminated when the current exceeds 20 milliamperes. IC4 monitors the voltage at the non-inverting input of IC2. The voltage at that point is nominally zero because the negative input of the divider balances the positive reference voltage. If a short circuit occurs, the output voltage drops and the voltage at the non-inverting input of IC3 swings positive. R51 and C5 delay IC4's reaction to this change for approximately 100 milliseconds. The output of IC4-6 then goes high, turning on the LED in optical isolator IC7. The photo-transistor in IC7 then conducts, grounding the input to NOR gate IC44-11. The instrument is then placed in the standby mode in the same manner as if the STANDBY push button had been pressed.

4.27 DIGITALLY CONTROLLED VARIABLE REFERENCE

4.28 The variable reference provides a very precise voltage which is proportional to the settings of the front panel voltage select switches, or to BCD inputs through the optional IEEE-488 interface. It consists of IC1, IC8 through IC42, IC47, DS1 through DS7, and Q1 through Q4. IC47 is highly accurate, oven-stabilized +7 volt reference. Field effect transistors Q2 and Q4 form a single pole double throw switch. Their output feeds reference filter IC1 via R18 and R20. When Q2 conducts, the switch output is connected to the 7 volt reference. When Q4 conducts, the switch output is connected to ground. A variable reference is produced at the output of the switch by alternately switching on Q2 and Q4, using a pulse of precisely adjustable duty cycle. To illustrate, a 50% duty cycle pulse would result in a reference voltage of 3.5 volts; a 25% duty cycle pulse would produce 1.75 volts, and so on.

4.29 There are two variable duty cycle pulse generators in the variable reference circuit. One is controlled by the three highest order switch decades, the other by the low order switch decades. The operation of the latter is typical of both, and will be described in detail. The block diagram of Figure 4-3 shows how the integrated circuits in this section are interconnected.

4.30 A crystal-controlled, one megahertz clock, IC42, drives a pair of three-decade counters which both run continuously. The reference counter, IC18 through IC20, consists of three TTL up-down counters connected in the down-count mode. Each time the count reaches 000, the reference counter outputs a pulse. The pulse is the clock input to one half of dual D flip flop IC13. The data input of IC13 is grounded, so that the pulse resets the flip flop. The same pulse is also the LOAD input of the presettable counter which consists of counters IC32 through IC34 connected in the count-down mode. Each time the presettable counter reaches count 000, it outputs a pulse to the SET input of flip flop IC13.

4.31 The parallel data inputs of IC32-IC34 are fed indirectly from the voltage selection switches, or the optional IEEE-488 interface, in the following manner. BCD voltage selection codes from the switches, or from the IEEE-488 interface, are fed to the address inputs of IC39 through IC41, which are 256-bit, programmable read-only memories (PROM's). These are the control PROM's and are organized as thirty-two 8-bit words. They are programmed to convert BCD at their address inputs to seven-segment display codes at their data outputs, to furnish a carry to the next higher order decade for inputs of ten or eleven and to provide an output one number higher than the BCD input when a carry is received from a lower order decade.

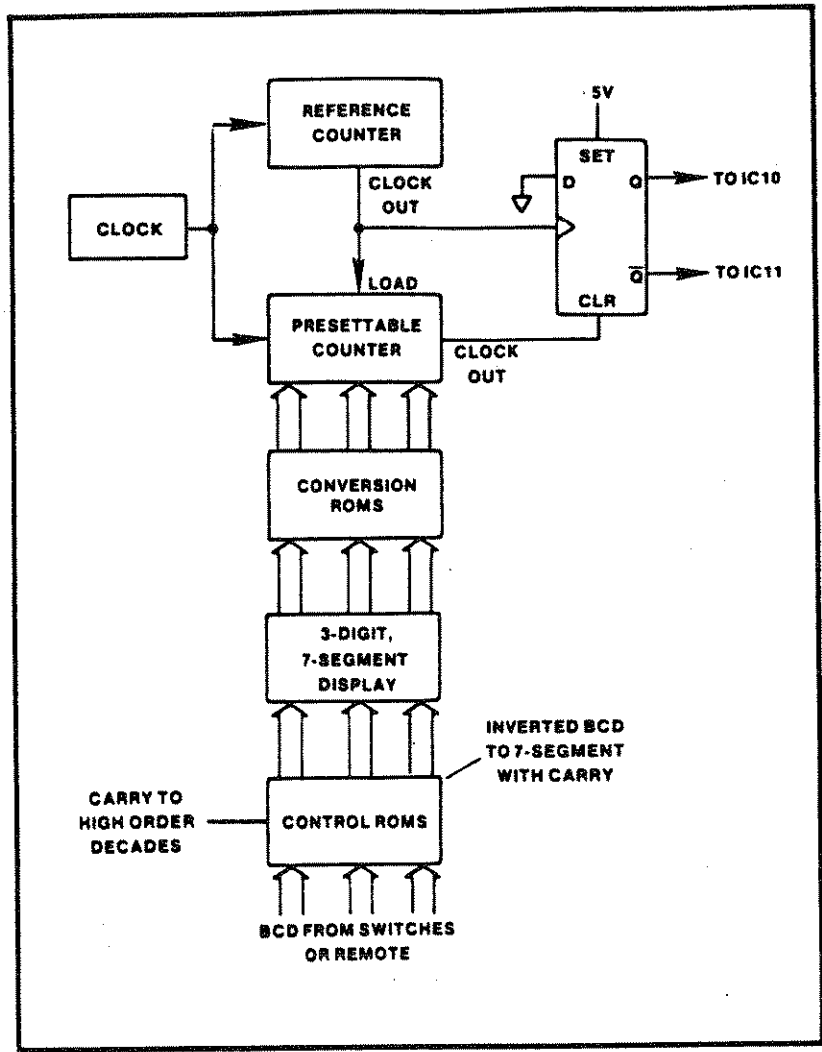


Figure 4-3. Block Diagram, Variable Reference Source.

4.32 The data outputs of the control PROM's drive seven-segment displays DS5 through DS7, and feed the inputs of conversion ROM's IC32 through IC34. The conversion ROM's are CMOS seven-segment to BCD converters. The outputs of the conversion ROM's are always BCD numbers between 000 and 999, because all carries have been performed by the control PROM's. These outputs are fed to the parallel data inputs of the presettable counter.

4.33 A typical counting sequence occurs as follows. An input number, less than 999, is placed on the data inputs of the presettable counter. When the reference counter next reaches the count of 000, the input number is loaded into the presettable counter. The reference counter is next at 999, and the presettable counter is at the input number. The two counters count down simultaneously. The presettable counter, since it was loaded with a number less than 999, reaches its terminal count first, setting flip flop IC13. Later, the reference counter reaches 000, resetting IC13. The process is then repeated, and continues at a rate of 1000 times per second. The output of the flip flop is a pulse with a 1000 Hz repetition rate. The duty cycle of the pulse is proportional to the BCD digits at the control PROM inputs. Selecting 999 yields a pulse with a duty cycle of 99.9%; selecting 500 produces a 50% duty cycle, etc.

4.34 The Q and \bar{Q} outputs of flip flop IC13 control the conduction periods of FET switch transistors Q2 and Q4, to which they are coupled through optical isolators IC10 and IC11, respectively. Q2 is connected to the 7 volt reference, and Q4 is connected to ground. At the point where the two FET's are connected together, a series of positive-going pulses are produced with their peaks at precisely +7 volts and their negative peaks at 0 volts. The duty cycle is identical to the output of IC13. The pulse passes through R16, R18, R20, R21, R23 and R24 to the junction of C3 and C7. The circuit of IC1 is a capacitance multiplier that filters the pulse input, producing an output that is the average value of the pulsed input. This average voltage is dependent on the input duty cycle, which is determined by the front panel switch settings and may be any of 1000 precisely repeatable values.

4.35 The high order section of the variable reference consists of reference counters IC14 through IC17, presettable counters IC21 through IC24, conversion ROM's IC28 through IC31, displays DS1 through DS4, and control PROM's IC35 through IC38. The high order counters differ from the previous description in the following respects. First, there is a fourth decade. Its purpose is to accept a carry from the next decade; therefore, there is no switch connected to it. Display DS1 is driven by its control PROM, IC35, and displays a "0" or a "1".

4.36 Second, the reference counter is loaded to the count of 1299 each time it reaches 0000. This requires the use of both halves of dual flip flop IC12. One half functions as does IC13, loading the presettable counter, while the other is used to provide the reference counter's clock pulse. The data input of IC12 is tied to ground. When the reference counter reaches 0000, it drives the clock input of IC12, resetting it. The output at IC12-5 then loads the counter. IC12 is set one count later by the signal from the 1-MHz system clock. As a result, the reference counter counts down from 1299 to 0000, then, a few nanoseconds later, it is again preset to 1299 to begin a new cycle. There are a total of 1300 counts in the sequence.

4.37 The switch transistors for the high order portion of the variable reference are Q1 and Q3. The outputs of the high order and low order switches are combined at the junction of R16, R17 and R18. Note that the ratio of the series combination of R18 and R20 to R17 is 1300:1 with R18 set at midrange. This ratio establishes the relative weights of the pulses from the high order and low order decades. The two pulses are combined in a 1300:1 ratio, filtered as described in paragraph 4-34 and provided some additional filtering by R25 and C4. The filtered voltage is applied to the non-inverting input of IC2. The output of IC2 is then applied to the base of Q17 as the reference voltage for series regulator, as described in paragraph 4-26.

4.38 CONTROL LOGIC

4.39 The control logic selects standby, operate and remote modes in response to control inputs. Operate and standby may be selected using front panel push buttons. Standby may also be selected automatically, as described in paragraph 4-26. The remote mode may be selected only by an external device through the optional IEEE-488 interface. The control logic consists of one half of a CMOS dual D flip flop, IC43, and a CMOS quad NOR gate, IC44, and transistors Q5, 6, 21, 22 and 23.

4.40 When the OPERATE push button is pressed, it places a logic low on IC44-1 and IC44-2. A high is produced at IC44-3, which operates the set input of the flip flop IC43, setting its pin 12 low. Pin 12 is connected to IC44-8. Since the output of IC44-9 is always low unless the system is in the remote control mode, a high is produced at IC44-10. This turns on Q6, placing a ground on the wipers of all the voltage select switches. With ground applied to their wipers, the switches produce inverted (ground true) BCD codes. The control ROMs, IC35 through IC41, are programmed to accept the inverted codes.

4.41 When the STANDBY push button is pressed, it resets flip flop IC43 which sets its pin 12 low. The switch wipers are pulled up by R121 causing all of the inputs to the control

ROM's to be high. Since all of the inputs to the ROM's are zeros, the output of the instrument drops to zero. Pin 12 of IC43 is also connected to the base Q21. Q21, when turned on, completes the circuit for the STANDBY LED, DS13, and turns Q22 off. When Q21 is turned off, Q22 is turned on and completes the circuit for the OPERATE LED.

4.42 Remote control is initiated by placing a low, from the IEEE-488 interface, on IC44-6. This low turns on Q23 to complete the circuit for the REMOTE LED, DS12.

4.43 INSTRUMENT POWER SUPPLY

4.44 The instrument power supply, as distinguished from the switching power supply, provides power for the instrument's internal circuits.

4.45 The operational amplifiers in the instrument are powered by positive and negative 15-volt supplies. The power source is transformer T2. Its output is rectified by diodes CR56 to CR59 and filtered by C15 and C18. IC45 and IC46 regulate the two rectified outputs to +15 volts and -15 volts respectively.

4.16 A positive 12 volt unregulated supply, consisting of CR46, CR47 and C19, provides power to the range-select relay coils. Power for the logic ICs and displays comes from an unregulated +5 volt supply consisting of CR48, CR49 and C20.

SECTION V - MAINTENANCE

2701B CALIBRATION

5.1 INTRODUCTION

5.2 This section provides maintenance information for the Model 2701B Programmable Precision DC Calibrator. Included are a recommended test equipment list and calibration procedures.

5.3 RECOMMENDED TEST EQUIPMENT

5.4 This procedure requires the following items of test equipment:

Digital Voltmeter	Guildline 9574 (1 microvolt sensitivity with accuracy of 0.01% or better).
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Calibration System	Guildline 9936
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or the following:

Precision Kelvin-Varley Voltage Divider	John Fluke Model 720A
Voltage Reference Standard	John Fluke Model 731B
Null Detector	John Fluke Model 845AB

5.5 CALIBRATION PROCEDURE

5.6 The following procedure should be performed at routine intervals to insure that the instrument remains within specified limits. In addition, calibration should be performed whenever repairs have been completed involving accuracy determining components.

CAUTION

The voltage at the front panel output and sense terminals may be dangerous to life. Contact with the terminals while the instrument is in the operate mode may result in serious injury or death. The user should exercise extreme caution to insure that the instrument is in the standby mode before connecting or disconnecting leads to the output or sense terminals.

5.7 If the Guildline Calibration System is used, refer to its operation manual for instructions on equipment connections. If

other equipment is used, connect it to the 2701B as in Figure 5-1. Verify that the sense terminals of the Model 2701B are strapped to their respective output terminals.

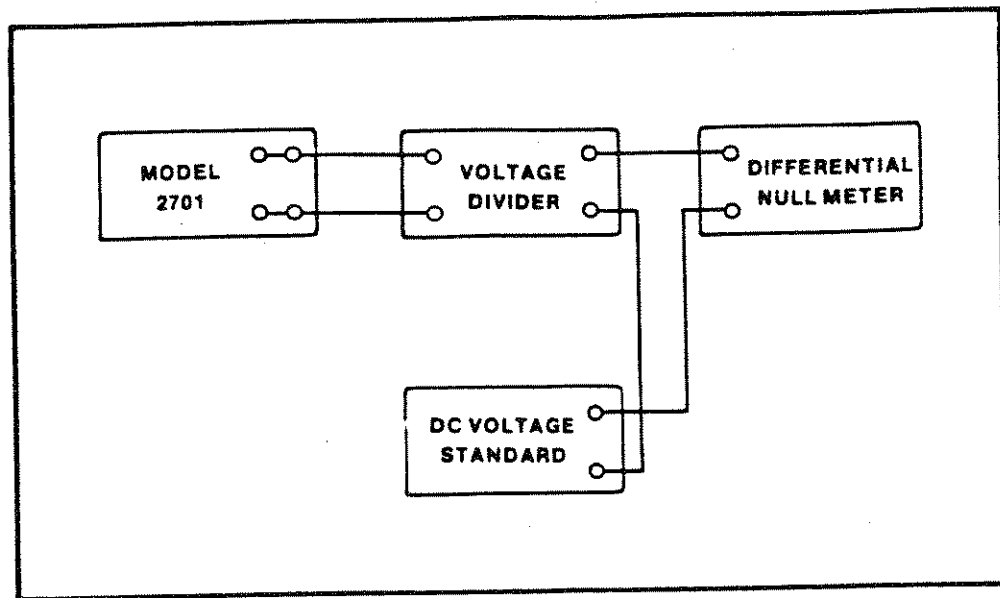


Figure 5-1. Test Equipment Connections.

5.8 CALIBRATION SET-UP

5.9 Connect the 2701B per Figure 5-1. Apply power to the 2701B, ensure the 2701B is in standby and in the 1.2V range. Allow 1 hour for warm up stabilization. Note: Keep 2701B Top Cover in place during calibration.

5.10 ZERO CALIBRATION

5.11 Substitute the voltage standard with a short circuit.

5.12 Set the voltage divider to 0.99999X.

5.13 With the 2701B in the 1.2 volt range, adjust R26 for a null detector reading of less than ± 2 microvolts.

5.14 Select the 12. volt range and adjust R157 for a null detector reading of less than +20 microvolts.

5.15 LINEARITY CALIBRATION

5.16 Connect the output terminals of the 2701B to the DVM. Select the 12 volt range and press the OPERATE push button on the 2701B.

5.17 Set the output switches of the 2701B to 0000999.

5.18 Adjust R18 for a DVM reading of 9.990 millivolts.

5.19 Set the rightmost output switch to 10, so the 999's will carry to 0001000.

5.20 Adjust R148 for a DVM reading of 10.00 millivolts.

5.21 Repeat the steps of paragraphs 5-17 through 5-20 until optimum accuracy is achieved.

5.22 FULL SCALE CALIBRATION

5.23 Reconnect the test equipment as in Figure 5-1.

5.24 Set reference standard for 10 volts output.

5.25 Place the 2701B in the 12 volt range and set its switches to 10.00000. Set the voltage divider to .99999X.

5.26 Ensure the 2701B is in operate mode and adjust R23 for a null detector reading of less than +50 microvolts on the null detector.

5.27 Place the 2701B in the 1.2 volt range and set the Reference Standard to 1 volt. Ensure the voltage divider is still set to .99999X.

5.28 Ensure the 2701B is in the operate mode and adjust R158 for a null of less than +5 microvolts on the null detector.

5.29 Set the voltage divider to 0.10000. Set the Reference Standard for 10 volts. Place the 2701B in standby and switch to the 120 volt range.

5.30 Place the 2701B in the operate mode and adjust R41 for a null less than +50 microvolts on the null detector.

5.31 Place the 2701B in the standby mode. Set the voltage divider to 0.01000. Switch the 2701B to the 1200 volt range.

5.32 Place the 2701B in the operate mode. Allow 3 minutes for the 2701B to settle then adjust R46 for a null less than +50 microvolts on the null detector.

5.33 Repeat the steps in paragraphs 5-22 through 5-32 allowing the 2701B to settle until accuracy is achieved.

SECTION VI
2701B IEEE-488 OPERATION INSTRUCTIONS

6.0 GENERAL

6.1 This manual contains information regarding the operation of options TL-3 IRP and PRM.

The TL-3 option is a full talk/listen IEEE-488 (1981) interface with the capability of full control of the 2701B and options.

The option IRP is an additional option giving the capability of controlling an external current source such as the Valhalla 2500EP.

The option PRM is an additional option giving the capability of controlling the polarity of the 2701B's output voltage.

These options are factory fitted as ordered.

6.2 BUS OPERATION

6.3 IEEE-488, AN EXPLANATION OF THE BUS

The reader that is unfamiliar with the IEEE-488 standard will want to review the following paragraphs which provide explanations of the terms, commands and some examples. For a complete explanation of the requirements the reader should refer to the IEEE-488 standard itself.

6.4 DEFINITIONS

The following are the definitions of the terms used in describing the IEEE-488 interface bus.

- BI-DIRECTIONAL BUS : A "highway" used for two-way communication, both input and output data being conveyed on the same lines.
- BUS : A data link which is usually a set of several wires within a multi-wire cable.
- BIT PARALLEL : Used to describe data which is present simultaneously on a number of wires in a bus.
- BYTE : A group of data bits (usually 8) which is treated as a single item of data.

- BYTE SERIAL : Information, in bit-parallel bytes, transferred between devices sequentially.
- DEVICE DEPENDANT MESSAGE : A message containing data/commands specifically for a recipient device.
- HANDSHAKE : A sequence of signals controlling the transfer of data over a bus. Each condition in turn must occur before the next.
- INTERFACE : The part of an instrument or system which enables it to be connected to another via a bus.
- INTERFACE MESSAGE : A message that is for interface management.
- LOCAL OPERATION : Operation of a device by its front panel controls, also known as MANUAL CONTROL.
- REMOTE OPERATION : Operation of a device under the control of another via a bus.

The IEEE-488 interfacing standard (also known as IEC DTC66 (WG3), ANSI MC1-1, GPIB, HP-IB, etc) defines a bi-directional bus for interconnecting programmable instrumentation in a bit-parallel, byte serial manner. It determines limitations as follows:

A maximum of 15 devices may be interconnected by a single bus.

The total bus length may not exceed 20 meters or the number of devices multiplied by two, whichever is shorter.

Maximum transmission rate is 1 megabyte per second.

All bus data is digital.

Of the devices on the bus, only one may be a controller, exercising control over all other devices and also capable of operating as a "talker" or a "listener". The other devices may be listeners (only able to receive data), talkers (only able to transmit data) or both (as 2701B is). The controller addresses other devices and commands them to listen, talk or neither (as required). Only a single device may talk at any one time.

The interconnecting cable contains 16 signal wires and 8 ground wires linking all devices into a complete system. The cable connectors consist of a plug/socket combination that enable "daisy-chaining" of units.

The 16 signal wires comprise:

8 data wires (DIO 1 through 8)

5 management wires (ATN, EOI, SRQ, IFC and REN)

3 handshake wires (DAV, NRFD and NDAC)

It should be noted that all wires use "inverse logic", i.e. a low level is used to indicate a true state, while a high level indicates false.

- ATN - Is asserted by the controller whenever an address or an interface message is present on the bus.
- EOI - Is asserted by the talker to indicate the last byte of data, or it may be asserted by the controller (with the ATN wire asserted) to poll the state of the devices on the bus.
- SRQ - May be driven by any device on the bus. When asserted it is normally used to indicate to the controller that a device requires attention.
- IFC - Is driven by the controller to initialize the interfaces of all devices on the bus.
- REN - Is driven by the controller to indicate to all devices on the bus that they may (or may not) enter the REMOTE state.

The transfer of data on the bus is controlled by the 3 handshake wires such that the bus speed is reduced to that of the slowest device. The talker first waits for all devices to be ready to accept data (NRFD false) then puts the data on the bus with DAV true. It then waits for all devices to accept the data (NDAC becoming false). This process is repeated for every byte to be transferred.

Data is sent in 8-bit bytes on the DIO wires, usually (as in 2701B) using the ISO-7 standard ASCII characters. Table 6-1 lists the ASCII character set. Note that the table is divided into two main groups, the Primary Command Group and the Secondary Command Group. The Secondary Command Group is unused in the 2701B.

NO. BIT & DATA LINE NUMBER	COLUMN							ROW	
	0	1	2	3	4	5	6		7
0 0 0 0	0 0 0 0	0 0 0 0	0 0 0 0	0 0 0 0	0 0 0 0	0 0 0 0	0 0 0 0	0 0 0 0	0
0 0 0 1	0 0 0 1	0 0 0 1	0 0 0 1	0 0 0 1	0 0 0 1	0 0 0 1	0 0 0 1	0 0 0 1	1
0 0 1 0	0 0 1 0	0 0 1 0	0 0 1 0	0 0 1 0	0 0 1 0	0 0 1 0	0 0 1 0	0 0 1 0	2
0 0 1 1	0 0 1 1	0 0 1 1	0 0 1 1	0 0 1 1	0 0 1 1	0 0 1 1	0 0 1 1	0 0 1 1	3
0 1 0 0	0 1 0 0	0 1 0 0	0 1 0 0	0 1 0 0	0 1 0 0	0 1 0 0	0 1 0 0	0 1 0 0	4
0 1 0 1	0 1 0 1	0 1 0 1	0 1 0 1	0 1 0 1	0 1 0 1	0 1 0 1	0 1 0 1	0 1 0 1	5
0 1 1 0	0 1 1 0	0 1 1 0	0 1 1 0	0 1 1 0	0 1 1 0	0 1 1 0	0 1 1 0	0 1 1 0	6
0 1 1 1	0 1 1 1	0 1 1 1	0 1 1 1	0 1 1 1	0 1 1 1	0 1 1 1	0 1 1 1	0 1 1 1	7
1 0 0 0	1 0 0 0	1 0 0 0	1 0 0 0	1 0 0 0	1 0 0 0	1 0 0 0	1 0 0 0	1 0 0 0	8
1 0 0 1	1 0 0 1	1 0 0 1	1 0 0 1	1 0 0 1	1 0 0 1	1 0 0 1	1 0 0 1	1 0 0 1	9
1 0 1 0	1 0 1 0	1 0 1 0	1 0 1 0	1 0 1 0	1 0 1 0	1 0 1 0	1 0 1 0	1 0 1 0	10
1 0 1 1	1 0 1 1	1 0 1 1	1 0 1 1	1 0 1 1	1 0 1 1	1 0 1 1	1 0 1 1	1 0 1 1	11
1 1 0 0	1 1 0 0	1 1 0 0	1 1 0 0	1 1 0 0	1 1 0 0	1 1 0 0	1 1 0 0	1 1 0 0	12
1 1 0 1	1 1 0 1	1 1 0 1	1 1 0 1	1 1 0 1	1 1 0 1	1 1 0 1	1 1 0 1	1 1 0 1	13
1 1 1 0	1 1 1 0	1 1 1 0	1 1 1 0	1 1 1 0	1 1 1 0	1 1 1 0	1 1 1 0	1 1 1 0	14
1 1 1 1	1 1 1 1	1 1 1 1	1 1 1 1	1 1 1 1	1 1 1 1	1 1 1 1	1 1 1 1	1 1 1 1	15
	ADDRESS COMMAND GROUP (ACB)	UNIVERSAL COMMAND GROUP (UCB)	LOWER ADDRESS GROUP (LAG)	TALK ADDRESS GROUP (TAG)				STANDARD ISO 7 CHARACTERS	
	PRIMARY COMMAND GROUP (PCG)				SECONDARY COMMAND GROUP (SCG)				

1 Requires Secondary Command

Notes

1 Device Address messages shown in decimal

2 Message codes are

DC1	Device Clear	L111	Local Enable	S1K	Selected Device Clear
G1T	Device Trigger	PPC	Parallel Poll Configure	SPI	Serial Poll Disable
G11	Go to Local	PPU	Parallel Poll Unconfigure	SPE	Serial Poll Enable

Table 6-1. ASCII Character Codes.

The Primary Command Group is divided into 4 sub-groups as follows:

- 1) Addressed Command Group - commands applied only to addressed devices.
- 2) Universal Command Group - commands applied to all devices on the bus.
- 3) Listen Address Group - commands addressing listeners.
- 4) Talk Address Group - commands addressing talkers.

All data/commands in the above groups are sent with ATN asserted. Data with ATN false is "Device Dependent".

6.5 GENERAL 2701B BUS BEHAVIOR

6.6 Input Buffer

All incoming device-dependent data to the 2701B is placed in an input buffer as it is received. No action is taken on this data until any one of the valid delimiters is received. When the delimiter is received, then the data contained in the input buffer is decoded, actioned and the data erased. Input data is held off until the complete input buffer has been decoded and actioned.

Should an invalid character be detected in the input buffer, then the contents are only decoded and actioned up to that character, the rest being erased. An SRQ may be asserted at this time if required by the user. The valid input delimiters are Carriage Return or EOI asserted with the last character.

6.7 Output Buffer

An output buffer is continuously updated (even when in LOCAL) with the present state of the 2701B. This data may be read by the controller at any time.

6.8 IEEE Address

The address of the 2701B within the bus is set by means of a set of switches on the rear-panel. These switches form the address by a binary code (ON=1) as shown on the rear-panel. The user should note that the state of the switch is only read at the application of power to the 2701B.

6.9 REMOTE/LOCAL States

At power-up the 2701B will be in the LOCAL state, i.e., the front-panel controls are fully operational. In order for the 2701B to enter the REMOTE state the following sequence of events must occur (as stipulated by the IEEE-488 standard):-

- 1) The REN (Remote ENable) wire must be true (pulled low).
- 2) The 2701B must receive its listen address.

With an HP85 computer this is achieved by the statement REMOTE 709 (assuming the 2701B address is set to 9). In all future examples an address of 9 is assumed as is an HP85 controller.

When the 2701B is in REMOTE the REMOTE indicator on the 2701B front panel will be illuminated and all front-panel controls except POWER, OPERATE and STANDBY will be inoperative.

The controller may return the 2701B to the LOCAL state at any time by unasserting the REN wire (LOCAL 7 for the HP85), by sending the character "L", or by device clear.

6.10 SIMPLE COMMANDS AND EXAMPLES

6.11 Non-free Format Commands

6.12 "V" Command - Voltage Display.

For compatibility with previous 2701B IEEE-488 interface products this unit has the capability of receiving a 6 character output voltage command. This command is of the form Vxxxxxx. Each x represents the character whose least significant 4-bits are the digit required in that decade of the 2701B's display in order to provide the required output.

For example: the string V123456 - will cause 0123456 to be displayed
V:23456 - will cause 1023456 to be displayed
V::3456 - will cause 1103456 to be displayed

The user is referred to Table 6-1 for exact details of the LS 4-bits of the above characters.

6.13 "R" Command - Range Command

The range of the 2701B may be commanded by a 2-character command. This takes the form Rx. The digit x may be between 0 and 3 representing:

- 0 - 1.2V range
- 1 - 12V range
- 2 - 120V range
- 3 - 1200V range

This range command is usually used in conjunction with the Vxxxxxx command shown above.

e.g. sending the string R1V:00000 - will cause an output of 10V.

6.14 "I" Command - IRP and/or PRM Option Control

This command is used to directly control the control of the option IRP and PRM. This command takes the form Ixy. The first digit x is the control of the IRP option and takes the following meaning when used with a 2500EP:

9 - 100uA range
: - 1mA range
; - 10mA range
(- 100mA range
= - 1A range
> - 10A range
0,1,2,3,4,5,6,7,8,? cause no 2500EP range to be selected.

The second digit y is used to control polarity with the PRM option. If the least significant bit of the code is a 0 (e.g. 0,2,4,6,8, etc.) then a positive output polarity is obtained while if the least significant bit of the code is a 1 (e.g., 1,3,5,7,9, etc.) then a negative polarity output is obtained.

The user should note that, independent of which option(s) is fitted both x and y are required.

e.g. The string I00 - will command no 2500EP range,
 positive output
 I01 - will command no 2500EP range,
 negative output
 I90 - will command 100uA 2500EP range,
 positive output

6.15 Previous 2701B IEEE compatability.

This TL-3 option is fully compatible with previous versions of this interface. The above mentioned commands are the only commands utilized by the previous version of this interface. If the user had previously used the "oddities" of the previous version then some modifications may be required and the user should note the following changes:

- 1) A delimiter is required - either Carriage Return
 or EOI with the last
 character
 or Group Execute
 Trigger
- 2) The previous version would take the next 6 characters
after the "V", or 2 characters after the "I", or 1
character after the "R". This version will not
update data which is missing -

e.g. The string V: - will cause the last 5 decades to not be changed, only setting the first 2 to "10".

6.16 Free-format Commands.

6.17 Voltage Control

The required output voltage from the 2701B may be directly controlled by utilizing the "V0" command. This command takes the form V0 followed by the required voltage in free-format numeric form (no range command is required). The following are some examples of the string of characters that the 2701B will accept:

V0+1.234567	-	will cause +1.23456 volts to be output
V01.234567	-	will cause +1.23456 volts to be output
V0-1.5e+1	-	will cause -15 volts to be output
V0-1.5E+1	-	will cause -15 volts to be output
V0+1.6e-3	-	will cause +0.0016 volts to be output

The user should note that entry of the numeric value may be in any commonly used format and is delimited by any character not normally used in numeric fields.

The user should also note that the commands in section 6.4.1 above may be used in conjunction with the V0 command if required.

6.18 Current Control

The required output current from a 2701B+2500EP combination may be directly controlled by utilizing the "I0" command. This command takes the form I0 followed by the required current (in milliamps) in any form. The following are some examples of the string of characters that the 2701B will accept:

I0+0.06	-	will cause an output of +60uA
I010000	-	will cause an output of +10A
I0-1e4	-	will cause an output of -10A
I01E-2	-	will cause an output of +10uA

The user should note that entry of the numeric value may be in any commonly used format and is delimited by any character not normally used in numeric fields.

The user should also note that the commands in section 6.4.1 above may be used in conjunction with the I0 command if required.

6.19 Standby/Operate Mode Control

In contrast to previous versions, this version of the TL-3 interface has the capability of controlling the STANDBY/OPERATE status of the 2701B's output. Every output command received by the 2701B causes operate to be selected and the command string "S" will cause standby to be selected.

The string	R2V:00000	-	will cause 100V OPERATE to be selected
	R2V:000005	-	will cause 100V STANDBY to be selected
	S	-	will cause STANDBY to be selected
	V	-	will cause OPERATE at the previously used output to be selected.

6.20 Reading the Output

This version of the 2701B option TL-3 enables the user to read the present output of the 2701B (or 2701B+2500EP if required). The string sent by the 2701B is of the following format:-

sn.nnnnnnEsn xxo

where s is the applicable polarity

n is the numeric value

xx is "V" if IRP is commanding no range, "mA" if an IRP range is commanded. o is a space if in OPERATE, "*" if in STANDBY.

While the numeric data is correct with the 2701B in LOCAL or REMOTE, the exponent is only available in REMOTE (set to 0 in LOCAL).

6.21 ADVANCED COMMANDS AND EXAMPLES

6.22 Output Delimiter Control

The output string shown in 6.4.3 above may have one of various delimiters. The delimiter used is commanded by the "E" command-

The string	E0	-	Causes (Carriage Return), (Line Feed)
	E1	-	Causes (Carriage Return), (Line Feed with EOI)
	E2	-	Causes (Carriage Return)
	E3	-	Causes (Carriage Return with EOI)
	E4	-	Causes EOI with the last character

to be output following (or with) the last character.

6.23 Serial Poll Control/Response

If required by the user, the 2701B may assert SRQ upon attempted decode of an invalid command. This is controlled by the use of the "Q" command.

The string Q0 - Causes no SRQ's to be generated.
 Q1 - Causes an SRQ to be generated upon attempted decode of an invalid command.

The response to a serial poll will be a null byte if the 2701B was not requesting service and will have the least significant bit set if it was. The most significant bit is set if the 2701B is in REMOTE.

6.24 USEFUL HINTS

6.25 Polarity Changing

The PRM option in the 2701B operates by reversing the output terminals of the 2701B. Thus, it is not recommended to change polarity while the output is at a voltage of over 100 volts. If a polarity change with over 100V is required the user is recommended to reduce the output, delay for 5mS per V and then command the polarity change.

Examples (using HP85 controller)

```
Output was at +1000V, requires to change to -10V
OUTPUT710;"V0100"
WAIT 5000
OUTPUT710;"V0-10"
```

```
Previous output voltage held in variable A
Output required held in variable B
10 IF ABS(A)<100 THEN 100
20 C=A/10 (Ensures output less than 100V)
30 OUTPUT710;"V0",C (Note 2701B will ignore spaces)
40 WAIT 5*ABS(C)
100 OUTPUT710;"V0",B
```


SECTION VII - PARTS LISTS

7.1 GENERAL

The parts lists of this section are reproductions of factory parts lists. The designators are those of the diagrams in Section V. When ordering replacement parts, provide the reference designator, manufacturer's part number, manufacturer's name and Valhalla part number if contained in the parts list. The manufacturer is identified in the parts list by a federal stock number. A list of manufacturer's federal stock codes is provided as an appendix to this manual for identification of manufacturer's.

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NOTES:



PARTS LIST

CHASSIS ASSEMBLY

MODEL

2701B

DWG NO

2701-400

REV

H

#	REF DES	VALHALLA PART NO	DESCRIPTION	CODE IDENT	MFG PART NO	QTY		REMARKS
						I	N	
1	J 1,2	5-10001	BINDING POST, RED	SUPERIOR	BP21RC	2		
2	J 3,4	5-10003	BINDING POST, BLK	SUPERIOR	BP21BC	2		
3			MAIN P.C.B.	53504	2701-601	1		
4			DISPLAY P.C.B.	53504	2701-600	1		
5								
6	S 10	5-03030	PUSH BUTTON SWITCH	SCHADOW	ZFFA200 BLK YEL 2URSE	1		
7	S 11	5-03034	115/230 SLIDE SWITCH	82389	47227LFR	1		
8		5-10430	SWANOFF, 1/4" MF HEX, 5/16 LG	RAF	4531-632-SS-O	2		
9	T 1	4-20025	HIGH VOLTAGE TRANSFORMER	53504	2701-012	1		
10	T 2	4-20026	POWER TRANSFORMER	53504	2701-013	1		
11	F 1	5-04007	FUSE 1/2A		1/2A SLO-BIO	1		230V OPERATION
12	F 1	5-04002	FUSE 1A	75915	1A SLO-BIO	1		115V OPERATION
13		5-10166	LINE FILTER, AC REPT.	COROOM	6EF1	1		
14		5-10067	POWER COED	ELECTROCORD	39806	1		
15		5-10291	R.P. HOLE PLUG	83330	3091	4		FOR REAR POST
16	XF 1	5-10018	FUSE HOLDER, PNL MT	75915	342004A	1		
17		5-10201	NUMBER SKIRT	95146	AW5-310	6		
18		5-10202	COLLET KNOB	95146	AS1-352-W3-302	7		
19		5-10203	POINIER SKIRT	95146	AW5-382	1		
20			SCREW, CAD,PHIL,PAN,HD		6-32 x 9/16	4		
21			SCREW, CAD,PHIL,FLAT,HD	82°	6-32 x 3/8	11		
22			SCREW, BLK PHIL,PAN HD		4-40 x 1/2	4		
					0-32 x 1/2	8		

NOTE



PARTS LIST

CHASSIS ASSEMBLY

MODEL
2701B

DWG NO
2701-400

REV
H

#	REF DES	VALHALLA PART NO	DESCRIPTION	CODE IDENT	MFG PART NO				REMARKS
						I	N	I	
24			SCREW, CAD PHIL FLAT HD	82°	8-32 x 1/2	4			FOR RACK EARS
25			SCREW, CAD PHIL PAN HD		6-32 x 3/8	12			
26		5-10086	WASHERS GND LUG	83330	1412-6	1			
27			WASHERS INTERNAL STAR		#6	16			
28			WASHERS SPLIT-LOC		#6	7			
29			WASHERS SPLIT-LOC		#4	4			
30			WASHERS INTERNAL STAR		#8	16			
31			SCREW, CAD, PAN, HD		6-32 x 1/2	10			
32			NUTS, RADIO HEX		6-32	13			
33			NUTS, RADIO HEX		4-40	4			
34			NUTS, STD HEX		8-32	8			
35		5-10015	RUBBER FEET	83330	701-BIK	4			
36		4-10332	CORNER BLOCK	53504	2724-204	4			
37		4-10476	FRONT PANEL	53504	2701-100	1			
38		4-10291	REAR PANEL	53504	2701-208	1			
39		4-10459	SIDE RAIL	53504	2701-215	2			
40		4-10288	MOUNTING BRACKET	53504	2701-211	2			
41		4-10318	COVERS TOP/BOTTOM	53504	2724-203	2			
42		4-10277	MOUNTING EARS	53504	2714-206	2			
43		4-10333	BEZEL	53504	2724-205	4			
44		4-10305	BUTCH PLATE IERE	53504	4300-213	1			
45		4-10257	BUTCH PLATE IRP	53504	2701-206	1			
			4.5 BTMC TITC	06383	PV20-6R	8			



PARTS LIST

MAIN P.C.B. ASSEMBLY

MODEL

2701B

DWG NO

2701-601

REV

K

#	REF DES	VALHALLA PART NO	DESCRIPTION	CODE IDENT	MFG PART NO	QTY			REMARKS
						I	N	I	
1	C1	2-20009	CAP, MICA 470pf	81349	CR105FD471J03	1			
2	C41	2-60001	CAP, MYLAR .22µf			1			
3	C2	2-80003	CAP, .22µf, POLY, 1500V			1			
4	C4	2-90004	CAP, MYLAR .01µf	14752	625B1A103J	1			
5	C5, 29, 37, 38	2-90005	CAP, POLY .1µf/50V	14752	625B1A104J	2			
6	C8	2-10004	CAP, CERM .02µf/500V			1			
7	C9, 10, 11, 12	2-40009	CAP, ELEC 47µf/450V			4			
8	C13, 14, 36	2-40011	CAP, ELEC 10µf/350V	56389	TVA-1604	3			
9	C15	2-40010	CAP, ELEC 1000µf/25V	56289	TVA-1211	1			
10	C16, 19	2-40008	CAP, ELEC 470µf/25V	56289	TVA-1209	2			
11	C17, 21, 26, 27	2-30001	CAP, TAN 10µf/25V	05397	T360B106M025AS	7			
12	C20	2-40012	CAP, ELEC 15,000/10V	Illinois	159TFA0010B	1			
13	C22-25, 6	2-10000	CAP, CERM.005µf, 100V	56289	5CAD50	5			
14	C39, 40	2-10012	CAP, CERM .02µf/1KV	56289	5GA-S20	2			
15	C3, C7	2-50000	CAP, POLY .22µf	FDVNE	FSA .22µf 10%	2			
16	CR 1-6, 46-49	3-20002	DIODE, RECT	04713	IN4001	14			
17	CR 7, 62, 63	3-20005	DIODE, ZENER 6V	04713	IN5234	3			
18	CR 8, 11, 40, 41, 64	3-20000	DIODE	04713	IN4148	6			
19	CR9, 10	3-20006	DIODE	07263	FD300/IN3595	2			
20	CR42-45, 54, 55, 61	3-20015	DIODE RECT	04713	IN4007	7			

NOTE



PARTS LIST

MAIN P.C.B. ASSEMBLY

MODEL

2701B

DWG NO

2701-601

REV

K

#	REF DES	VALHALLA PART NO	DESCRIPTION	CODE IDENT	MFG PART NO	QTY			REMARKS
						I	N	N	
44	Q 1,2,3,4	3-10016	TRANSISTOR FET	17856	VN66AK	4			
45									
46	Q 7-18	3-10015	TRANSISTOR NPN	04713	MJE3439	12			
47	Q 19	3-10018	TRANSISTOR NPN	18722	T1P50	1			
48	Q 20	3-10017	TRANSISTOR POWER	16758	DTS-801 or 2N6543	1			
49									
50									
51									
52	K 1,2,3,	5-03043	LOW THERMAL EMF RELAY	71707	CR3221-12-810	3			
53									
54	R123, 124, 125	1-30019	RES FXD 100K 1 W 5%	81349	RC32GF273J	3			
55	R 1,3,5,7,9,13, 52,58,155	1-01081	RES FXD 100K 1/4W 5%	81349	RC07GF104J	9			
56	R 2,4,6,8,10, 12,38,48,32,57 150,151	1-01041	RES FXD 1K 1/4W 5%	81349	RC07GF102J	12			
57	R 14	1-01014	RES FXD 39Ω 1/4W 5%	81349	RC07GF390J	1			
58	R 15, 149	1-30011	RES FXD 1K 10W	91637	RS-10-1K	2			
59	R 16, 19, 21		FACTORY SELECT			3			
60	R 17	1-20016	RES FXD 1K .05% 2PPM	00002	JL10-1K	1			
61	R 18	1-50024	RES VAR 100K	73138	68WR-100K	1			
62	R 20	1-10111	RES FXD 1.24M 1%	81349	RN60C1244F	1			
63	R 22 161	1-01070	RES FXD 33K 1/4W 5%	81349	RC07GF333J	2			

NOTES: * R24, R30, R39, R40A, R40B, R45 and R160 are a matched set.



PARTS LIST

MAIN P.C.B. ASSEMBLY

MODEL 2701B

DWG NO 2701-601

REV K

#	REF DES	VALHALLA PART NO	DESCRIPTION	CODE IDENT	MFG PART NO	QTY			REMARKS
						I	N	I N	
64	R 23	1-50033	RES VAR 50Ω	73138	68WR-50	1			
65	R 24, 30	*1-20109	RES FXD 49.75k, .01%	05347	1-20065	2			
66	R 25, 135	1-20035	RES FXD 5K .01%			2			
67	R 26, 157, 158	1-50028	RES VAR 50K	73138	68WR - 50K	3			
68	R 27	1-01089	RES FXD 300K 1/4W 5%	81349	RC07GF304J	1			
69	R 28	1-01007	RES FXD 10Ω 1/4W 5%	81349	RC07GF100J	1			
70	R 29	1-01045	RES FXD 2K 1/4W 5%	81349	RC07GF202J	1			
71	R 44, 47	1-01053	RES FXD 4.7K 1/4W 5%	81349	RC07GF472J	2			
72	R 33, 34, 35, 36	1-01087	RES FXD 240K 1/4W 5%	81349	RC07GF244J	4			
73	R 37	1-01083	RES FXD 150K 1/4W 5%	81349	RC07G154J	1			
74	R 39	*1-20109	RES FXD 200K .01%	05347	1-20066	1			
75	R 40A, 40B	*1-20109	RES FXD 899.75Ω .01%	05347	1-20067	2			
76	R 42, 62	1-01063	RES FXD 15K 1/4W 5%	81349	RC07GF153J	2			
77	R 43, 59, 60, 63 64, 65, 146, 31	1-01061	RES FXD 10K 1/4W 5%	81349	RC07GF103J	8			
78	R 45	*1-20109	RES FXD 19.995K .01%	05347	1-20068	1			
79	R 46	1-50018	RES VAR 10 Ω	73138	68WR - 10	1			
80	R 49	1-01047	RES FXD 2.4K 1/4W 5%	81349	RC07GF242J	1			
81	R 51, 161	1-01119	RES FXD 10M 1/4W 5%	81349	RC07GF106J	2			
82	R 53, 54, 55, 56	1-01031	RES FXD 390Ω 1/4W 5%	81349	RC07GF391J	4			
83	R 61, 156	1-01021	RES FXD 100Ω 1/4W 5%	81349	RC07GF101J	2			
84	R 41	1-50013	RES VAR 1K	73138	68WR - 1K	1			

NOTE



PARTS LIST

MAIN P.C.B. ASSEMBLY

MODEL

2701B

DWG NO

2701-601

REV

K

#	REF DES	VALHALLA PART NO	DESCRIPTION	CODE IDENT	MFG PART NO	QTY			REMARKS
						I	N	N	
86	R 50, 69	1-01049	RES FXD 3K 1/4W 5%	81349	RC07GF302J	2			
87	R 126	1-01033	RES FXD 470Ω 1/4W 5%	81349	RC07GF471J	1			
88	R 127	1-01026	RES FXD 220Ω 1/4W 5%	81349	RC07GF221J	1			
89	R128-131, 142-145	1-01085	RES FXD 200K 1/4W 5%	81349	RC07GF204J	8			
90	R 132	1-30012	RES FXD 500K 1/8 6W	Caddock	MS260-500K	1			
91	R 133, 134	1-01108	RES FXD 3M 1/4W 5%	81349	RC07GF305J	2			
92	R 147	1-30013	RES FXD 100Ω 1/2W	81349	RC20GF101J	1			
93	R 148	1-50012	RES VAR 10K	73138	68WR-10K	1			
94	R 153	1-30014	RES FXD 47K 1W	81349	RC32GF473J	1			
95	R 159	1-10048	RES FXD 1MΩ .01%	81349	RN55C1004F	1			
96	R 160	*1-20109	RES FXD 20608Ω .01%	05347	1-20109	1			
97	Y 1	5-02000	1MHz CRYSTAL		HC6-1MHz	1			
98									
99		4-30053	P.C. BOARD MAIN	53504	2701-701	1			
100		5-10025	HEAT SINK	13103	6003B-2	2			
101			SILICON GREASE			AR			
102		5-10229	WASHER, BERYLLIA		CG3835-1	2			
103	P 1-8		22 AWG WIRE .05LG			8			
104		5-10019	CABLE TIES	51705	WRN 3 1/2	4			
105		5-10221	CABLE TIES	51705	WRN 7	1			
106			SCREW PHIL PAN, CAD		4-40-1/4	3			
107			WASHER SPLIT LOCK		#4	3			
		4-10032	PANFL. STIFFENER	53504	4004-202	1			



PARTS LIST

OPTION L488
 MODEL 2701B
 DWG NO 2701-604
 REV A

#	REF DES	VALHALLA PART NO	DESCRIPTION	CODE IDENT	MFG PART NO	QTY			REMARKS
						I	N	N	
1									
2			IEEE BD ASSY	53504	1000-600	1			
3									
4									
5		5-10019	CABLE TIE 4" x 1/8"		PANDUIT WRN-4	25			
6		5-10261	BLOCK, ADHESIVE		PANDUIT ABR25-A	3			
7		5-50030	2701B	53504		1			
8									
9									
10									
11			WIRES:						
12			BRN, 22AWG		252"	1			
13			RED, 22AWG		144"	1			
14			ORN, 22AWG		144"	1			
15			YEL, 22AWG		108"	1			
16			GRN, 22AWG		108"	1			
17			BLU, 22AWG		108"	1			
18			VIO, 22AWG		72"	1			
19			GRAY, 22AWG		72"	1			
20			WHT, 22AWG		72"	1			
21			BLK, 22AWG		72"	1			
22									
23									



PARTS LIST

IEEE-488 INTERFACE

MODEL

MULTI

DWG NO
1000-600

REV
E

#	REF DES	VALHALLA PART NO	DESCRIPTION	CODE IDENT	MFG PART NO	-1		-2		REMARKS
						I	N	I	N	
1	C1	2-30014	CAP, TANT, 100µ, 10V	05397	T390EL07M010AS	1		1		
2	C2, 3	2-20018	CAP, MICA, 33p	56289	CM05ED330J03	2		2		
3	C4-7	2-10006	CAP, CER, .01µ, 50V	72982	8121-050-651-1.03M	4		4		
4	C8, C9	2-30002	CAP, TANT 33µ, 10V	05397	T390D336M010AS	2				
5	D1	3-20000	DIODE	04713	IN4148	1		1		
6	IC13		VOLTAGE REGULATOR	01295	LM340T-5.0	1				
7	IC1	3-30156	CCTIAL BUFFER	01295	74LS54IN	1		1		
8										
9	IC4	3-30158	GPIO, 1.0 MHZ	04713	MC68488P	1		1		
10	IC5	3-30160	MICROPROCESSOR	04713	MC680ZP	1		1		
11	IC6 OR	3-30154	EPROM, 4Kx8	01295	TMS2532JL	1		1	OR	(USE ONLY 1)
12	IC6		EPROM, 4Kx8	34649	2732	1		1		UNPROGRAMMED
13	IC7	3-30155	3 TO 8 DECODER	01295	74LS138N	1		1		
14	IC8-11	3-30159	PIA, 1.0MHZ	04713	MC6821P	4		4		
15	IC12	3-30184	HEX BUFFER	04713	74LS04N	1		1		
16										
17	SK5	5-10243	CONNECTOR, GP1B	00779	552791-1	1		1		
18		5-10244	CONN. HARDWARE	00779	552633-3	1		1		
19										
20	TR1-4	3-10013	TRANSISTOR, NPN	04713	2N4401	4		4		
21										
22	RI-11	1-01061	RESISTOR, 1/4W, 5%, 10K	81349	RC076GF103J	11		11		

NOTES:



PARTS LIST

IEEE-488 INTERFACE

MODEL

MULTI

DWG NO
1000-600

REV
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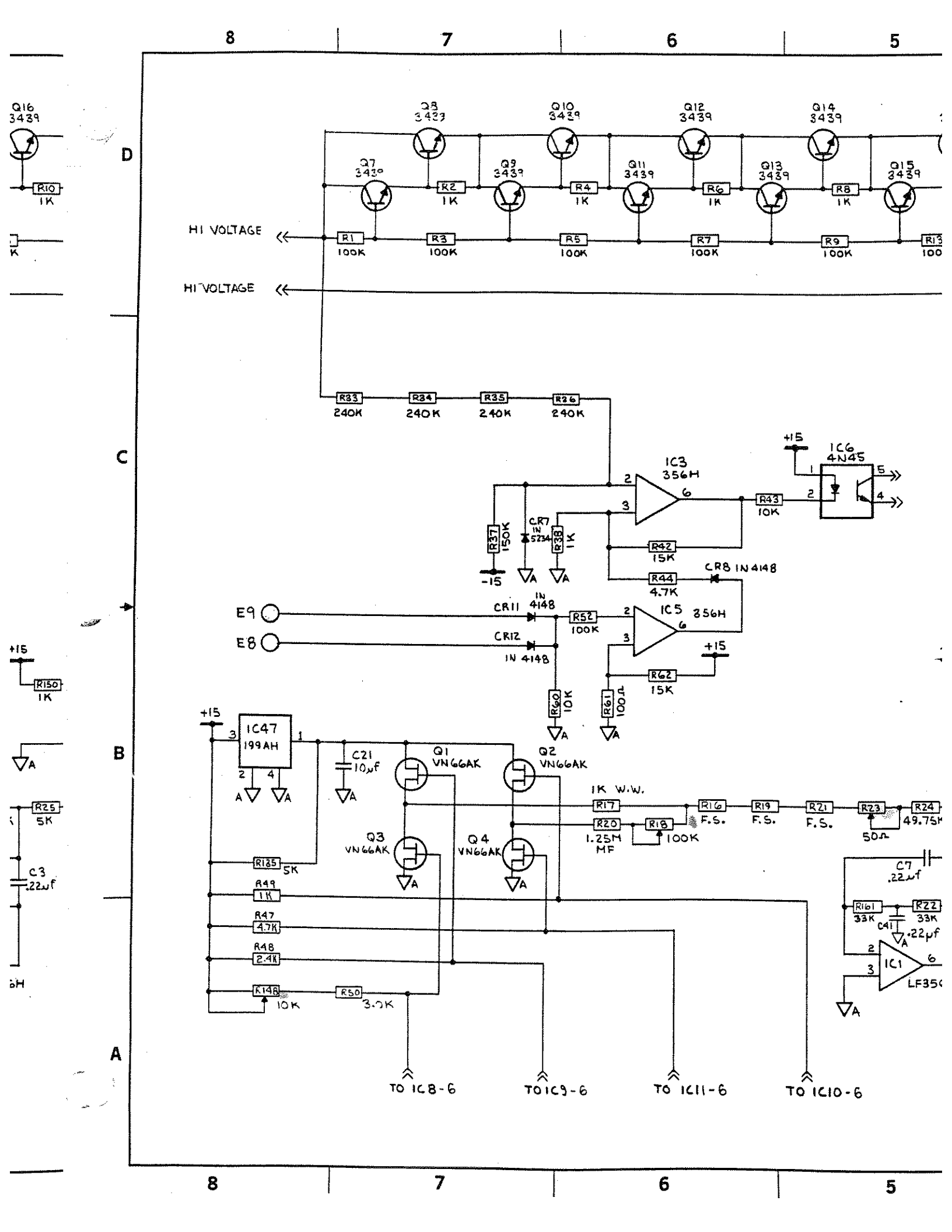
#	REF DES	VALHALLA PART NO	DESCRIPTION	CODE IDENT	MFG PART NO	QUANTITY			REMARKS
						I	N	I N	
23	SI	5-03037	ROCKER SW., 5 POS.	95146	DBS-5	1	1		
24		5-10170	SOCKET - 90°	52072	CA-14SE-10RAC3-01	1	1		FOR ROCKER SW.
25									
26	Y1	5-02004	4MHZ MICROPROCESSOR	M-TRON	HC-18/U	1	1		
27			CRYSTAL						
28		4-30084	PC BOARD	53504	1000-700B	1	1		
29		4-10296	PCB MFG. BRACKET	53504	2300-216	2	2		
30		5-10156	NUT-TINNERMAN	78553	C8094-632-24	2	2		
31			SCREW, PHIL. PAN. CAP.		6-32x3/8	4	4		
32			WASHER, SPLIT LOCK		6	4	4		
33			NUT, HEX		6-32	4	4		
34									
35									
36		5-10246	IC SOCKET, 40 PIN	BURNDY	D1LB40P-11	6	6		FOR IC4,5,8-11
37		5-10042	IC SOCKET, 24 PIN	BURNDY	D1LB24P-11	1	1		FOR IC6
38									
39									
40			SCREW, PHIL PAN LAD		2-56x3/8	2	2		
41			WASHER SPLIT LOCK		#2	2	2		
42			NUT, NEX RADIO		2-56	2	2		
43	IC2 & 3	3-30157	OCTAL BUS TRANSCIVER	04713	MC3447P3	2	2		
				53504	1000-601A	1	1		

SECTION VIII - DIAGRAMS

8.1 GENERAL

The illustrations of this section are the schematic, logic and assembly diagrams of the Model 2300/2301. To the extent practical, an assembly diagram is adjacent to the appropriate schematic or logic diagram. All diagrams are reductions of factory engineering drawings. Title blocks, and other information not required for the maintenance of the unit, have been removed.

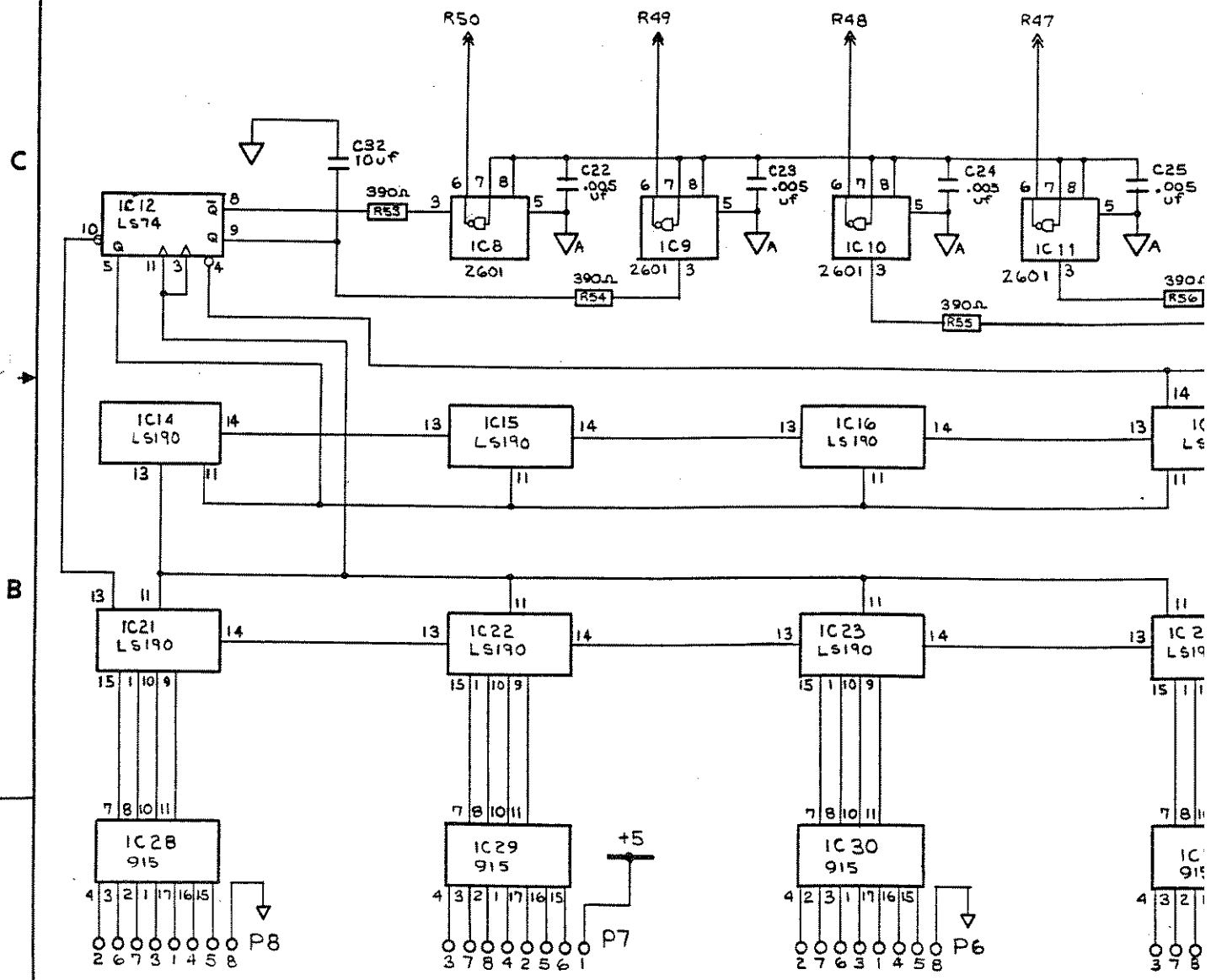
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IC NO.	OV	+5V
IC7	4	
IC8		2
IC9		2
IC10		2
IC11		2
IC12	2,7,12	1,13,14
IC13	2,7	1,13,14,15
IC14	1,10,9	5,15,16
IC15	15,10,9,4,8	5,11,16
IC16	1,10,4,8	5,9,13,16
IC17	1,10,4,8	5,9,15,16
IC18	4,8	5,11,16
IC19	4,8	5,11,16
IC20	4,8	5,11,16
IC21	4,8	5,16
IC22	4,8	5,16

IC NO.	OV	+5V
IC23	4,8	5,16
IC24	4,8	5,16
IC25	4,8	5,16
IC26	4,8	5,16
IC27	4,8	5,16
IC28	6,9,12	14,18
IC29	6,9,12	14,18
IC30	6,9,12	14,18
IC31	6,9,12	14,18
IC32	6,9,12	14,18
IC33	6,9,12	14,18
IC34	6,9,12	14,18
IC42	7	14
IC43	7,9,11	14
IC44	7	14

NOTES:
 1. S8 & S9 ARE LOCATED ON THE DISPLAY P.C.B. (2701-600).

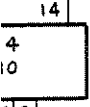


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NOTES:

- 1. FOR UNIT ASSY. SEE 2701-400.
- 2. FOR SCHEMATIC SEE 2701-070.

D

- FOR 240 V
(R132, 50

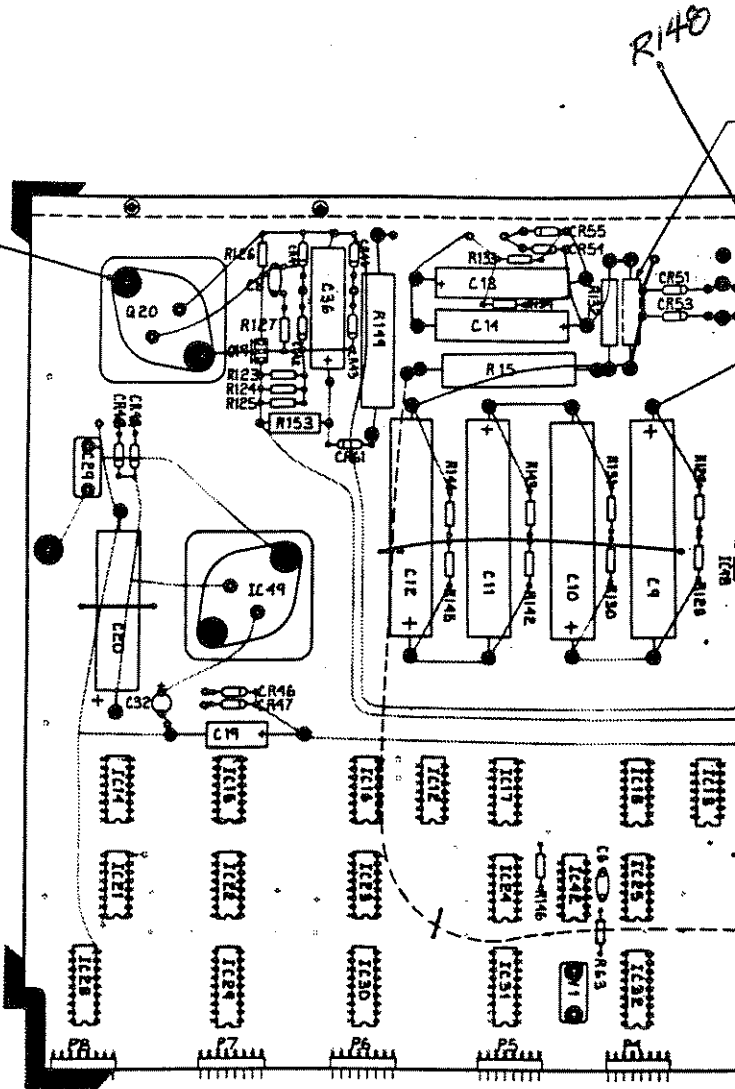


- SCREW, 4 PL 109
- HEATSINK, 2 PL 100
- P.C.B. 99
- WASHER, 4 PL 110
- NUT, 4 PL 111

C

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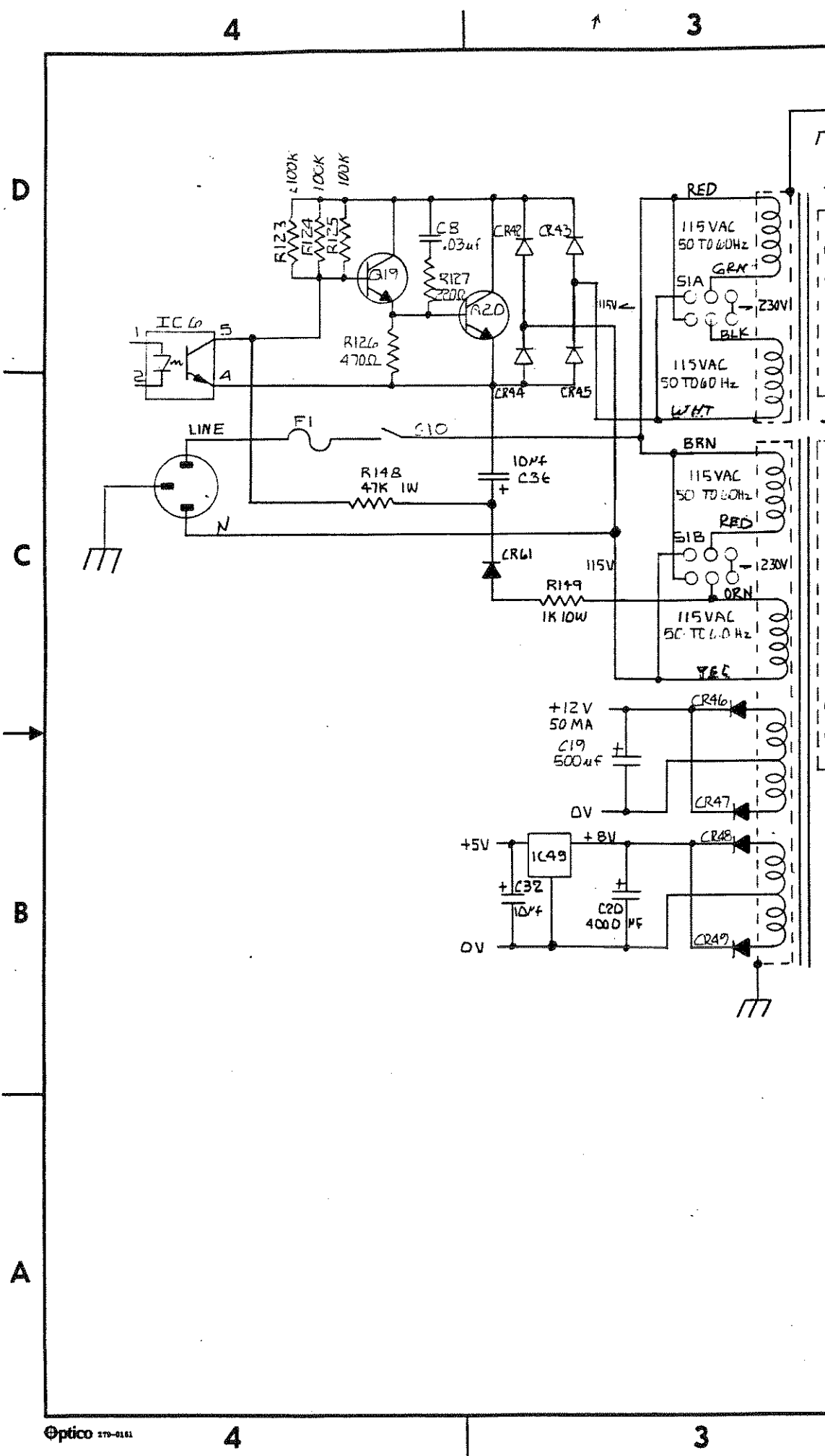
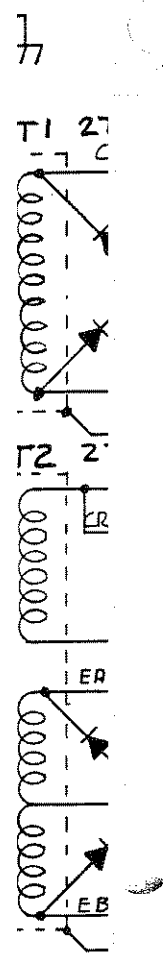
SHOWN IS P.C.B. ASSY. 2701-601
CLAD SHOWN IS FAR SIDE

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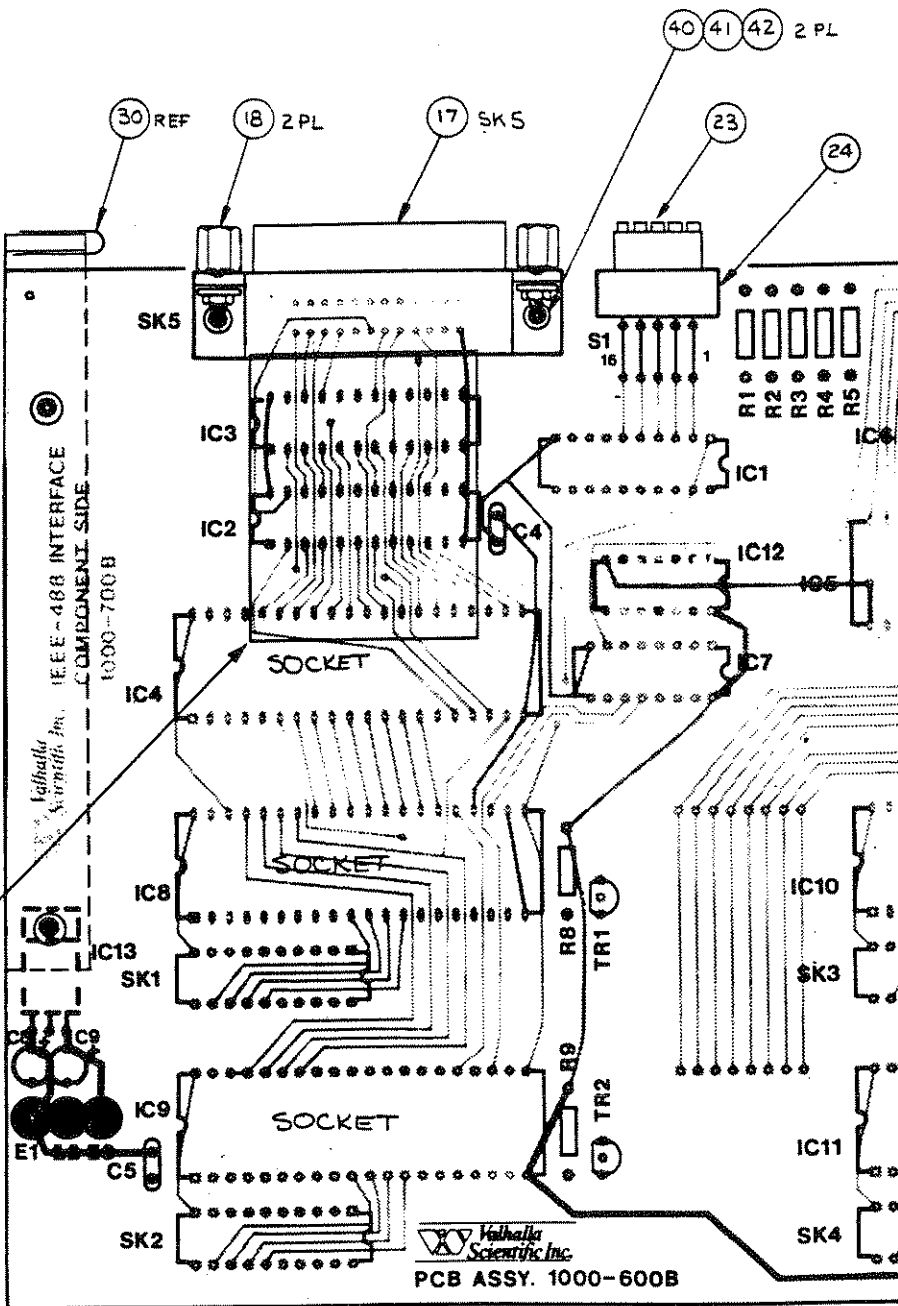
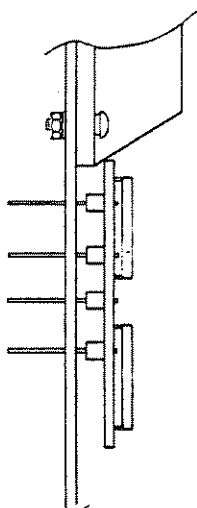
30 REF

18 2 PL

17 SK 5

23

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Valhalla Scientific Inc.
IEEE-488 INTERFACE
COMPONENT SIDE
1000-700B

INSTALL EXPANSION BL. ASSY 1000-601 IN PLACE OF IC 13. MAKE SURE PIN 1'S ON IEEE BD. AND PIN 1'S ON EXPANSION BL. LINE UP. (ONLY IF IC 2 & 3 ARE NOT MOUNTED ON IEEE BOARD)

Valhalla Scientific Inc.
PCB ASSY. 1000-600B

SHOWN IS P.C.B. ASSY 1000-600-1

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